

Hong-Ming Chiu

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EDUCATION

University of Illinois at Urbana Champaign (UIUC) (In Progress) <i>Doctor of Philosophy – Dept. of Electrical and Computer Engineering (ECE)</i> – Advisor: Richard Yi Zhang.	Champaign, IL Aug. 2021 – Present
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University of Illinois at Urbana Champaign (UIUC) <i>Exchange Student – Dept. of Electrical and Computer Engineering (ECE)</i>	Champaign, IL Jan. 2020 – May 2020
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National Yang Ming Chiao Tung University (NYCU) <i>Bachelor of Science in Electronics Engineering</i>	Hsinchu, Taiwan Sep. 2016 – Jun. 2020
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PUBLICATIONS

(Paper and source code available on <https://hong-ming.github.io/#mypublications>)

- [1] Jialun Zhang, **Hong-Ming Chiu** and Richard Y. Zhang. “Fast and Minimax Optimal Estimation of Low-Rank Matrices via Non-Convex Gradient Descent.” arXiv preprint.
- [2] **Hong-Ming Chiu** and Richard Y. Zhang. “Tight Certification of Adversarially Trained Neural Networks via Nonconvex Low-Rank Semidefinite Relaxations.” International Conference on Machine Learning (ICML 2023).
- [3] Jialun Zhang, **Hong-Ming Chiu** and Richard Y. Zhang. “Accelerating SGD for Highly Ill-Conditioned Huge-Scale Online Matrix Completion.” Advances in Neural Information Processing Systems (NeurIPS 2022).
- [4] **Hong-Ming Chiu**, Carrson C. Fung and Antonio Ortega, “Graph Learning and Augmentation Based Interpolation of Signal Strength for Location-Aware Communications,” 2020 European Signal Processing Conference (EUSIPCO 2020).
- [5] **Hong-Ming Chiu**, Kuan-Chih Lin and Tian Sheuan Chang, “Run Time Adaptive Network Slimming for Mobile Environments,” 2019 IEEE International Symposium on Circuits and Systems (ISCAS 2019).

RESEARCH & WORK EXPERIENCE

NYCU Artificial Intelligence and Multimedia Lab <i>Research Assistant (full-time) (Supervisor: Prof. Wen-Huang Cheng)</i>	Hsinchu, Taiwan Jul. 2020 – Feb. 2021
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USC Signal Transformation, Analysis and Compression Group <i>Summer Research (Supervisor: Prof. Antonio Ortega & Prof. Carrson C. Fung)</i>	Los Angeles, CA Jul. 2019 – Aug. 2019
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PROFESSIONAL ACTIVITIES

AISTATS 2024 Paper Reviewer	May. 2024, Valencia, Spain
ICLR 2024 Paper Reviewer	May. 2024, Vienna, Austria
NeurIPS 2023 Paper Reviewer	Nov. 2023, New Orleans, LA
ICML 2023 Paper Reviewer	Jul. 2023, Honolulu, HI
AISTATS 2023 Paper Reviewer	Apr. 2023, Valencia, Spain
NeurIPS 2022 Paper Reviewer	Nov. 2022, New Orleans, LA
ACM Multimedia 2020 Paper Reviewer	Oct. 2020, Seattle, WA

SKILLS & INTERESTS

Languages	Fluent in Mandarin; Conversational Proficiency in English.
Technical Skills	MATLAB, C++, Python, Verilog, SystemVerilog.
Research Interests	Machine Learning, Optimization.