## JEDEC PUBLICATION

# JC-42.6 Manufacturer Identification (ID) Code for Low Power Memories

## **JEP166C**

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JEDEC SOLID STATE TECHNOLOGY ASSOCIATION



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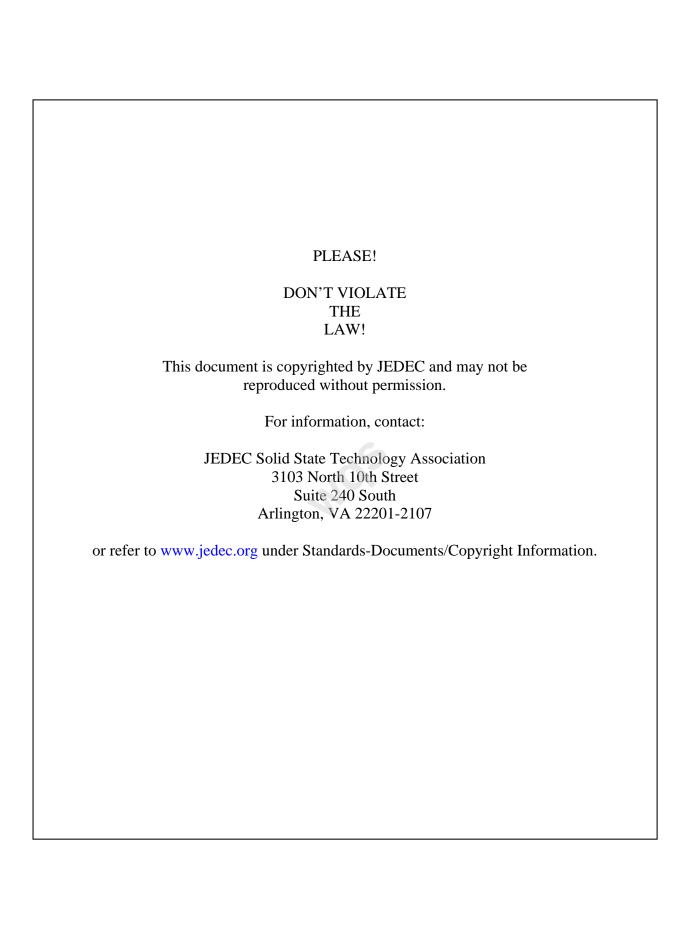
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## JC-42.6 Manufacturer Identification (ID) Code for Low Power Memories

(From JEDEC Board Ballot JCB-12-64, JCB-14-48, and JCB-15-47, formulated under the cognizance of the JC-42.6 Subcommittee on Low Power Memory.)

## 1 Scope

This document defines the JC-42.6 Manufacturer ID. This document covers Manufacturer ID Codes for the following technologies: LPDDR (JESD209), LPDDR2 (JESD209-2), LPDDR3 (JESD209-3), LPDDR4 (JESD209-4), Wide-IO (JESD229), and Wide-IO2 (JESD229-2). The purpose of this document is to define the Manufacturer ID for these devices.

## 2 LPDDR (JESD209) Manufacturer ID

	DQ3	DQ2	DQ1	DQ0					
	LPDDR Manufacturer ID								
				0000B : Res					
				0001B : Sar	_				
				0010B : In:					
				0011B : El	pida				
			ead-Only OP<3:0>	0100B : Et	ron				
				0101B : Nai	nya				
				0110B : SK	hynix				
LPDDR Manuf	acturar ID	Read-Only		0111B : Mos	sel				
LPDDK Manui	acturer in	Reau-Only	UP < 3.0>	1000B : Win	nbond				
					1001B : ESM	TP			
				1010B : NV	N				
				1011B : ISS	SI				
				1100B : JS	C				
				1101B : AP	Memory				
				1110B : al	l others				
_				1111B : Mi	cron				

## 3 LPDDR2 and LPDDR3 (JESD209-2 and JESD209-3) Manufacturer ID

OP7	OP6	OP5	OP4	OP3	OP2	OP1	OP0
LPDDR2/LPDDR3 Manufacturer ID							

		0000 0000B : Reserved
		0000 0001B : Samsung
		0000 0010B : Qimonda
		0000 0011B : Elpida
		0000 0100B : Etron
		0000 0101B : Nanya
		0000 0110B : SK hynix
		0000 0111B : Mosel
		0000 1000B : Winbond
		0000 1001B : ESMT
		0000 1010B : Zentel
		0000 1011B : Spansion
		0000 1100B : SST
		0000 1101B : ZMOS_
		0000 1110B : Intel
LPDDR2/LPDDR3 Read-Only	OP<7:0>	0000 1111B : Reserved
Manufacturer ID	0. 1.10	0001 0000B : Reserved
		0001 0001B : Reserved
		0001 0010B : Being Advanced Memory Corp
		0001 0011B : Reserved
		0001 1010B : Xi'an UniIC Semiconductors
		Co., Ltd
		0001 1011B : ISSI
		0001 1100B : JSC
		1010 1010B : Tezzaron   1100 0010B : Macronix
		1111 1000B : Macronix
		1111 1000B : FIGELIX 1111 1100B : eveRAM
		1111 1101B : AP Memory 1111 1110B : Numonyx
		<u>-</u>
		1111 1111B : Micron

## 4 WIDE I/O (JESD229) Manufacturer ID

DQ3	DQ2	DQ1	DQ0			
Wide I/O Manufacturer ID						



## 5 LPDDR4 (JESD209-4) Manufacturer ID

OP7	OP6	OP5	OP4	OP3	OP2	OP1	OP0
LPDDR4 Manufacturer ID							

			0000 0000B : Reserved
			0000 0001B : Samsung
			0000 0010B : Reserved*
			0000 0011B : Reserved*
			0000 0100B : Reserved*
			0000 0101B : Nanya
			0000 0110B : SK hynix
			0000 0111B : Reserved*
			0000 1000B : Winbond
			0000 1001B : ESMT
			0000 1010B : Reserved
			0000 1011B : Reserved*
			0000 1100B : Reserved*
LPDDR4			0000 1101B : Reserved*
	Darad Oak	OD 7:0	0000 1110B : Reserved*
Manufacturer	Read-Only	OP<7:0>	0000 1111B : Reserved*
ID			0001 0010B : Reserved*
			0001 0011B : Reserved
			0001 1010B : Xi'an UniIC Semiconductors Co.,
			Ltd 0001 1011B : Reserved*
			0001 1011B : Reserved*
			1010 1010B : USC 1010 1010B : Reserved*
			1100 0010B : Reserved*
			1111 1000B : Reserved*
			1111 1001B : Ultra Memory 1111 1100B : Reserved*
			1111 1100B : Reserved* 1111 1101B : AP Memory
			1111 1101B : AP Memory 1111 1110B : Reserved*
			1111 1110B : Reserved
			All others : Reserved

<sup>\*</sup> Reserved codes highlighted with an asterisk (\*) have vendor name assignments in other standards in this document. Use caution when assigning those codes in the future.

## 6 WIDE I/O2 (JESD229-2) Manufacturer ID

OP7	OP6	OP5	OP4	OP3	0	P2	OP1	OP0
Wide I/O2 Manufacturer ID								
Wide I/O 2 Manufacturer ID	Read- Only	OP	<7:0>	0000 0000 0000 0001 0000 0010 0000 0100 0000 0101 0000 0101 0000 1001 0000 1001 0000 1001 0000 1011 0000 1101 0000 1101 0000 1111 0001 1001 1100 0010 1111 1110 1111 1111 All other	B : : : : : : : : : : : : : : : : : : :	Samsi Rese Rese Rese Nany SK h Rese Winbo ESMT Rese Rese Rese Rese Rese Rese Rese Res	ing rved* rved* rved* a ynix rved* ond rved rved* rved* rved* rved* rved* rved* rved* rved* rved*	

<sup>\*</sup> Reserved codes highlighted with an asterisk (\*) have vendor name assignments in other standards in this document. Use caution when assigning those codes in the future.



### Annex A (informative) Obtaining a JC42.6 Manufacturer Identification Code (ID)

JC-42.6 Manufacturer Identification Codes for Low-Power Memories are assigned, maintained, and updated by the JC-42.6 Subcommittee. The JC-42.6 chairperson is responsible for assigning the ID code. The JEDEC office will update JEP166 editorially and publish it in a timely manner.

A new Manufacturer Identification Code for Low-Power Memories can be obtained at the following web page <a href="http://www.jedec.org/id-codes-low-power-memories">http://www.jedec.org/id-codes-low-power-memories</a>.

## Annex B (informative) Differences between JEP166B and JEP166A

This annex briefly describes most of the additions/changes made to entries that appear in this publication, JEP166C, compared to its predecessor, JEP166B (February 2016).

Clause	Description of change
2, 3 and 5	"EMLSI" changed to "JSC" (LPDDR4)
3, 5	Added: "0001 1010B: Xi'an UniIC Semiconductors Co., Ltd" (LPDDR4 and LPDDR2/3)
5	Added: "1111 1001B: Ultra Memory" (LPDDR4)

### B.2 Differences between JEP166B and JEP166A (December 2014)

Clause	Description of change
Annex A	modified with new information
2, 3, 5	new ID codes assigned

### B.1 Differences between JEP166A and JEP166 (March 2014)

Clause	Description of change
1	In Scope: added reference to JESD209-4, and JESD229-2
5	Added
6	Added



## Standard Improvement Form

JEDEC **JEP166C** 

The purpose of this form is to provide the Technical Committees of JEDEC with input from the industry regarding usage of the subject standard. Individuals or companies are invited to submit comments to JEDEC. All comments will be collected and dispersed to the appropriate committee(s).

If you can provide input, please complete this form and return to:

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I recommend changes to the following:      Requirement, clause number      Test method number Clause	e number
The referenced clause number has proven to be:  Unclear Too Rigid In Error  Other	
2. Recommendations for correction:	
	5
Other suggestions for document improvement:	
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