

# Fabrication Overview

**Board name: ESP8266-Based Industrial DIDO & Analog I/O Control Gateway**

**Revision: Rev A**

**Board size: 156.2mm X 87mm**

**Layer count: 2**

**Finish: HASL**

**Solder mask: Green**

**Min trace/space: 5 mil / 10 mil**

**Min via: 10 drill / 20 pad**

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
▽	104	0.25mm	Plated	
◎	33	0.30mm	Plated	
+	3	0.70mm	Plated	
□	29	0.89mm	Plated	
☒	38	1.00mm	Plated	
▽	14	1.10mm	Plated	
☒	1	1.20mm	Plated	
○	27	1.40mm	Plated	
☒	12	1.50mm	Plated	
◇	3	3.00mm	Non-Plated	
□	1	3.00mm	Plated	
265 Total				

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
Top Overlay				Legend	GTO
Surface Material	Board Layer Stack Top Solder	0.03mm	solder resist	Solder Mask	GTS
Copper	<b>Top Layer</b>	<b>0.04mm</b>		Signal	GTL
		1.49mm	FR-4	Dielectric	
Copper	<b>Bottom Layer</b>	<b>0.04mm</b>		Signal	GBL
Surface Material	Board Layer Stack Bottom Solder	0.03mm	solder resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.61mm					