Schematic Errata

Lab t2

Schematic Errors:
- The board header "FTDI-TTL232-3V3" does not have a connection to MCLR at the FTDI pin 6 named "RTS#". To remedy this error, solder one wire between pin 6 of the FTDI-TTL232-3V3 and pin 1 named "MCLR#" of the "SV1" header.

Lab t3

Schematic Errors:
- The UART1 for the MCU is using pin RF0 on the schematic rather than the "FTDI-TTL232-3V3". The user should solder pin #RXD to RF0 located on H1 to accomplish serial communication with the MCU's UART1.

Lab t4

- No errors reported

Lab t5

- No errors reported

Lab t6
- The SDA and SCL lines for the PIC33 are connected to traces that don't connect to anything. We need to be able to use these pins to communicate with the DS1631. To resolve this issue, the solder mask for these traces must be scraped off and a blue wire must be run from each trace to the corresponding header pins on H2.

Lab t7
- RD0 goes to a header but the header doesn't route to the chip select so we had to blue wire the header to the chip select pin on the DAC

Lab t8
- No errors reported

Lab t9
- No errors reported