<u> Lab t2</u>

Schematic Errors:

- The board header "FTDI-TTL232-3V3" does not have a connection to MCLR at the FTDI pin 6 named "RTS#". To remedy this error, solder one wire between pin 6 of the FTDI-TTL232-3V3 and pin 1 named "MCLR#" of the "SV1" header.

<u>Lab t3</u>

Schematic Errors:

- The UART1 for the MCU is using pin RFO on the schematic rather than the "FTDI-TTL232-3V3". The user should solder pin #RXD to RFO located on H1 to accomplish serial communication with the MCU's UART1.