

Team Backlog				
Task Name	Assignee	Task Status	Expected Time to Finish (Hrs)	Actual Time to Finish (Hrs)
Design				
High Level Design				
Modules' Description	All Team	Done	4	4
Project Circuit Schematic	Hossam	Done	1	2
Drivers' Documentation	All Team	Done	2	1
Low Level Design				
Functions' Flowchart	All Team	Done	3	4
Summary				
Document File	Abdelrhman	Done	3	3
Implementation				
MCAL Module				
DIO Driver	Hossam	Done	1	1
EXI Driver	Abdelrhman	Done	3	2
TMR Driver	Mahmoud	Done	1	2
I2C Driver	Mahmoud	Done	4	9
SPI Driver	Hossam	Done	4	6
UART Driver	Abdelrhman	Done	4	5
HAL Module				
MBTN Driver (Multifunctional button)	Hossam	Done	2	1
BUZ Driver	Abdelrhman	Done	1	1
LCD Driver	Hossam	Done	1	1
KPD Driver	Abdelrhman	Done	3	2
EEPROM Driver	Mahmoud	Done	3	7
APP Module				
ATM ECU APP	Hossam	Done	10	7
Card ECU APP	Abdelrhman	Done	4	6
Testing				
Documentation				
Test Protocol Excel Sheet	All Team	Done	4	6
Unit Testing				
Test MCAL	All Team	Done	3	4
Test HAL	All Team	Done	3	6
System Testing				
Test APP	All Team	Done	2	3
Final Requirements Review	All Team	Done	1	1
Video				
Video Recording	All Team	Done	1	1
Video Editing	Hossam	Done	1	1

