1. Description

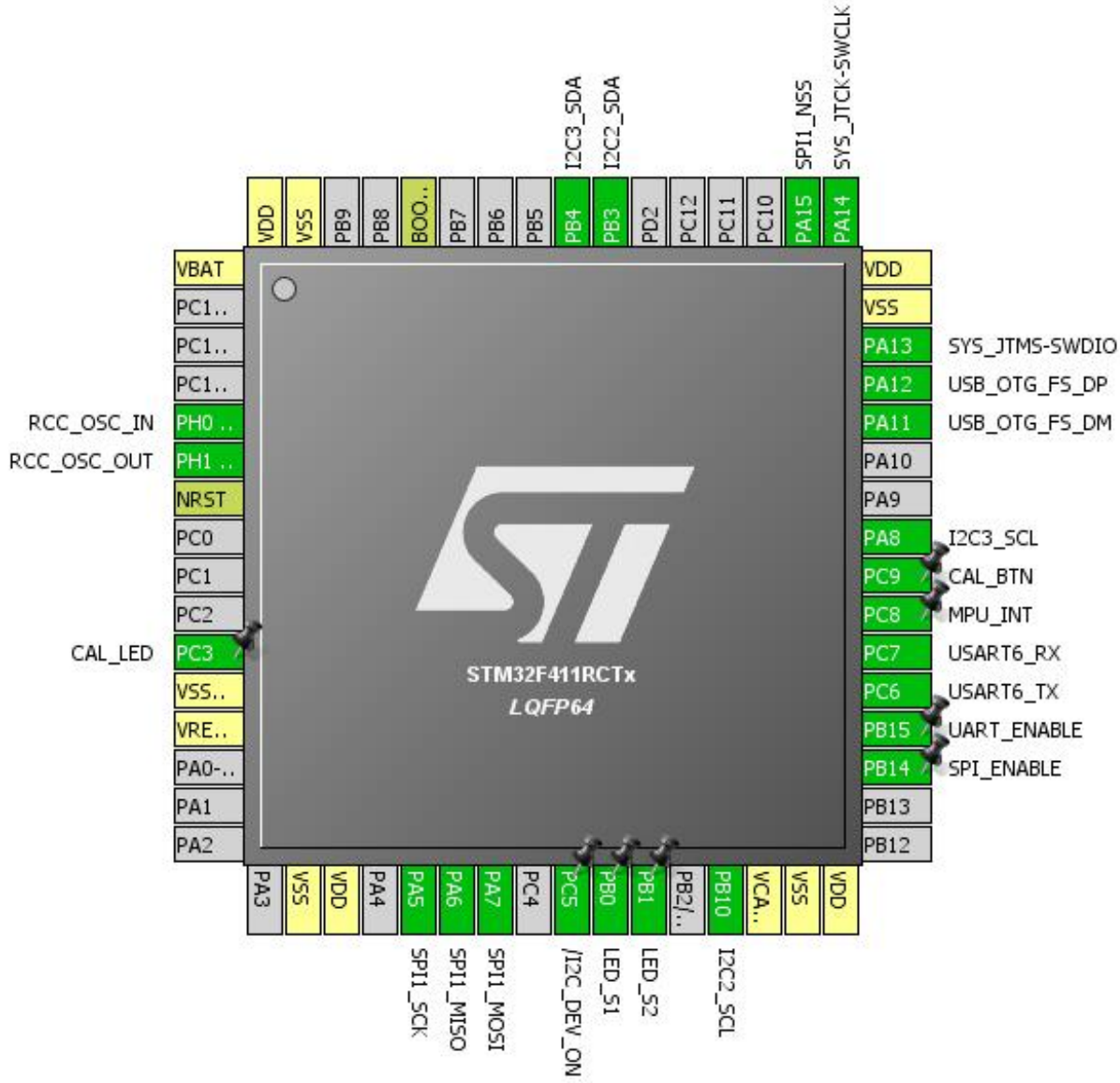
1.1. Project

Project Name	nav10_mxp_3_3
Generated with:	STM32CubeMX 4.4.0
Date	10/19/2014

1.2. MCU

MCU Serie	STM32F4
MCU Line	STM32F411
MCU name	STM32F411RCTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
I2C2	I2C: I2C	I2C2_SCL	PB10
		I2C2_SDA	PB3
I2C3	I2C: I2C	I2C3_SCL	PA8
		I2C3_SDA	PB4
RCC	High Speed Clock (HSE): Crystal/Ceramic Resonator	RCC_OSC_IN	PH0 - OSC_IN
		RCC_OSC_OUT	PH1 - OSC_OUT
SPI1	Mode: Full-Duplex Master	SPI1_MISO	PA6
		SPI1_MOSI	PA7
		SPI1_SCK	PA5
	Hardware NSS Signal	SPI1_NSS	PA15
SYS	Debug: Serial Wire Debug (SWD)	SYS_JTCK-SWCLK	PA14
		SYS_JTMS-SWDIO	PA13
USART6	Mode: Asynchronous	USART6_RX	PC7
		USART6_TX	PC6
USB_OTG_FS	Mode: Device_Only	USB_OTG_FS_DM	PA11
		USB_OTG_FS_DP	PA12

MiddleWare	Mode
USB_DEVICE	Class For FS IP: Communication Device Class (Virtual Port Com)

4. Pins Configuration

Pin	Pos	Function(s)	Label
PH0 - OSC_IN	5	RCC_OSC_IN	
PH1 - OSC_OUT	6	RCC_OSC_OUT	
PC3 *	11	GPIO_Output	CAL_LED
PA5	21	SPI1_SCK	
PA6	22	SPI1_MISO	
PA7	23	SPI1_MOSI	
PC5 *	25	GPIO_Output	/I2C_DEV_ON
PB0 *	26	GPIO_Output	LED_S1
PB1 *	27	GPIO_Output	LED_S2
PB10	29	I2C2_SCL	
PB14 *	35	GPIO_Input	SPI_ENABLE
PB15 *	36	GPIO_Input	UART_ENABLE
PC6	37	USART6_TX	
PC7	38	USART6_RX	
PC8 *	39	GPIO_Input	MPU_INT
PC9 *	40	GPIO_Input	CAL_BTN
PA8	41	I2C3_SCL	
PA11	44	USB_OTG_FS_DM	
PA12	45	USB_OTG_FS_DP	
PA13	46	SYS_JTMS-SWDIO	
PA14	49	SYS_JTCK-SWCLK	
PA15	50	SPI1_NSS	
PB3	55	I2C2_SDA	
PB4	56	I2C3_SDA	

* The pin is affected with an I/O function

5. Software Project

5.1. Project Settings

Name	Value
Project Name	nav10_mxp_3_3
Project Folder	C:\Users\Scott\Documents\svn_Kauailabs\Schematics\nav10\nav10_mxp_3_3\Fir
Toolchain / IDE	TrueSTUDIO 4.3.1
Firmware Package Name and Version	STM32Cube FW_F4 V1.3.0

5.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes

5.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed