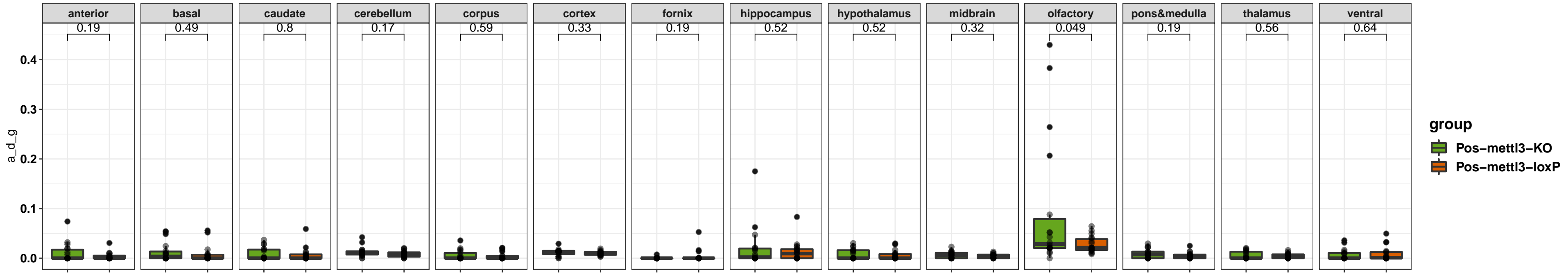
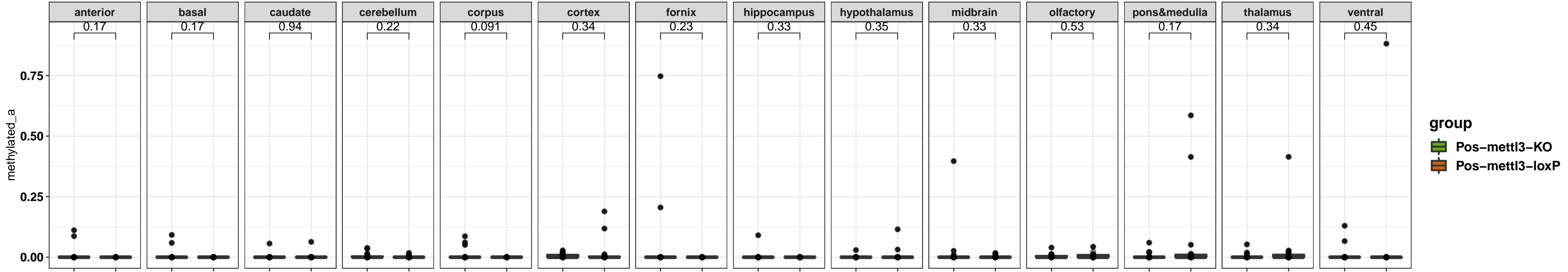


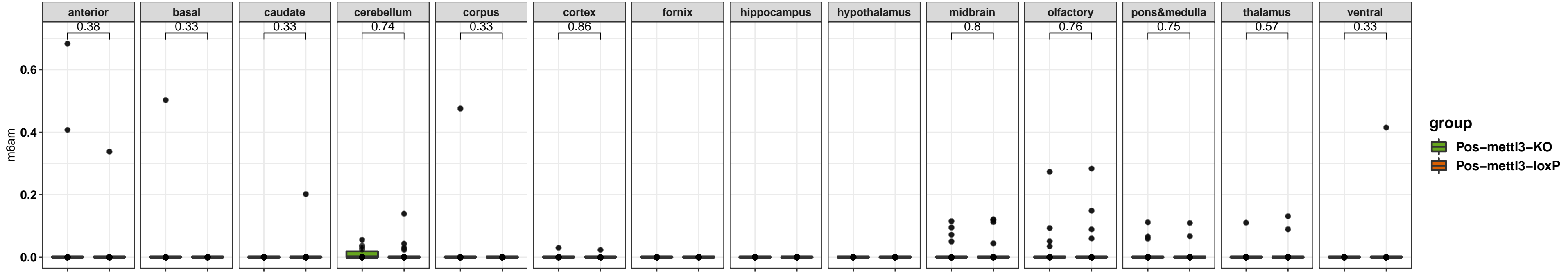
A_D_G



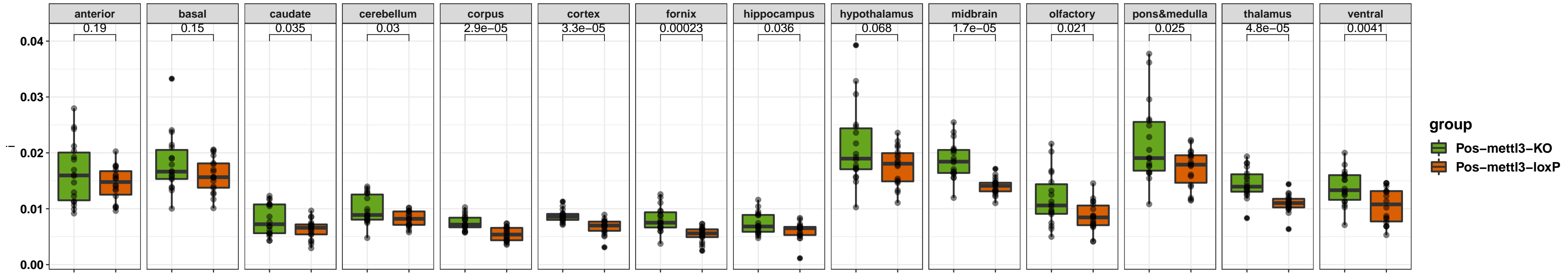
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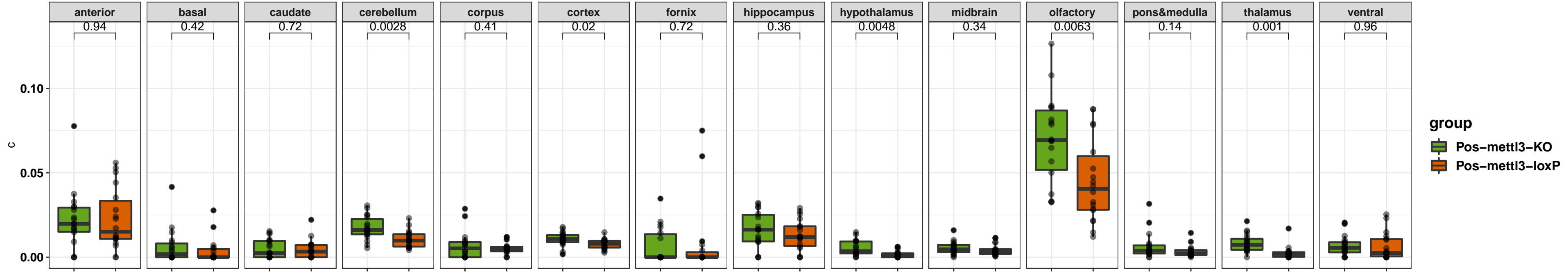
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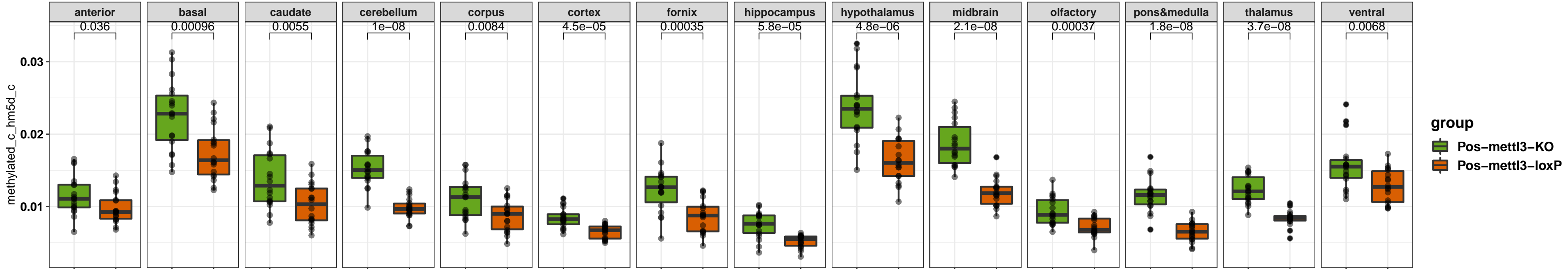
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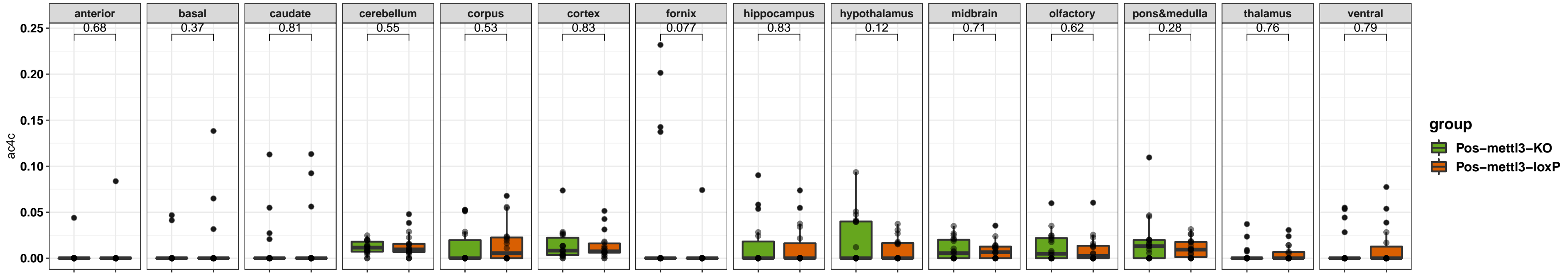
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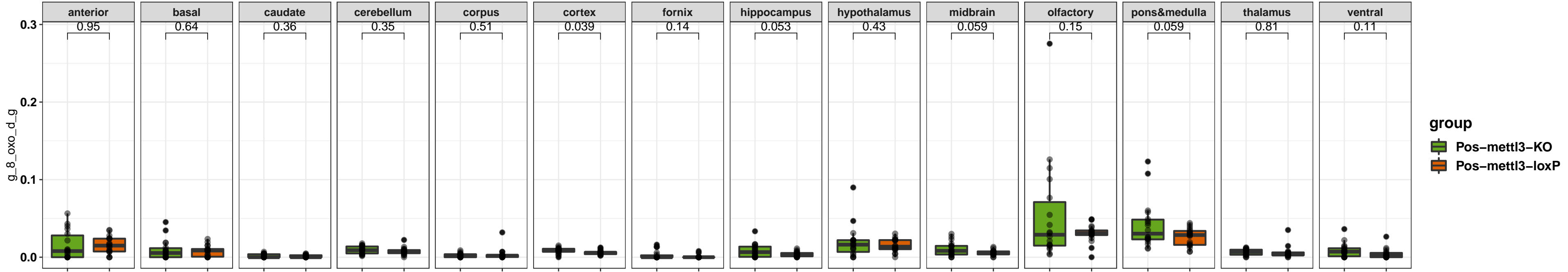
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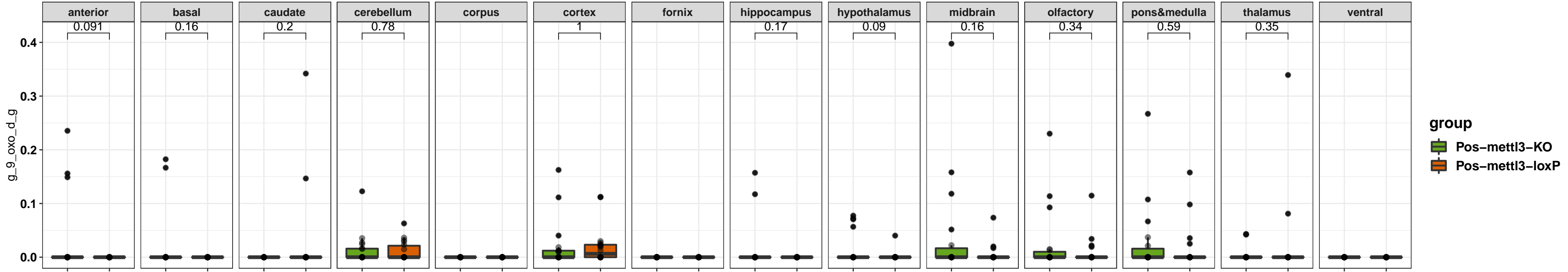
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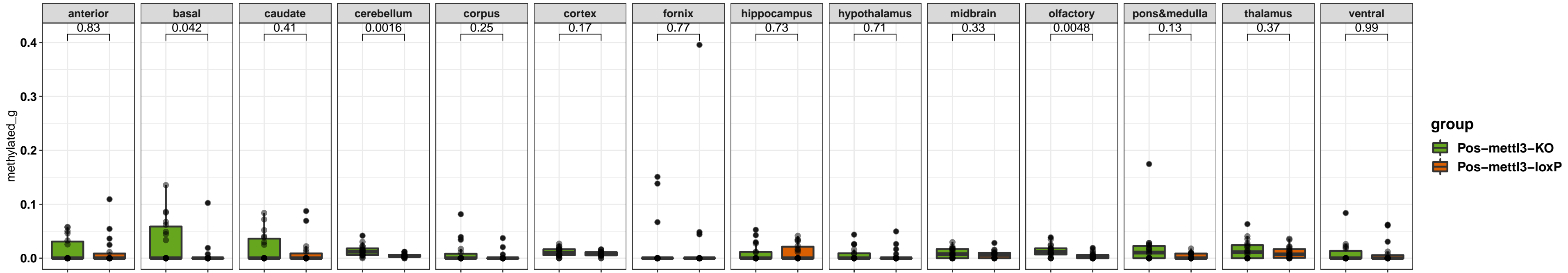
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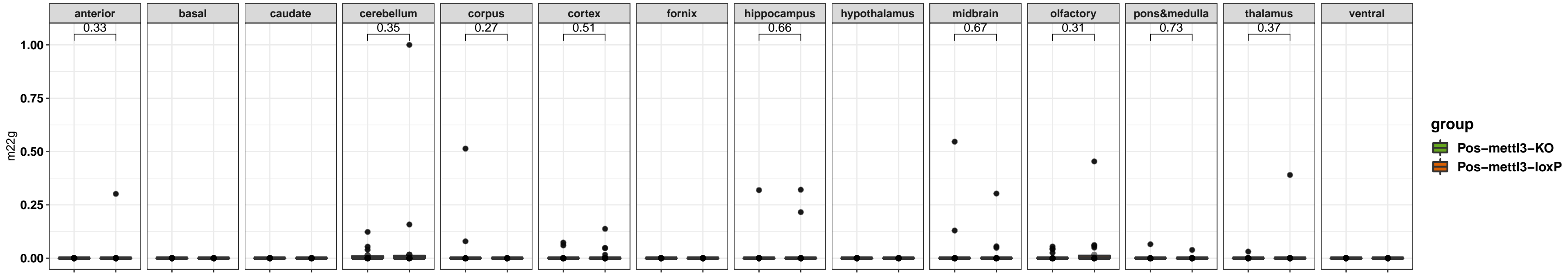
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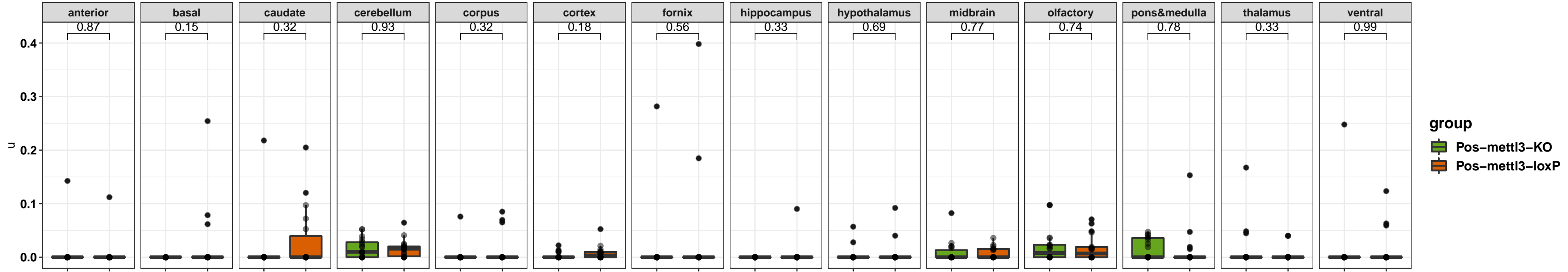
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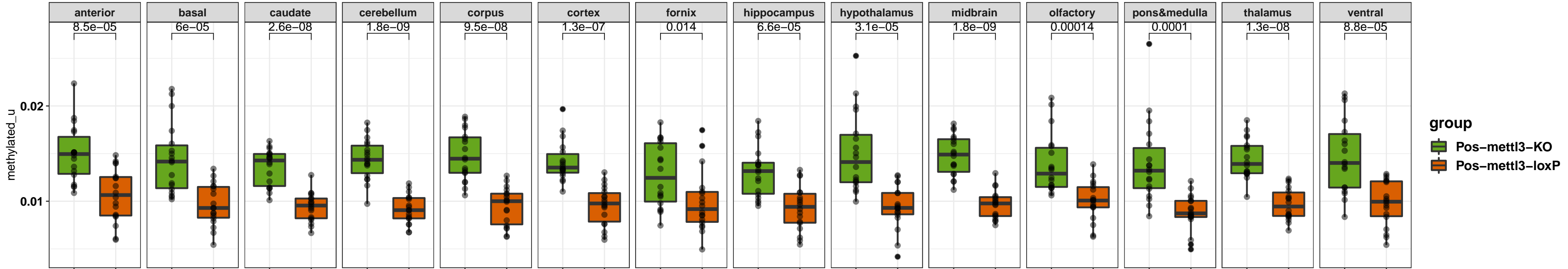
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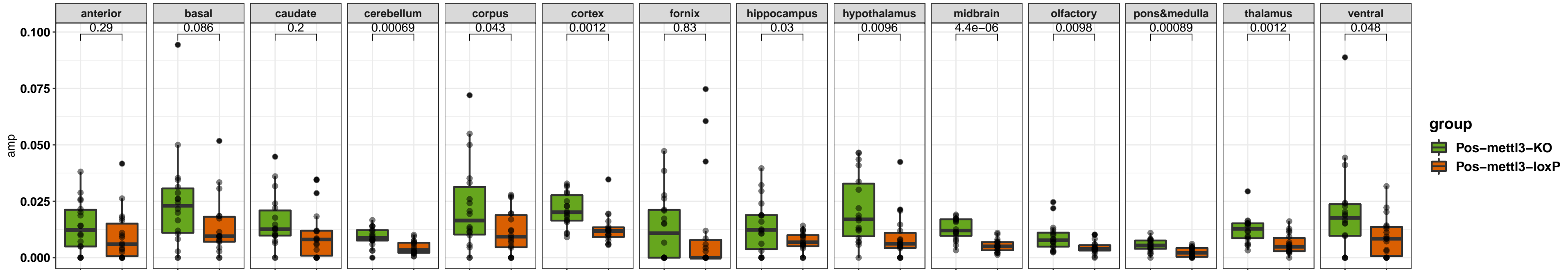
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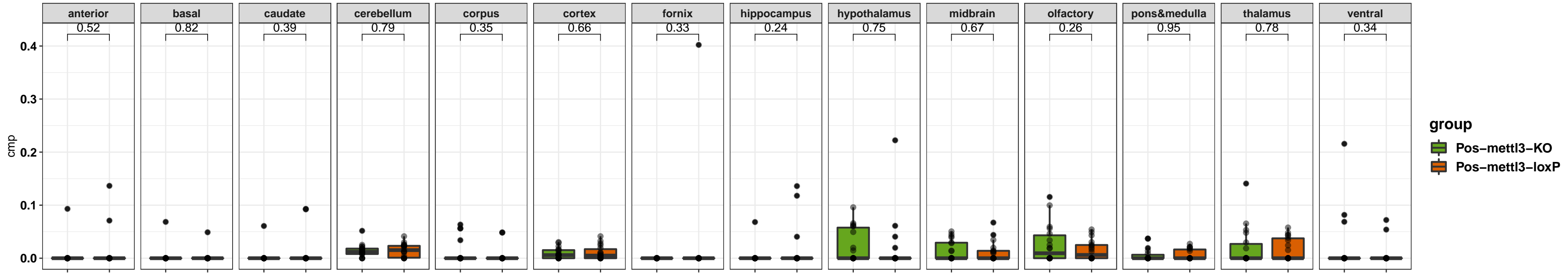
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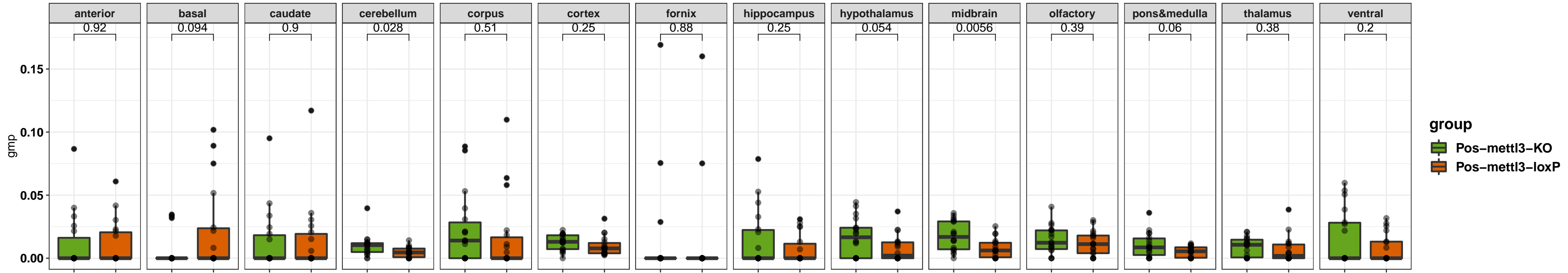
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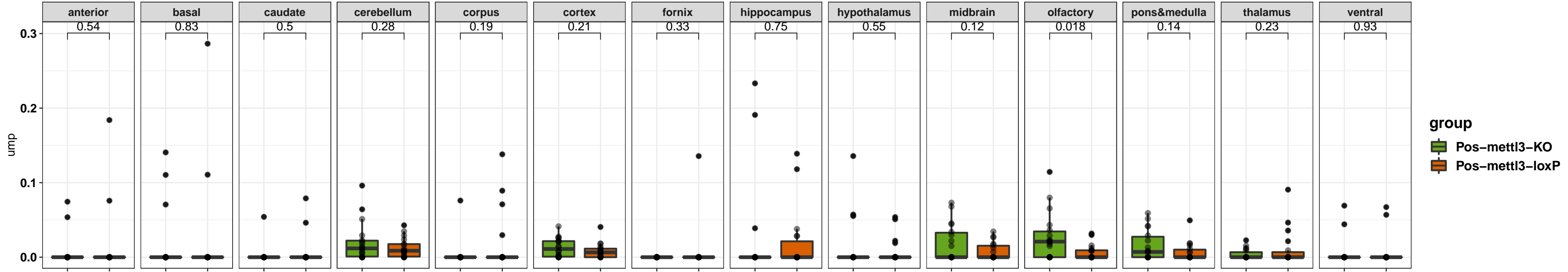
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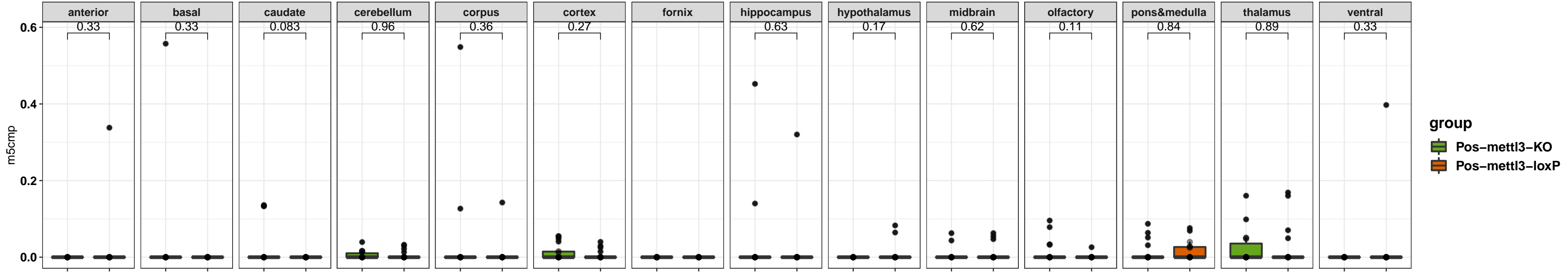
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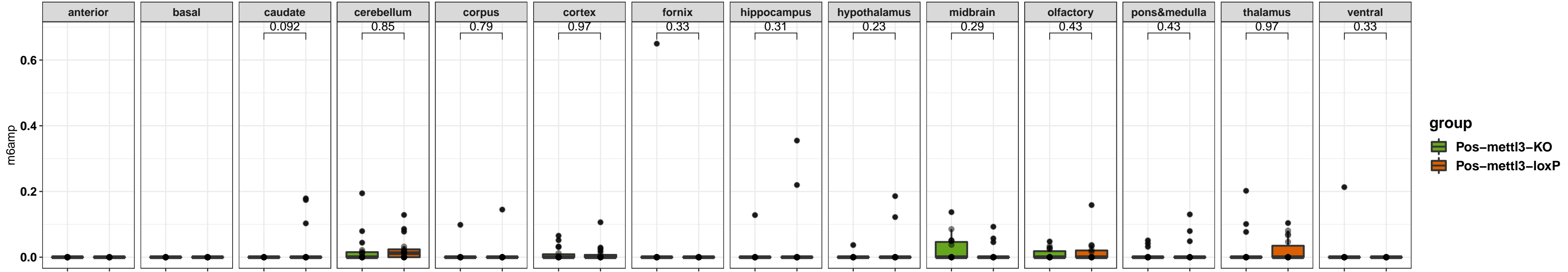
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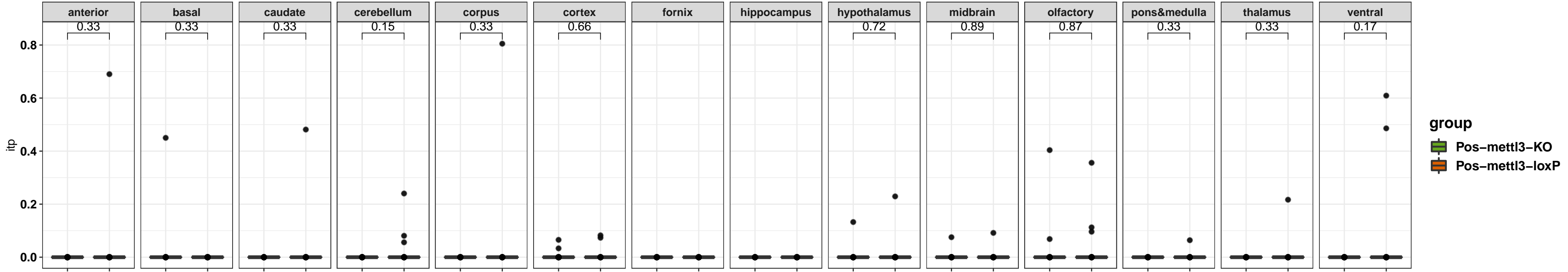
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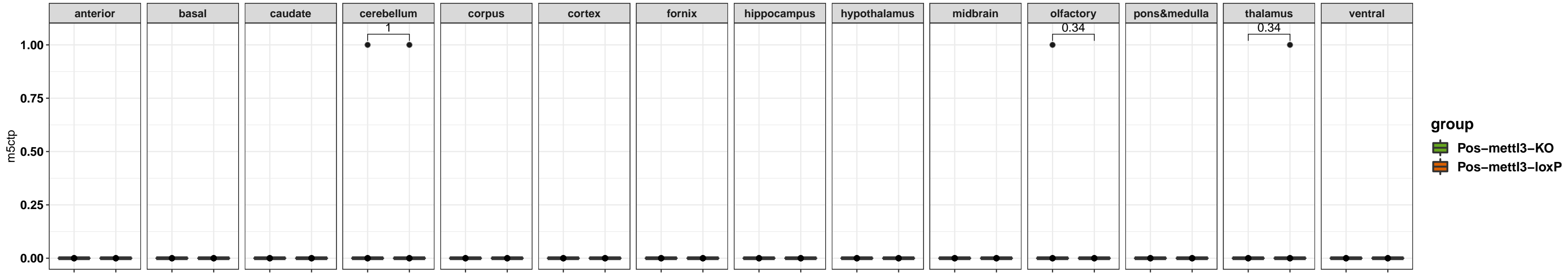
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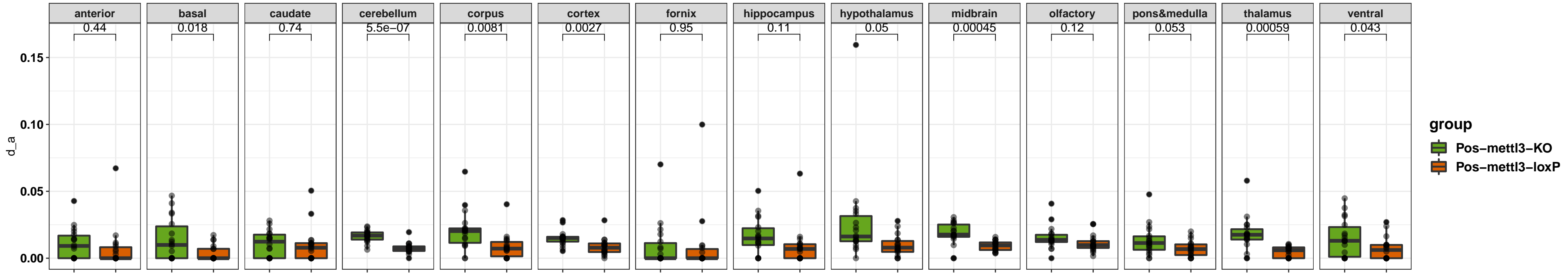
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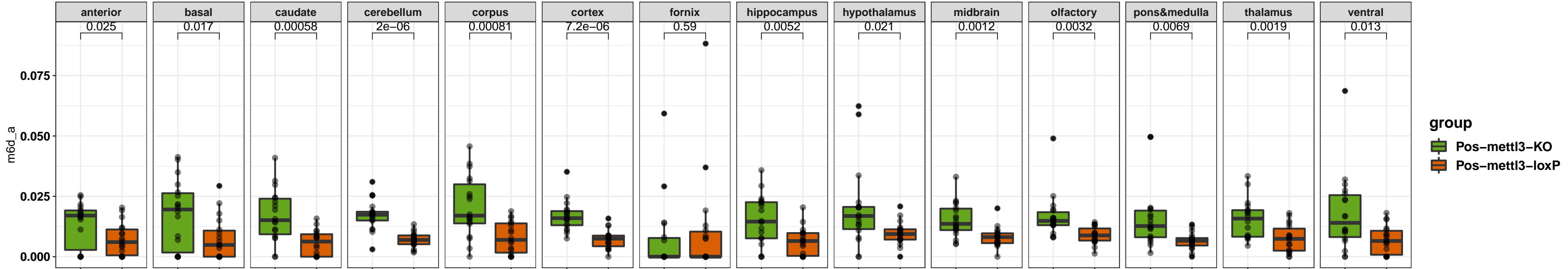
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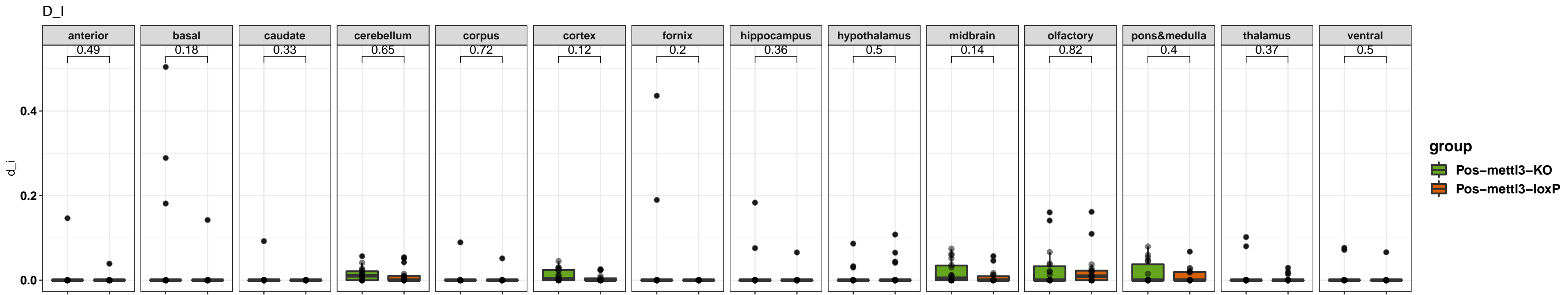


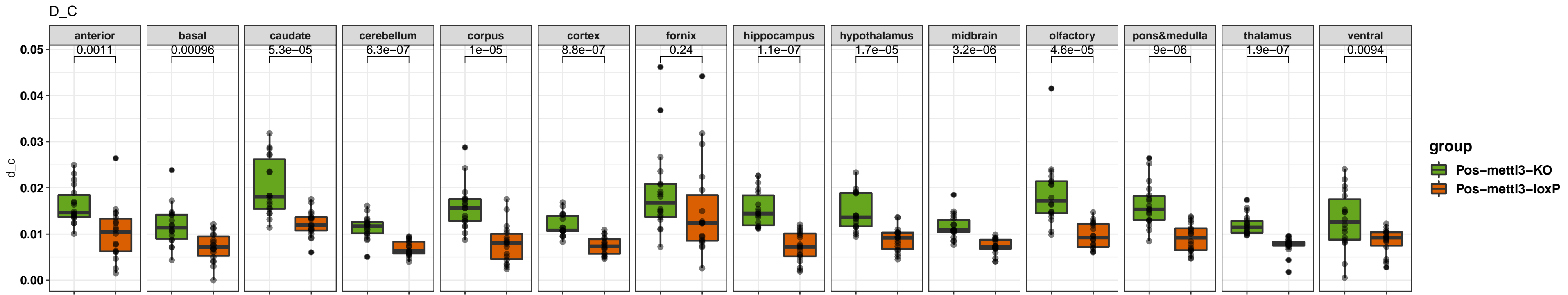
D_A



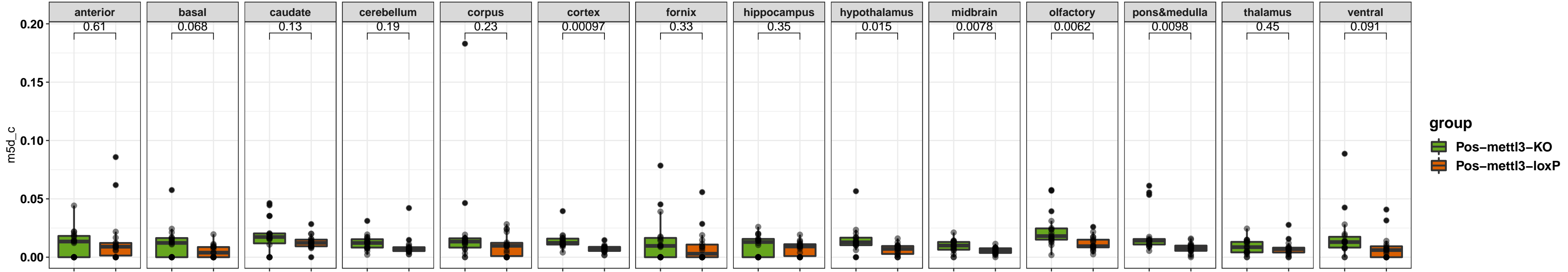
M6D_A



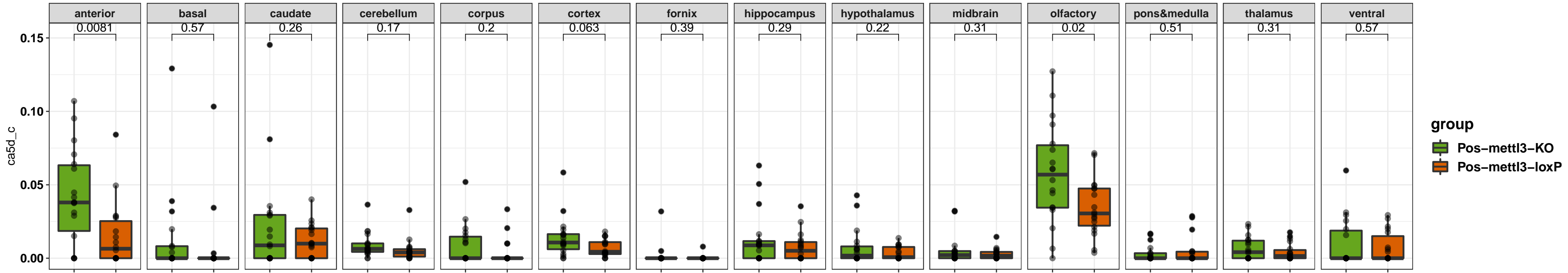


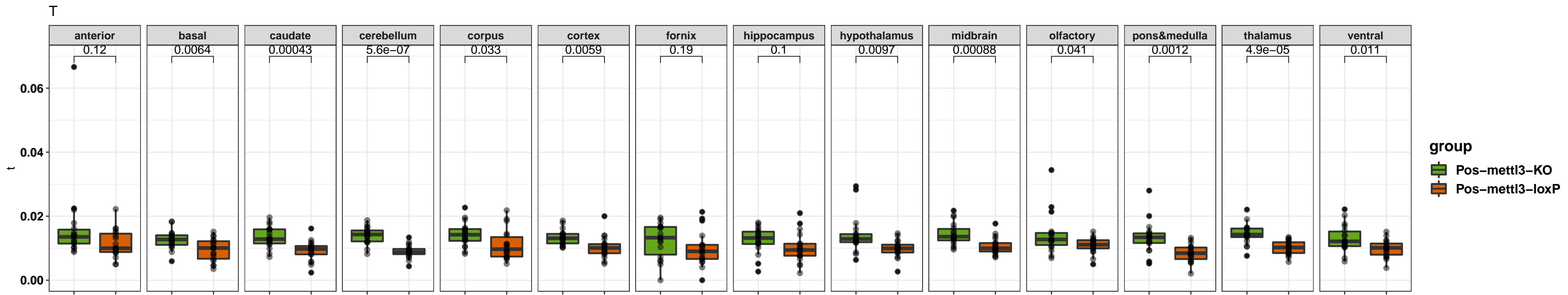


M5D_C

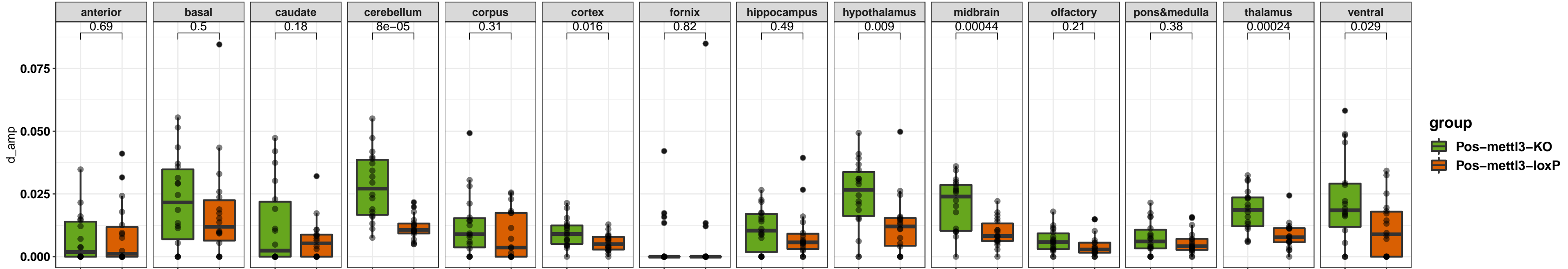


CA5D_C

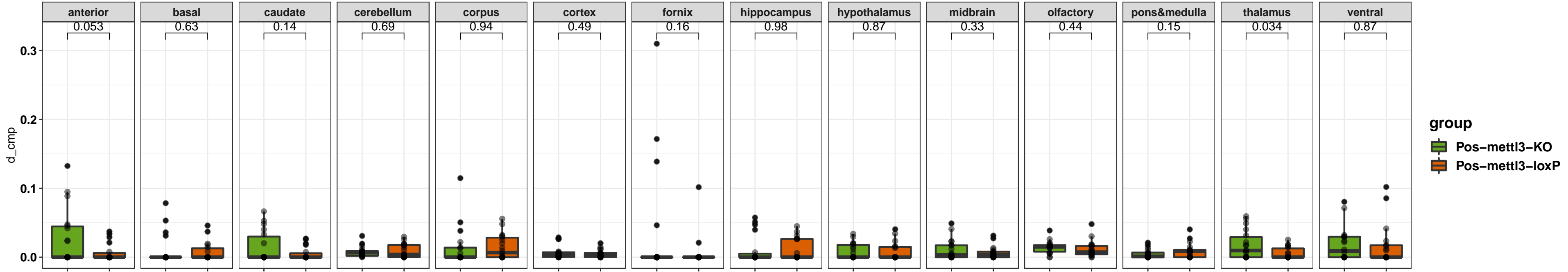




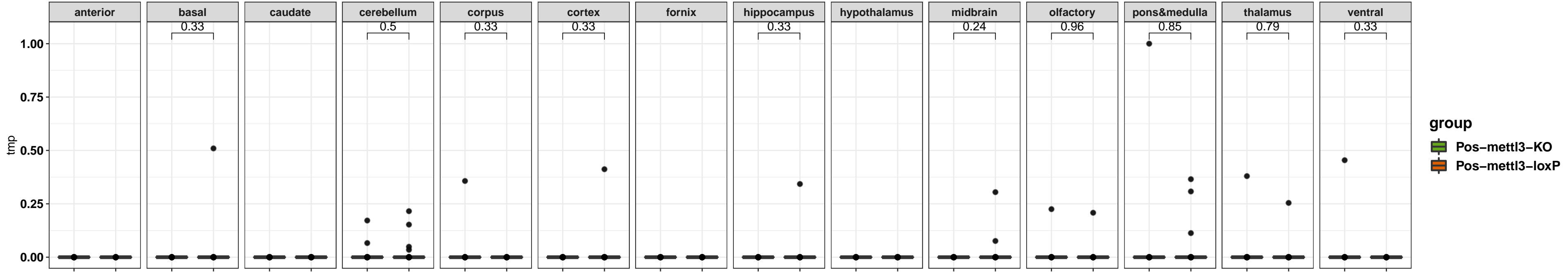
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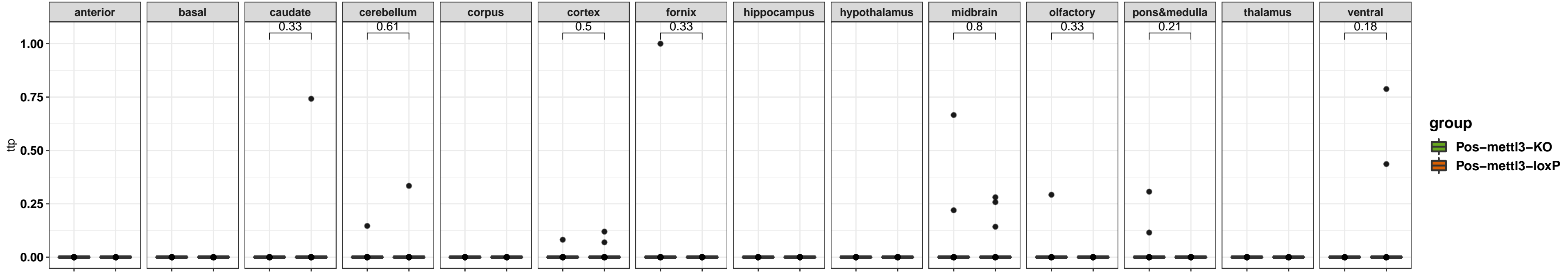
D_CMP



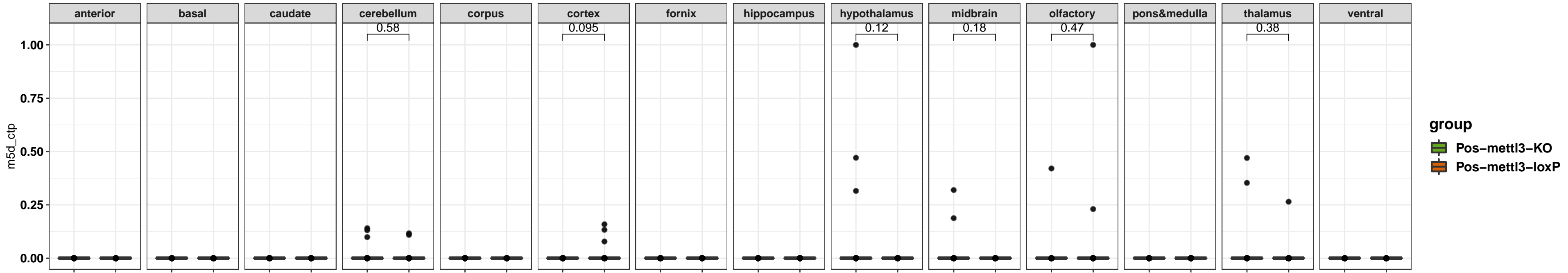
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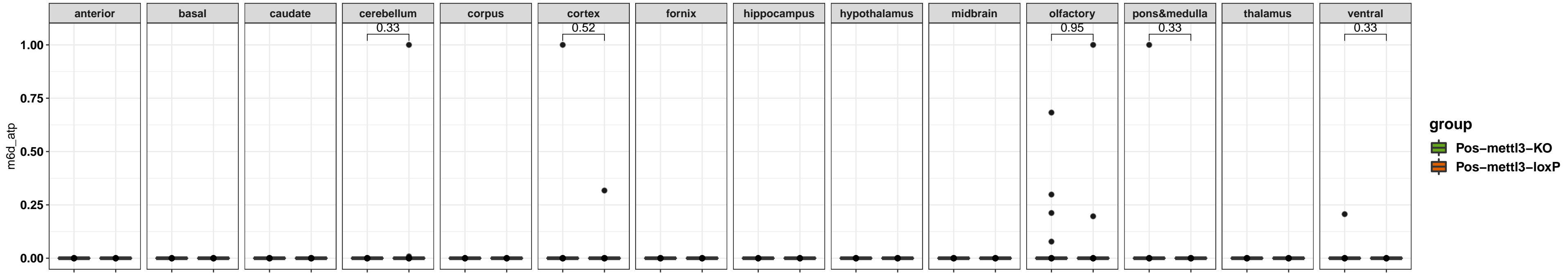
TTP



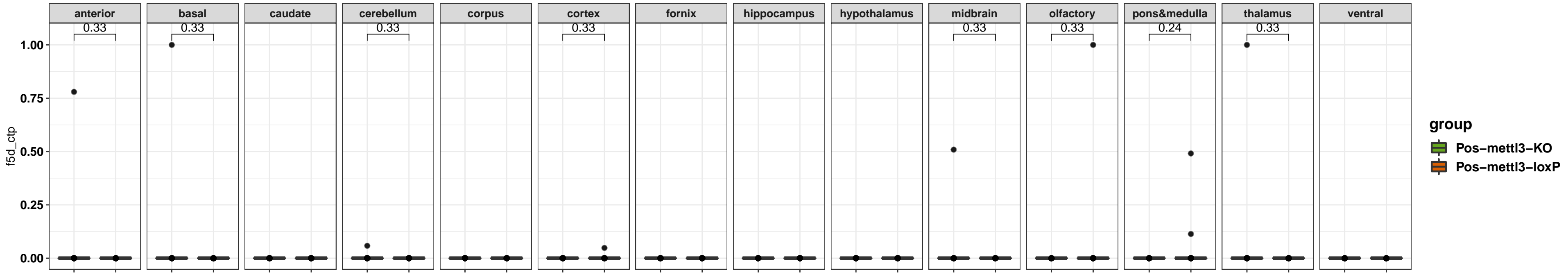
M5D_CTP



M6D_ATP



F5D_CTP



X8_OXO_D_GTP

