TI DSP, MCU 및 Xilinx Zynq FPGA 프로그래밍 전문가 과정

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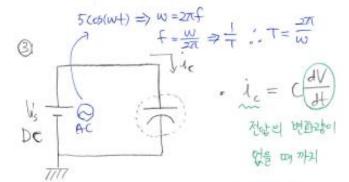
목차

회로이론

- 6) capacitor
- 7) capacitor 회로 해석
- 8) inductor
- 9) inductor 회로 해석
- 10) RLC 회로 해석

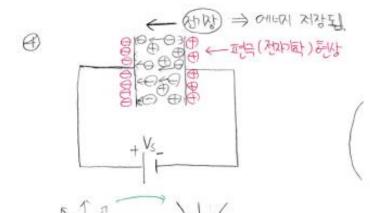
6) capacitor





• 전류의 원천
전대적 취사에비지)
전에 라이 같으면?
전체자 X

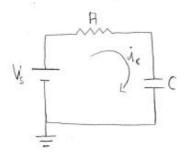
 전체 X



* 군테서 식절, 병절 연결• 직원: 저하의 병절처럼• 방절: 저하의 직결처럼

$$(b) \begin{array}{c} (b) \\ (b) \\ (b) \\ (b) \\ (b) \end{array} = \begin{array}{c} (b) \\ (b) \\ (b) \end{array} = \begin{array}{c} (b) \\ (b) \\ (b) \end{array} = \begin{array}{c} (b) \\ (b) \\ (c) \end{array} = \begin{array}{c} (c) \\ (c) \end{array} = \begin{array}{c} (c)$$

7) capacitor 회로 해석



1)
$$Q = CV$$
 $V_e = \frac{Q}{C}$

$$= \frac{dQ}{dt}R + \frac{q}{c}$$

3)
$$V_3 = Rg' + \frac{1}{c}g$$

 $g' + \frac{1}{Rc}g = \frac{V_3}{R}$
 $About M = e^{\frac{t}{Rc}}$

4)
$$\frac{4}{D} \frac{e^{\frac{t}{RC}}}{ND} + \frac{4}{ND} \frac{1}{RC} e^{\frac{t}{RC}} = \frac{V_s}{R} e^{\frac{t}{RC}}$$

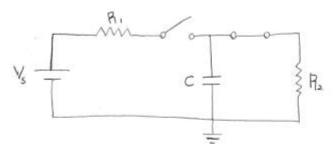
$$\int \frac{d}{dt} \left(3e^{\frac{t}{RC}} \right) = \int \frac{V_s}{R} e^{\frac{t}{RC}}$$

$$\Re e^{\frac{1}{R}} = \frac{V_R}{R} R c e^{\frac{1}{R}} + D$$

..
$$g = CV_s + De^{\frac{t}{Rc}}$$
, $g(o) = o(:b = -CV_s)$
= $CV_s(1 - e^{-\frac{t}{Rc}})$

5)
$$V_c = V_s (1 - e^{\frac{t}{Rc}}) \implies t = \infty$$
, $V_c = V_s$ (전标 공급원)

O ग्रेडियाम Capacitor हैमर्स (अल्पेड्स)

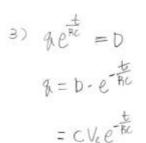


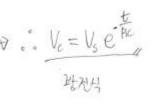
,
$$Q = CV \Rightarrow V = \frac{Q}{C}$$

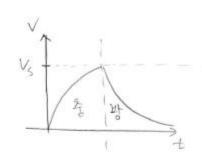
2)
$$o = \frac{1}{C} \int \dot{l}_c dt + \dot{l}_c \dot{R}_2$$

$$i = \frac{d9}{dt}$$

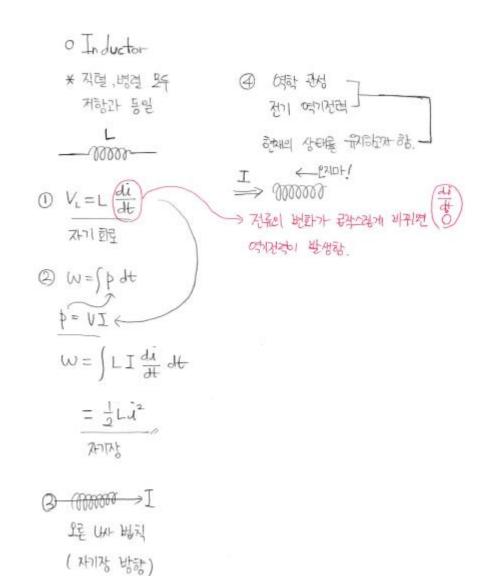
$$= \frac{1}{c} \mathfrak{J}_1 + R_2 \mathfrak{J}_1'$$



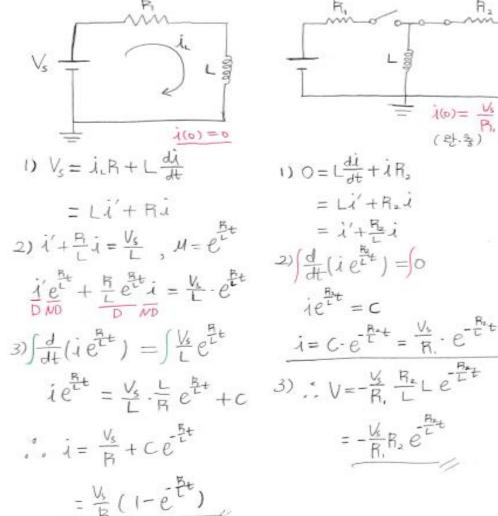


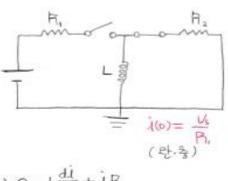


8~9) inductor, 회로 해석



o 회로에서 Inductor 해석 (충전) ←> 방전





1)
$$0 = L\frac{di}{dt} + iR$$
,

$$= Li' + R_{2}i$$

$$= i' + \frac{R_{2}i}{2}i$$
2) $\int \frac{d}{dt} (ie^{\frac{R_{2}t}{2}}) = \int 0$

$$ie^{\frac{R_{2}t}{2}} = C$$

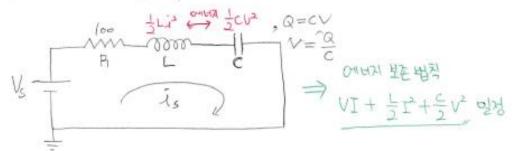
$$i = C \cdot e^{-\frac{R_{2}t}{2}} = \frac{V_{2}}{R} \cdot e^{-\frac{R_{2}t}{2}}$$

$$= -\frac{V_{2}}{R}R_{2}e^{-\frac{R_{2}t}{2}}$$

$$= -\frac{V_{2}}{R}R_{2}e^{-\frac{R_{2}t}{2}}$$

10) RLC 회로 해석

o RLC 화로해석



1)
$$V_s = i_s R + L \frac{di_s}{dt} + \frac{l}{C} \int i_s dt$$

3)
$$\dot{\lambda}_{s}'' + \frac{P_{s}}{L}\dot{\lambda}_{s}' + \frac{1}{Lc}\dot{\lambda}_{s} = 0$$

5)
$$\chi = -\frac{b \pm \int b^2 - 400}{20}$$

= $-\frac{B}{L} \pm \sqrt{\frac{B^2}{L^2} - 4\frac{1}{Lc}}$

- 6) j(t) の短程年起 Get+Cet
- © इंट c,e^{ht}+C₂te^{ht}
- ③ 数元 (入+i川) Gercos(Ut)+Czersin(Ut)

- o 저항에 選託 전압 えs(t) R = V_R(t)
- 이 인턴터에 걸라는 건합 V_L(±) = L di_s(±) d±