

2018. 3. 13 화 - 14 회차

과정 : TI, DSP, Xilinx Zynq FPGA, MCU 기반의 프로그래밍 전문가 과정

Prof. 이상훈

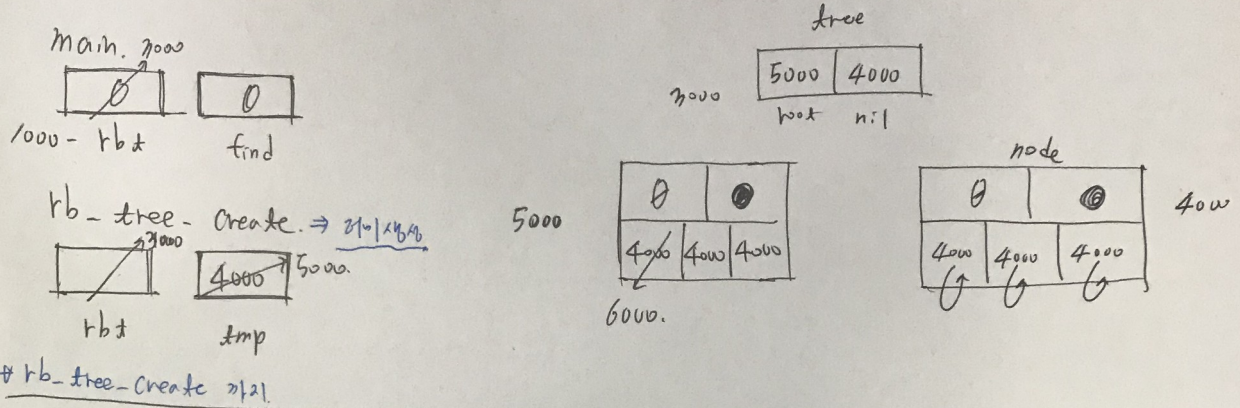
gcccompil3r@gmail.com

Stu. 정상용

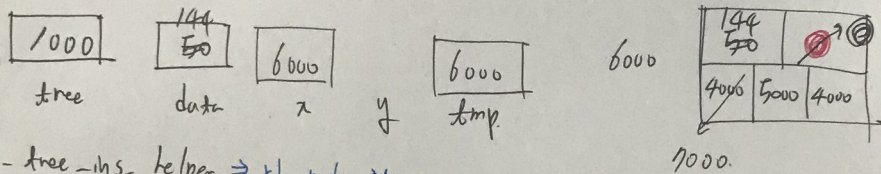
fstopdg@gmail.com

자료구조_7

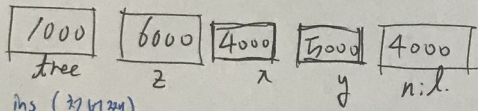
2018. 3. 17. 화 <14차화>



rb-tree-ins

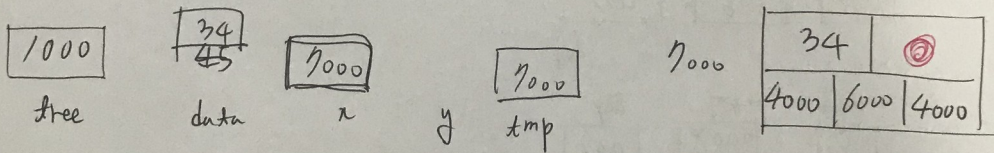


rb-tree-ins. helper \Rightarrow rb-node 생성



→ rb-tree ins (22, 12, 24)

rb-tree-ins



rb-tree-ins. helper

