

I DSP,Xilinx zynq FPGA,MCU 및
Xilinx
zynq FPGA 프로그래밍 전문가 과정

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```

#include<stdio.h>
#include<math.h>
#define delta 0.001
#define e 2.71828182845904523536
Int x_dif(Float x1_dif)
{
    Int i;
    Float sum;
    For(i=1;1<99;i++)
    {
        Sum=x[i]+0.001;
    }
    Return Sum;
}
Int y_dif(Float y1_dif)
{
    Int i;
    Float sum1;
    For(i=1;1<99;i++)
    {
        Sum1=y[i]+rand()%4;;
    }
    Return Sum;
}
Int dif(Float y1_dif, Float y2_dif)
{
    Int i;
    If(y[0])
    {

        For(i=0;i<100;i++)
        {
            If(i=0)

```

```

{
Y'[i]=y[i]/delta;
}
Else
{
Y'[i]=y[i]-y[i-1]/delta;
Return y';
}
}
}
Int dif1(Float y'1_dif)
{
Int i;
Float sum;
For(i=0;i<100;i++)
sum=-2x[i]y[i];
}
Return sum;
}

```

```

Int main(void)
{
Int i;
Float dif1,dif2,dif3;
Float Res1,Res2,Res3;
Float x[100]={0};
Float y[100]={0};
Float y'[100]={0};
Float y'1[100]={0};
Float y[0]={3};
Float x[0]={0.001};

For(i=0;i<100;i++)
{

```

```

Res1=x_dif(x[l+1]);
Res2=y_dif(y[l+1]);
Res=dif(x[l],y[l],y[l]')
Res3=dif1(y'1,x);
Int dif1(float y'1_dif,float x_dif)
{
Float dif_y=-2x[i];
Return dif_y;
}

Printf("'y'의 값은=%d,y'1의 값은 =%d\n",y'[i],y1'[i]);
Return 0;
}

```