

2018.05.09.

노트북: SW

만든 날짜: 2018-05-09 오전 11:16

수정한 날짜: 2018-05-10 오전 7:16

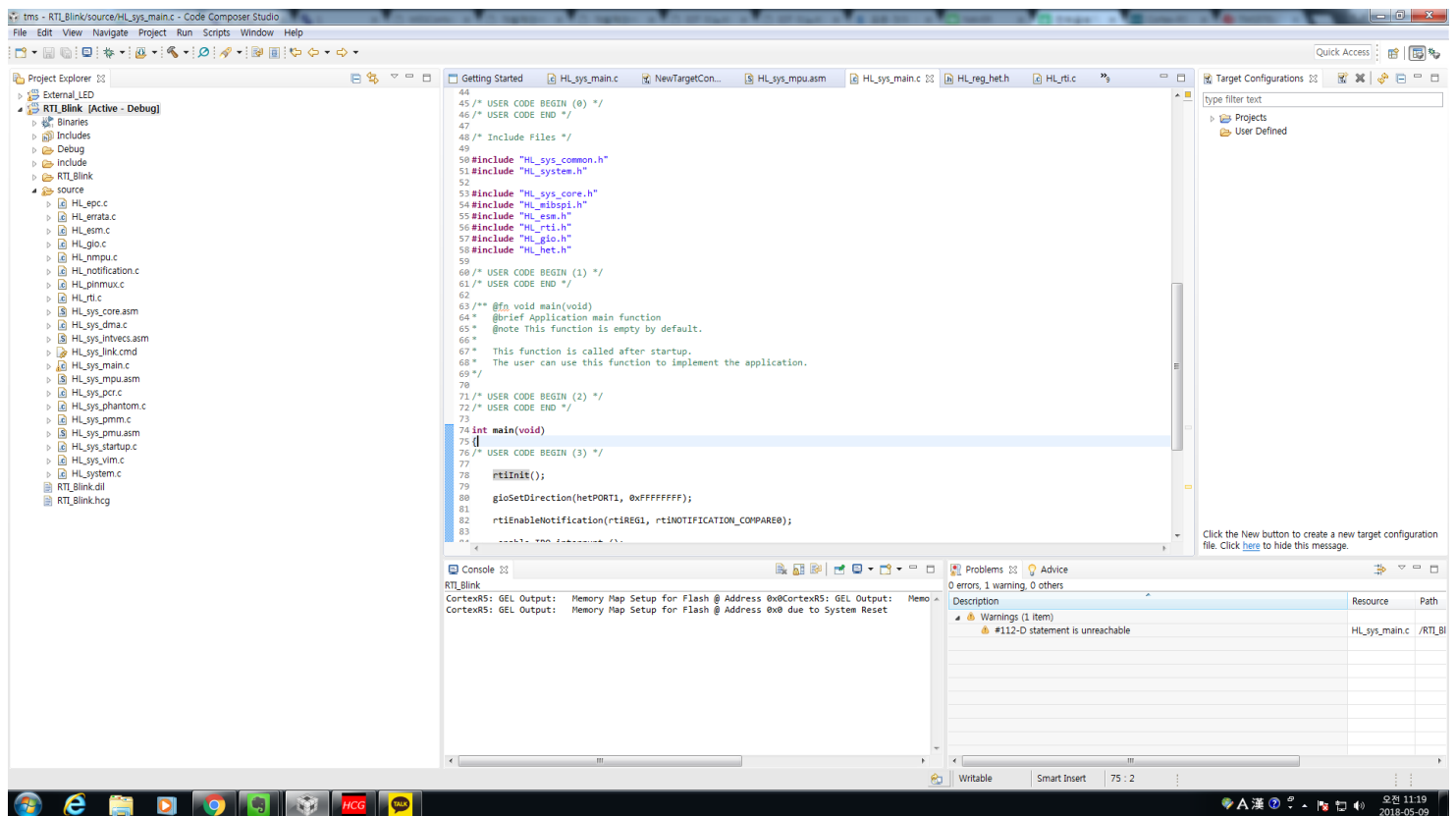
작성자: 정상요

2018. 4. 13 금 – 37회차

과정 : TI, DSP, Xilinx Zynq FPGA, MCU 기반의 프로그래밍 전문가 과정

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RTI - Interrupt & Interrupt handler
물리



Code Composer Studio interface showing the RTI_Blink project.

Project Explorer: External_LED, RTI_Blink (Active - Debug), Binaries, Includes, Debug, Include, RTI_Blink, source, HL_epc.c, HL_errata.c, HL_ism.c, HL_gpio.c, HL_mmpu.c, HL_notification.c, HL_pinmux.c, HL_rti.c, HL_sys_core.asm, HL_sys_dma.c, HL_sys_intvecs.asm, HL_sys_link.cmd, HL_sys_main.c, HL_sys_mpu.asm, HL_sys_pcr.c, HL_sys_phantom.c, HL_sys_pmm.c, HL_sys_pmu.asm, HL_sys_startup.c, HL_sys_vim.c, HL_system.c, RTI_Blink.dll, RTI_Blink.hcg.

Editor: HL_sys_main.c

```
63 /** @fn void main(void)
64 * @brief Application main function
65 * @note This function is empty by default.
66 *
67 * This function is called after startup.
68 * The user can use this function to implement the application.
69 */
70
71 /* USER CODE BEGIN (2) */
72 /* USER CODE END */
73
74 int main(void)
75 {
76 /* USER CODE BEGIN (3) */
77
78 rtiInit();
79
80 gpioSetDirection(hetPORT1, 0xFFFFFFFF);
81
82 rtiEnableNotification(rtiREG1, rtiNOTIFICATION_COMPARE0);
83
84 _enable_TRQ_interrupt();
85
86 rtiStartCounter(rtiREG1, rtiCOUNTER_BLOCK0);
87
88 while(1);
89 /* USER CODE END */
90
91
92 return 0;
93 }
94
95 void rtiNotification(rtiBASE_t *rtiREG, uint32 notification)
96 {
97     gpioSetPort(hetPORT1, gpioGetPort(hetPORT1) ^ 0x00000001);
98 }
99
100 /* USER CODE BEGIN (4) */
101 /* USER CODE END */
102
```

Console:

```
RTI_Blink
CortexRS: GEL Output: Memory Map Setup for Flash @ Address 0x0
CortexRS: GEL Output: Memory Map Setup for Flash @ Address 0x0 due to System Reset
```

Problems: 0 errors, 1 warning, 0 others

Description	Resource	Path
Warnings (1 item)		
#112-D statement is unreachable	HL_sys_main.c	/RTI_Blink

Click the New button to create a new target configuration file. Click [here](#) to hide this message.

Compare 0 Period: 1000 Update Compare 0: 9375000
Compare 0: 9375000

Comp 0 Source:
Counter 0: → [] — [] → 9.375000000
Counter 1: → [] — [] →

Actual Period (ms): 1000.000

Compare 1 Period: 5.000 Update Compare 1: 46875
Compare 1: 46875

Comp 1 Source:
Counter 0: → [] — [] → 9.375000000
Counter 1: → [] — [] →

Actual Period (ms): 5.000

Compare 2 Period: 8.000 Update Compare 2: 75000
Compare 2: 75000

Comp 2 Source:
Counter 0: → [] — [] → 9.375000000
Counter 1: → [] — [] →

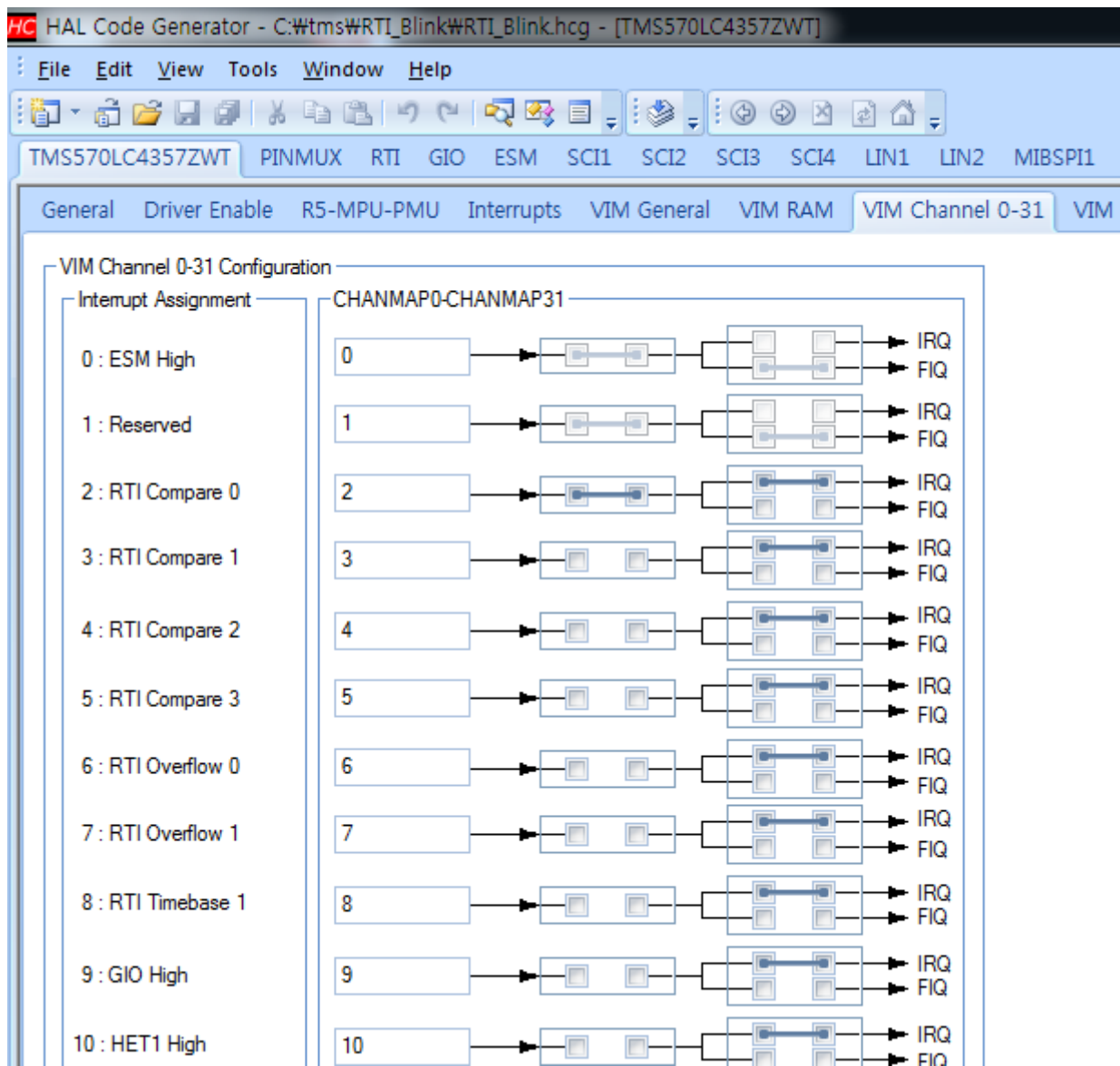
Actual Period (ms): 8.000

Compare 3 Period: 10.000 Update Compare 2: 93750
Compare 2: 93750

Comp 3 Source:
Counter 0: → [] — [] → 9.375000000
Counter 1: → [] — [] →

Actual Period (ms): 10.000

Interrupt/DMA



compare : 기존에 비교할 값
update : 비교하고 그 다음에 비교할 값

Real time interrupt를 사용하기 위하여 기본 세팅
hetport1번을 활성화 시킴
인터럽트 활성화시킴
실제 CPU가 동작모드 IRQ를 쓸수있도록 세팅
RTI를 쓸수있도록 최종세팅

rtiNotification : 인터럽트 발생 1sec 후 동작
gioGetPort :



sketch_may09a \$

```
#include <Servo.h>

Servo myservo;

int pos = 0;

void setup()
{
  myservo.attach(9);
}

void loop()
{
  for(pos = 0; pos < 180; pos += 1)
  {
    myservo.write(pos);
    delay(15);
  }
  for(pos = 180; pos >= 1; pos = 1)
  {
    myservo.write(pos);
    delay(15);
  }
}
```