

TI DSP, MCU 및 Xilinx Zynq FPGA 프로그래밍 전문가 과정



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50 일차

강사 - Innova Lee(이상훈)

gcccompil3r@gmail.com

학생 - 신민철

akrn33@naver.com

```

int main(void)
{
    /* USER CODE BEGIN (3) */
    rtiInit();

    gpioSetDirection(hetPORT1, 0xFFFFFFFF);

    rtiEnableNotification(rtiREG1, rtiNOTIFICATION_COMPARE0);

    _enable_IRQ_interrupt_();

    rtiStartCounter(rtiREG1, rtiCOUNTER_BLOCK0);

    while(1);
    /* USER CODE END */

    return 0;
}

void rtiNotification(rtiBASE_t* rtiREG, uint32 notification)
{
    gpioSetPort(hetPORT1, gpioGetPort(hetPORT1) ^ 0x00000001);
}

```

죄송합니다.