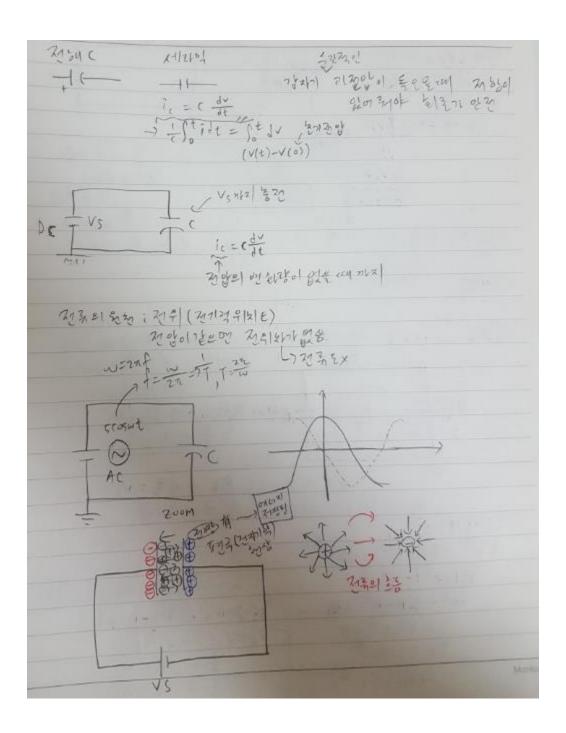
TI DSP, MCU, Xilinx Zynq FPGA 기반의 프로그래밍전문가 과정

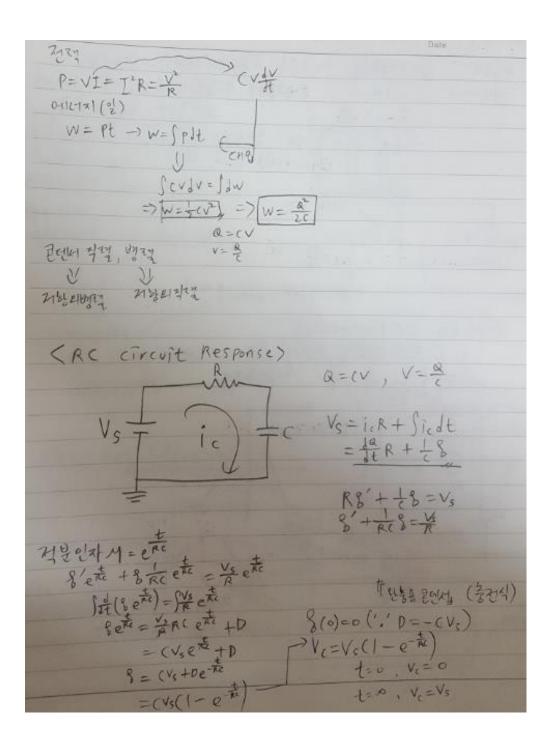
<회로이론> 2018.06.20 - 77일차

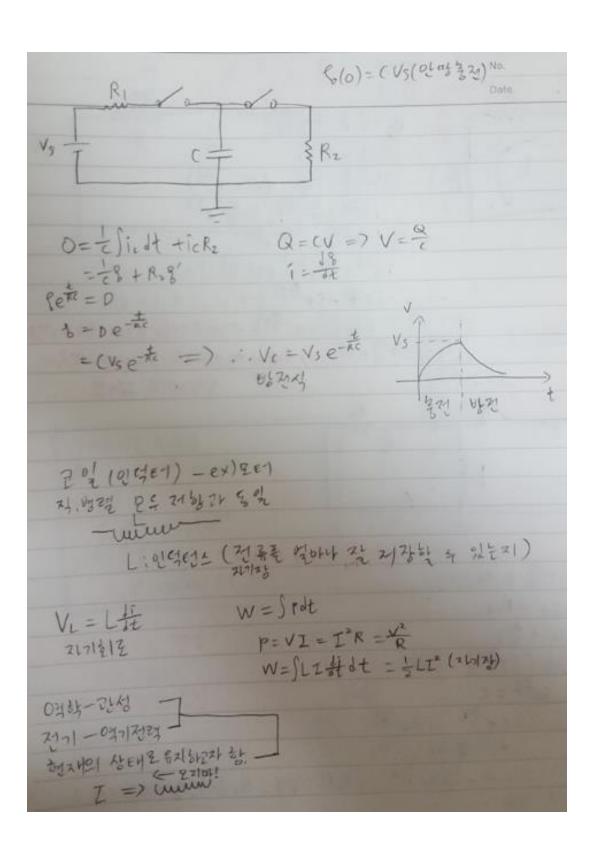
강사 – Innova Lee(이상훈) gcccompil3r@gmail.com

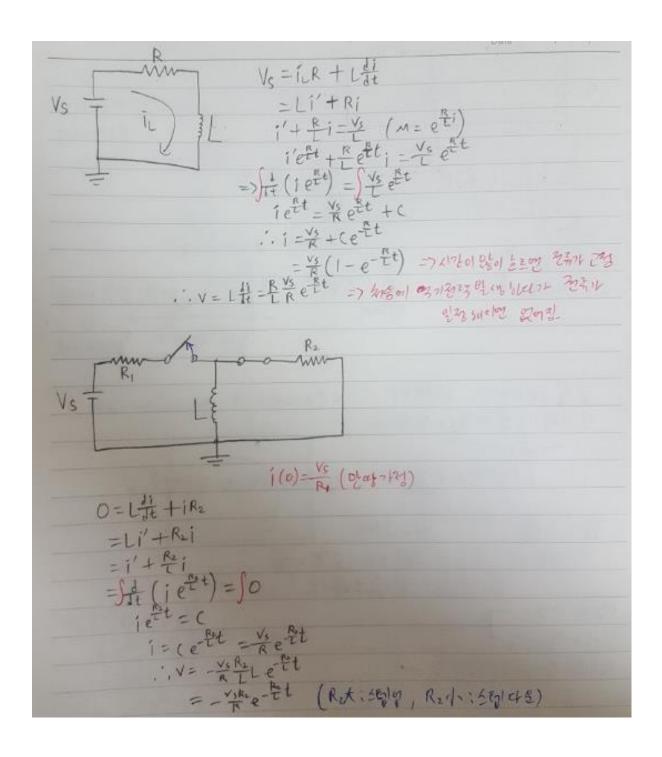
학생 – 안상재 sangjae2015@naver.com

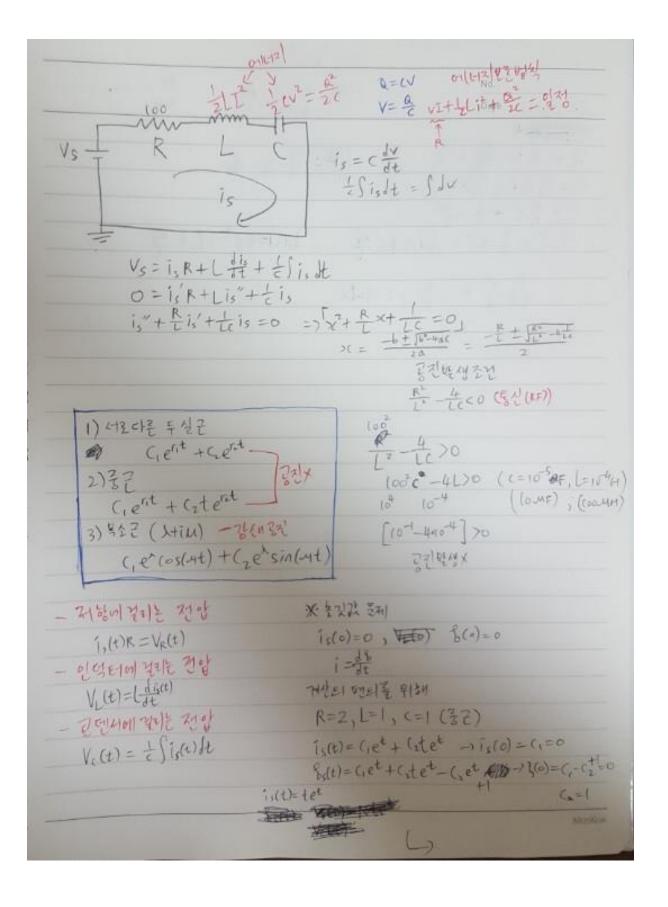
- 콘덴서 개념 및 정의식
- RC 회로 분석
- 코일 개념 및 정의식
- LC 회로 분석
- RLC 회로 분석











201 V= L # No.
201/1 0= CV No.
201/1 0= CV No.
Date

VS = isR + L dis + Vc

VS = isR + L dis + Vc

VS = isR + L dis + E

8" + E 8' + E = Vs

(Am)" + E (Am)" + tc(Am) = Ve

Control V= L dis

VS = isR + L dis + Vc

VS = isR + L dis