

# TI DSP, MCU 및 Xilinx Zynq FPGA 프로그래밍 전문가 과정



2018.05.15

54 일차

강사 - Innova Lee(이상훈)

[gcccompil3r@gmail.com](mailto:gcccompil3r@gmail.com)

학생 - 신민철

[akrn33@naver.com](mailto:akrn33@naver.com)

```

#include "HL_sys_common.h"
#include "FreeRTOS.h"
#include "os_task.h"

#include "HL_het.h"
#include "HL_gio.h"

xTaskHandle xTask1Handle;
xTaskHandle xTask2Handle;

void vTask1(void *pvParameters)
{
    for(;;)
    {
        gioSetBit(hetPORT1, 17, gioGetBit(hetPORT1, 17) ^ 1);
        vTaskDelay(100);
    }
}
void vTask2(void *pvParameters)
{
    for(;;)
    {
        gioSetBit(gioPORTB, 4, gioGetBit(gioPORTB, 4) ^ 1);
        vTaskDelay(100);
    }
}
void main(void)
{
    gioSetDirection(hetPORT1, 0xFFFFFFFF);
    gioInit();
    if(xTaskCreate(vTask1, "Task1", configMINIMAL_STACK_SIZE, NULL, 1,
&xTask1Handle) != pdTRUE)//fork 와 같음
    {

        while(1);
        gioSetBit(hetPORT1, 5, gioGetBit(hetPORT1, 5) ^ 1);

    }
    if(xTaskCreate(vTask2, "Task2", configMINIMAL_STACK_SIZE, NULL, 1,
&xTask2Handle) != pdTRUE)
    {
        while(1);
    }

    vTaskStartScheduler();

    while(1);
}

/* USER CODE BEGIN (4) */
/* USER CODE END */

```

