## Xilinx Zynq FPGA, TI DSP, MCU 기반의 프로그래밍 및 회로 설계 전문가 과정

Mibspi(Loopback)

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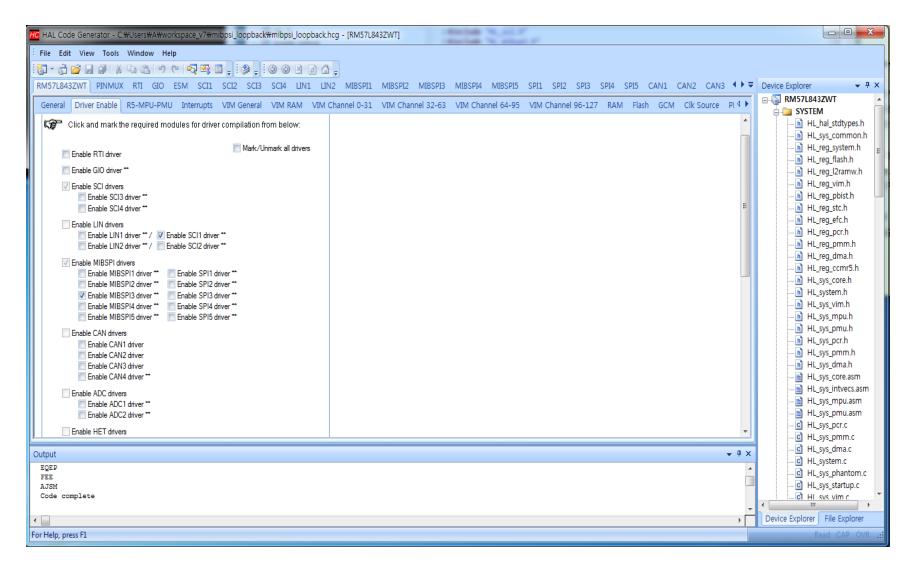
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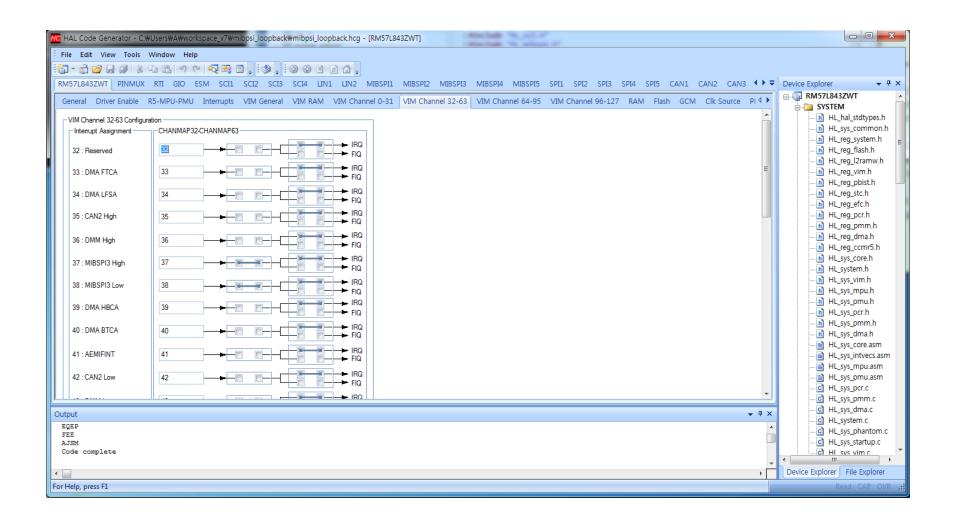
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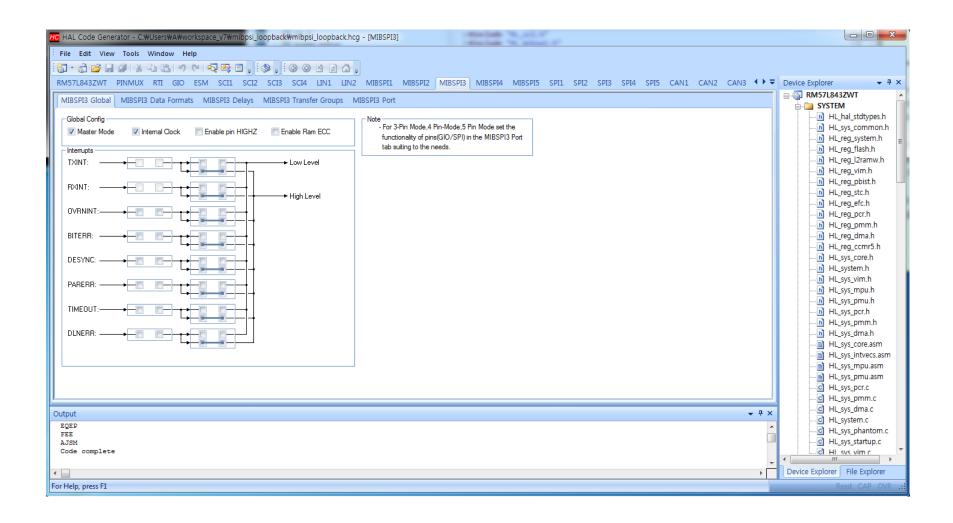
# 1.Halcogen Setting(Driver Enable)



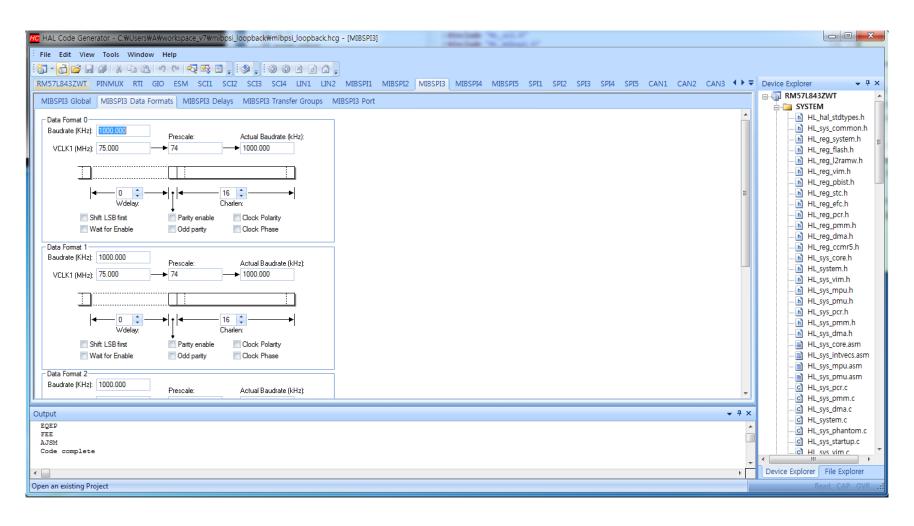
## 2.Halcogen Setting(VIM Channel)



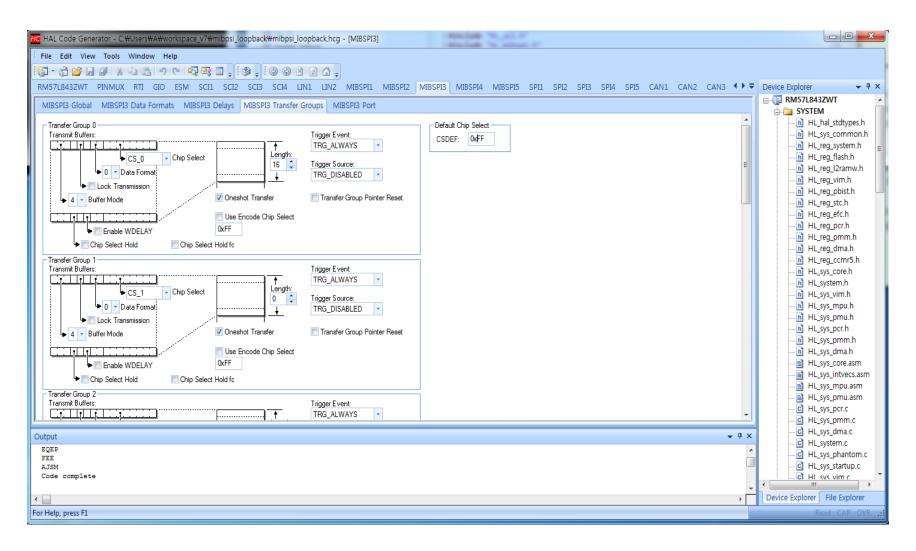
# 3. Halcogen Setting (Mibspi Global)



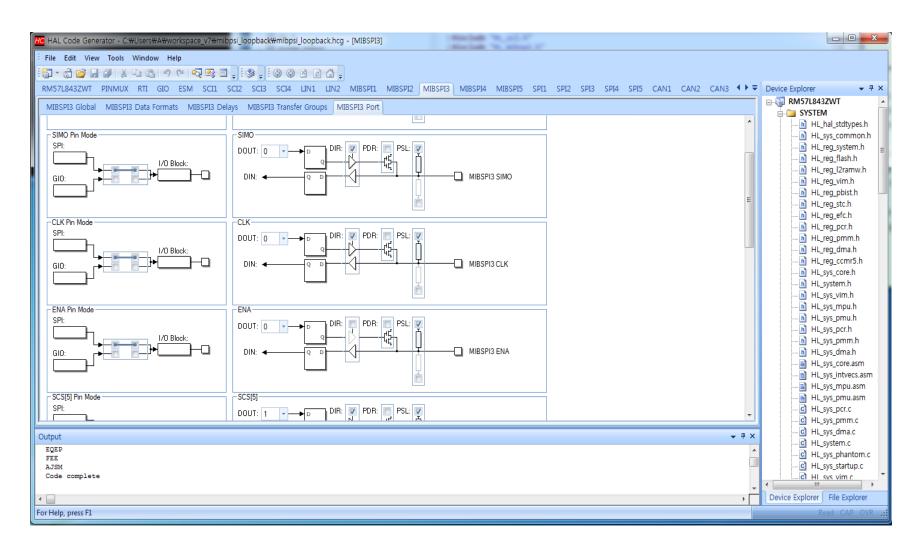
# 4.Halcogen Setting(Mibspi Data Formats)



#### 5. Halcogen Setting (Mibspi Transfer Groups)



## 6.Halcogen Setting(Mibspi port)



#### 7.Code(Mibspi Loopback)

```
1 #include "HL sci.h"
  2 #include "HL mibspi.h"
  3 #include "HL sys common.h"
  4 #include "HL system.h"
  5 #include <stdio.h>
  6 #include <string.h>
  8 #define UART
                    sciREG1
 10 uint16 tx data[8] = {1,2,3,4,5,6,7,8};
 11 uint16 rx data[8] = {0};
 12
 13 void sciDisplayText(sciBASE t *sci, uint8 *text, uint32 len);
 14
 15 void main(void)
 16 {
       volatile i;
17
       char txt buf[256];
 18
       unsigned int buf len;
 19
 20
 21
       enable IRQ interrupt ();
 22
 23
       sciInit();
       mibspiInit();
 24
 25
       mibspiEnableLoopback(mibspiREG3, Digital Lbk);
 26
       //mibspiEnableGroupNotification(mibspiREG3, 0, 1);
 27
       mibspiSetData(mibspiREG3, 0, &tx data[0]);
 28
       mibspiTransfer(mibspiREG3, 0);
 29
```

#### 8.Code(Mibspi Loopback)

```
30
31
      sprintf(txt buf, "Master Transfer\n\r\0");
      buf len = strlen(txt buf);
32
      sciDisplayText(sciREG1, (uint8 *)txt buf, buf len);
33
34
35
      while(!(mibspiIsTransferComplete(mibspiREG3, 0)))
36
37
38
      mibspiGetData(mibspiREG3, 0, &rx data[0]);
39
40
      for(i=0;i<8;i++)
41
42
               sprintf(txt buf, "rx data[%d] = %d\n\r\0", i,(char)rx data[i]);
43
               buf len = strlen(txt buf);
44
              sciDisplayText(sciREG1, (uint8 *)txt_buf, buf len);
45
          }
46
47
      while(1);
48
49
50
51
52 }
53
```

## 9.Tera Term(Mibspi Loopback)

```
COM5 - Tera Term VT
             수정(E)
                         설정(S)
                                     제어(O)
                                                  창(W)
                                                            도움말(H)
Master Tr<u>ansfer</u>
rx_data[0] = 1
rx_data[1] = 2
rx_data[2] = 3
rx_data[3] = 4
rx data[4] = 5
rx data[5] = 6
rx_data[6] = 7
rx_data[7] = 8
```

## 10.CCS(Expressions)

	pressions 🛭 👭 Registers	v I			•••	1 1	
Expression	Туре	Value	Address				
(x)= RX_data	unknown	identifier not found: RX					
(x)= rx_data1	unknown	identifier not found: rx					
🛮 🏉 rx_data	unsigned short[8]	[1,2,3,4,5]	0x08001550				
(x)= [0]	unsigned short	1	0x08001550				
(x)= [1]	unsigned short	2	0x08001552				
(x)= [2]	unsigned short	3	0x08001554				
(x)= [3]	unsigned short	4	0x08001556				
(x)= [4]	unsigned short	5	0x08001558				
(x)= [5]	unsigned short	6	0x0800155A				
(x)= [6]	unsigned short	7	0x0800155C				
(x)= [7]	unsigned short	8	0x0800155E				
Add new exp.	ressio						