Xilinx Zynq FPGA, TI DSP, MCU 기반의 프로그래밍 및 회로 설계 전문가 과정

Mibspi(Loopback)

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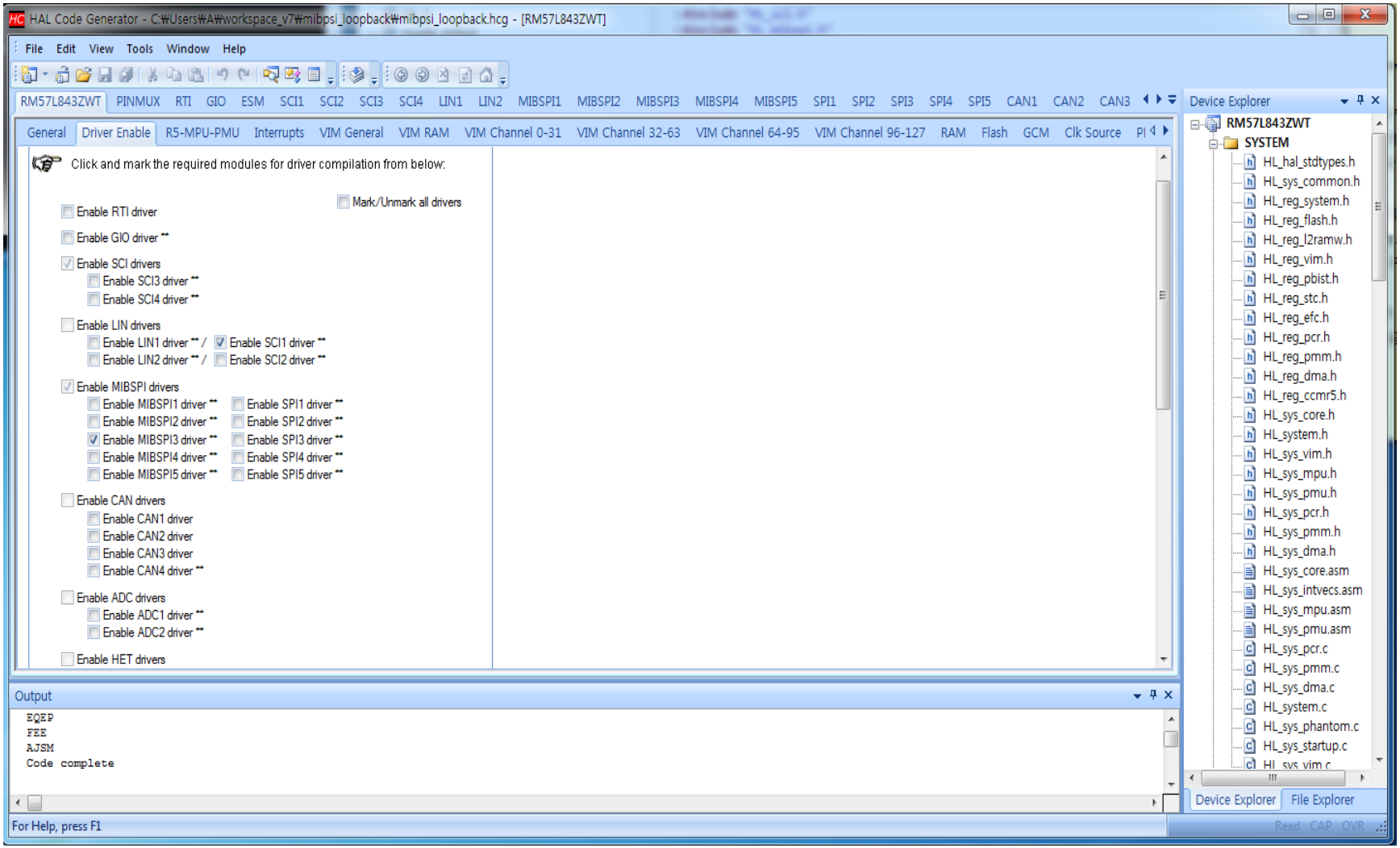
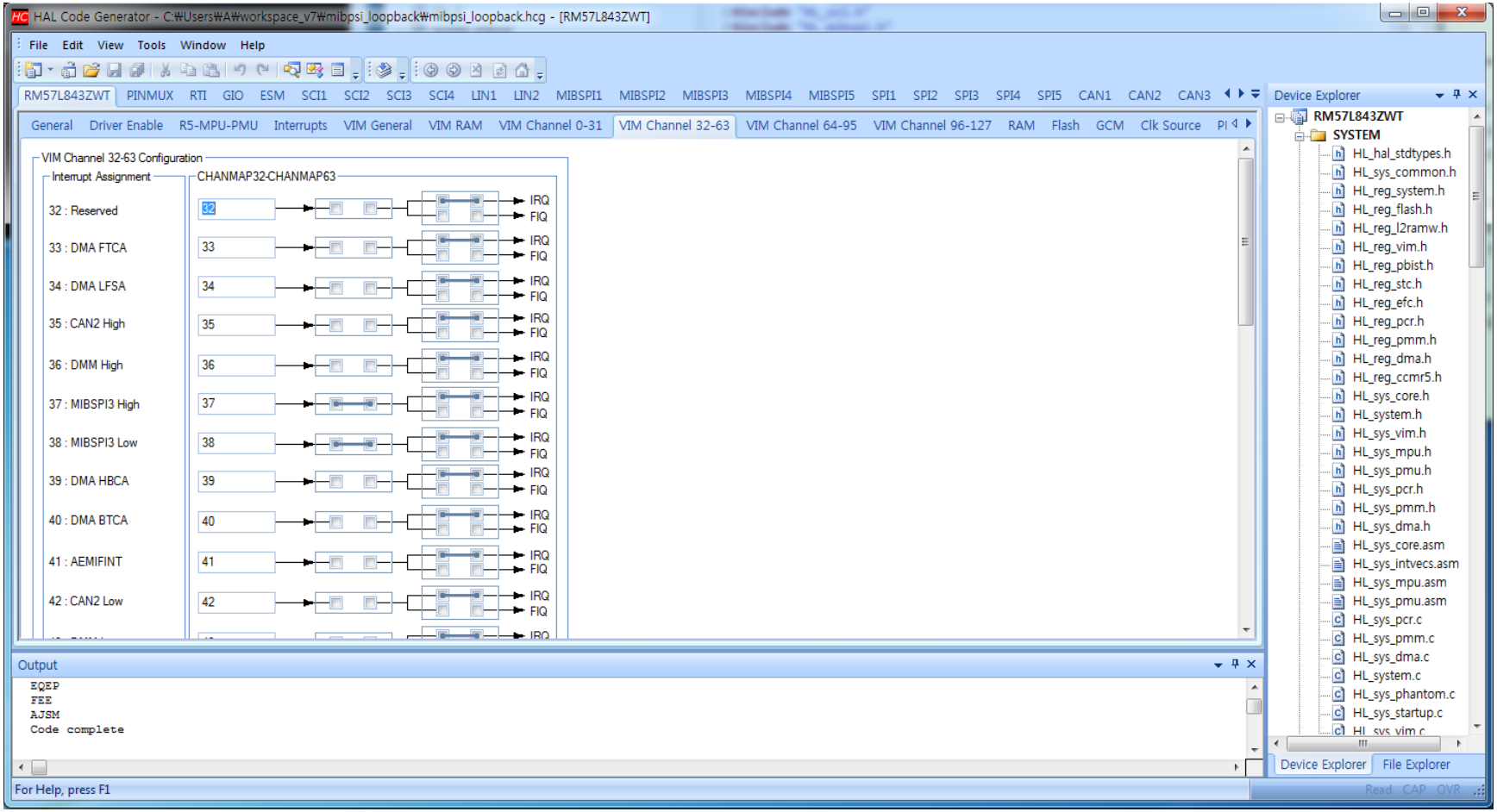
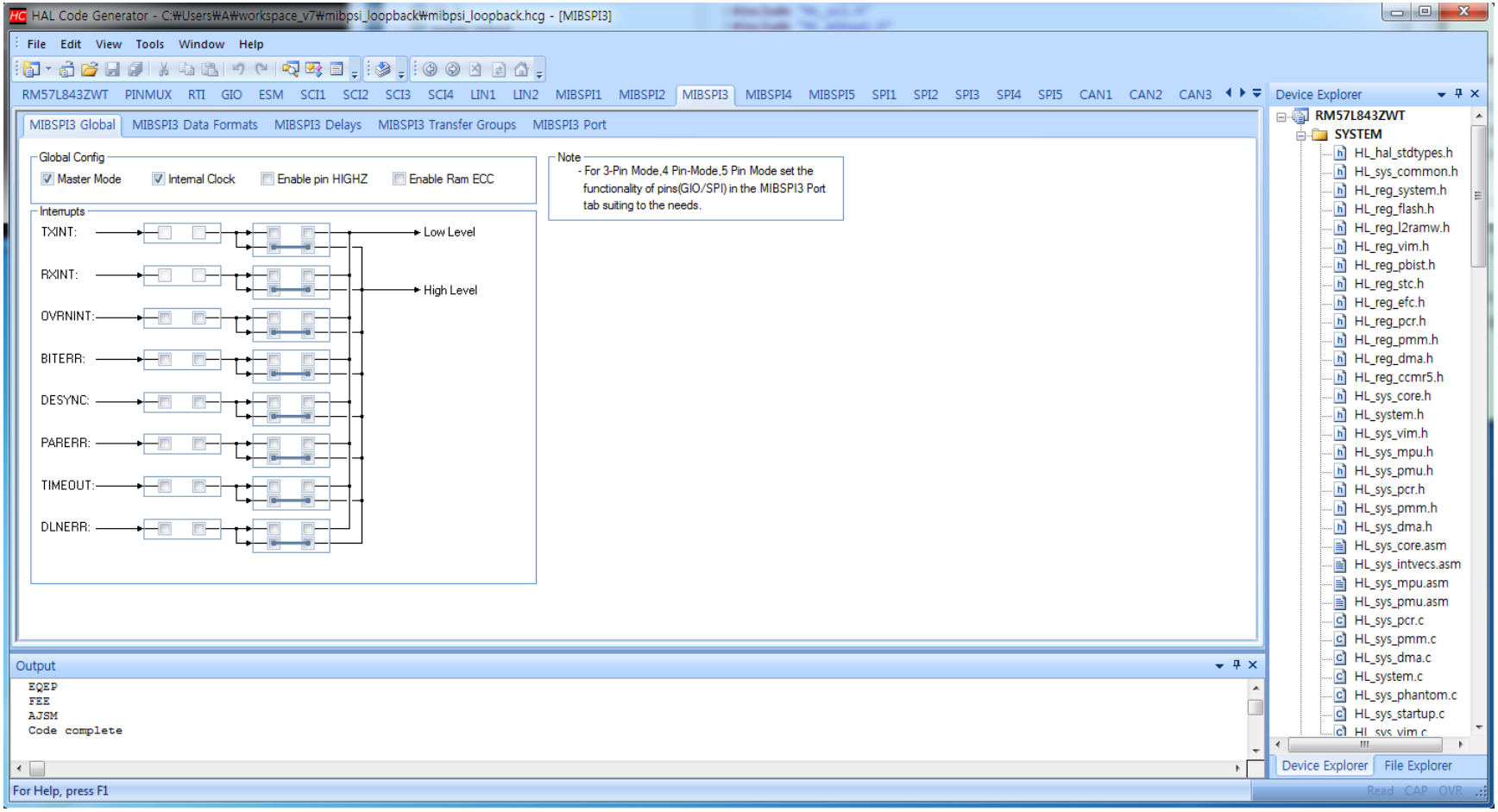
1.Halcogen Setting(Driver Enable)



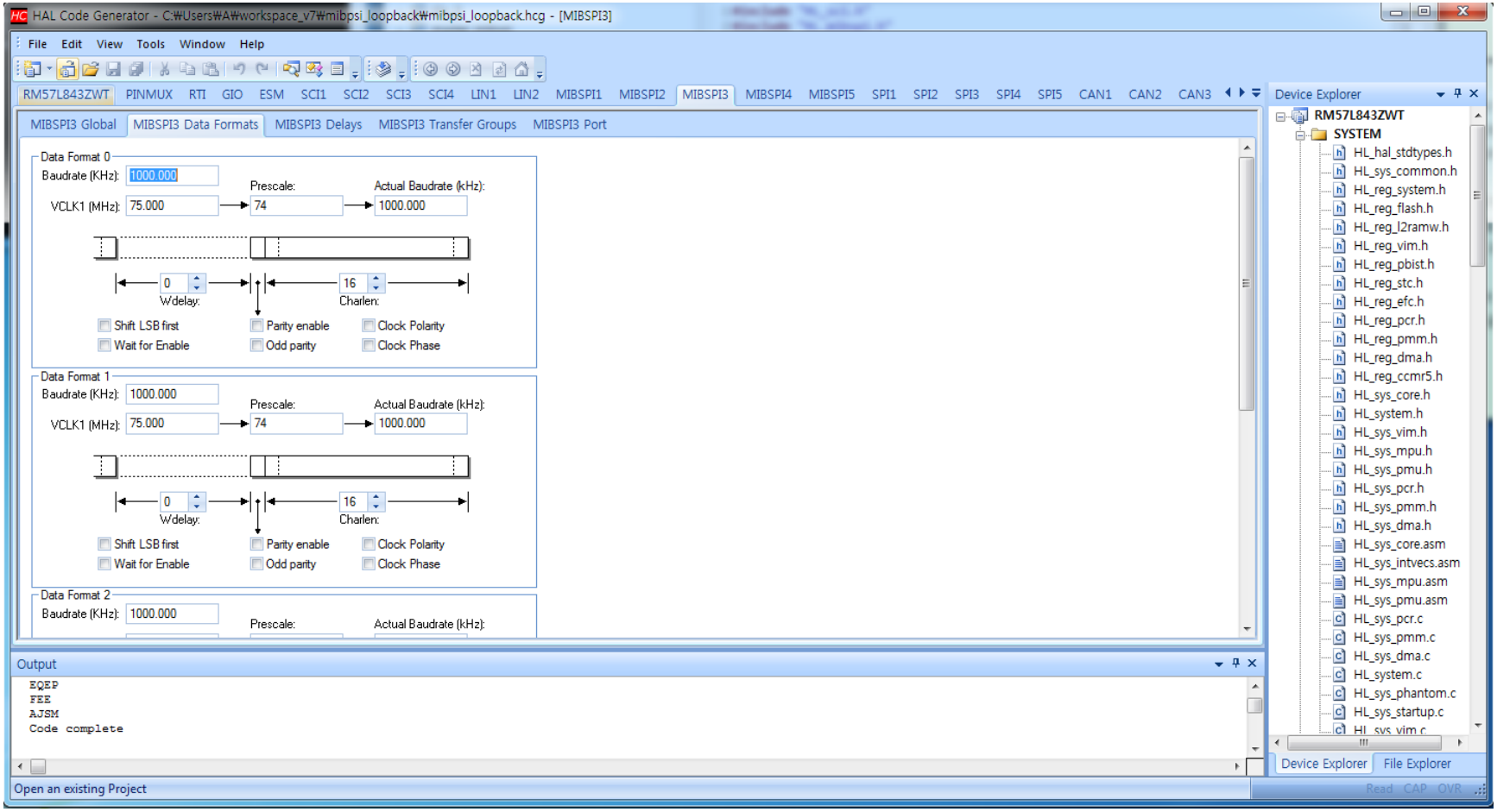
2.Halcogen Setting(VIM Channel)



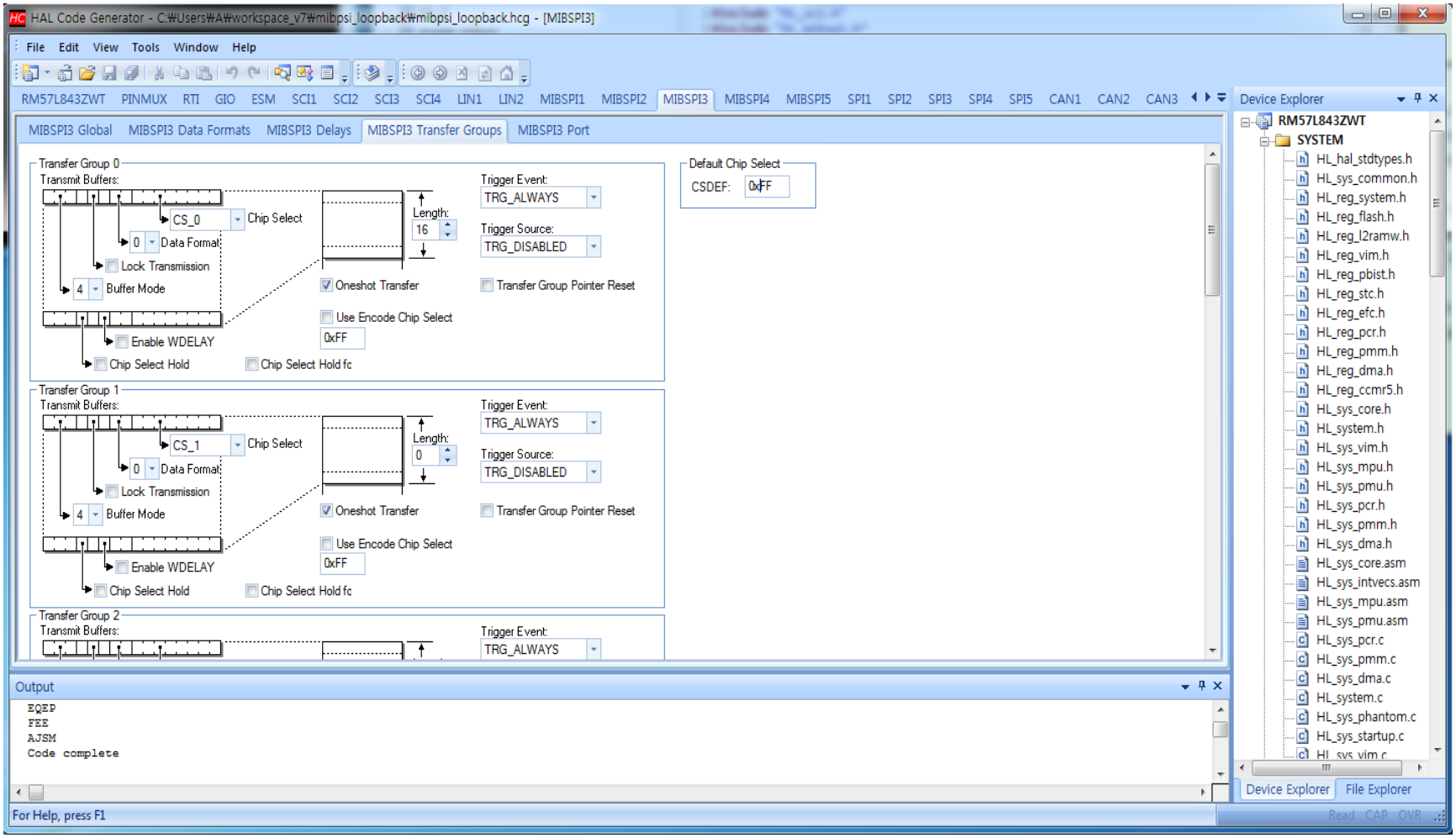
3.Halcogen Setting(Mibspi Global)



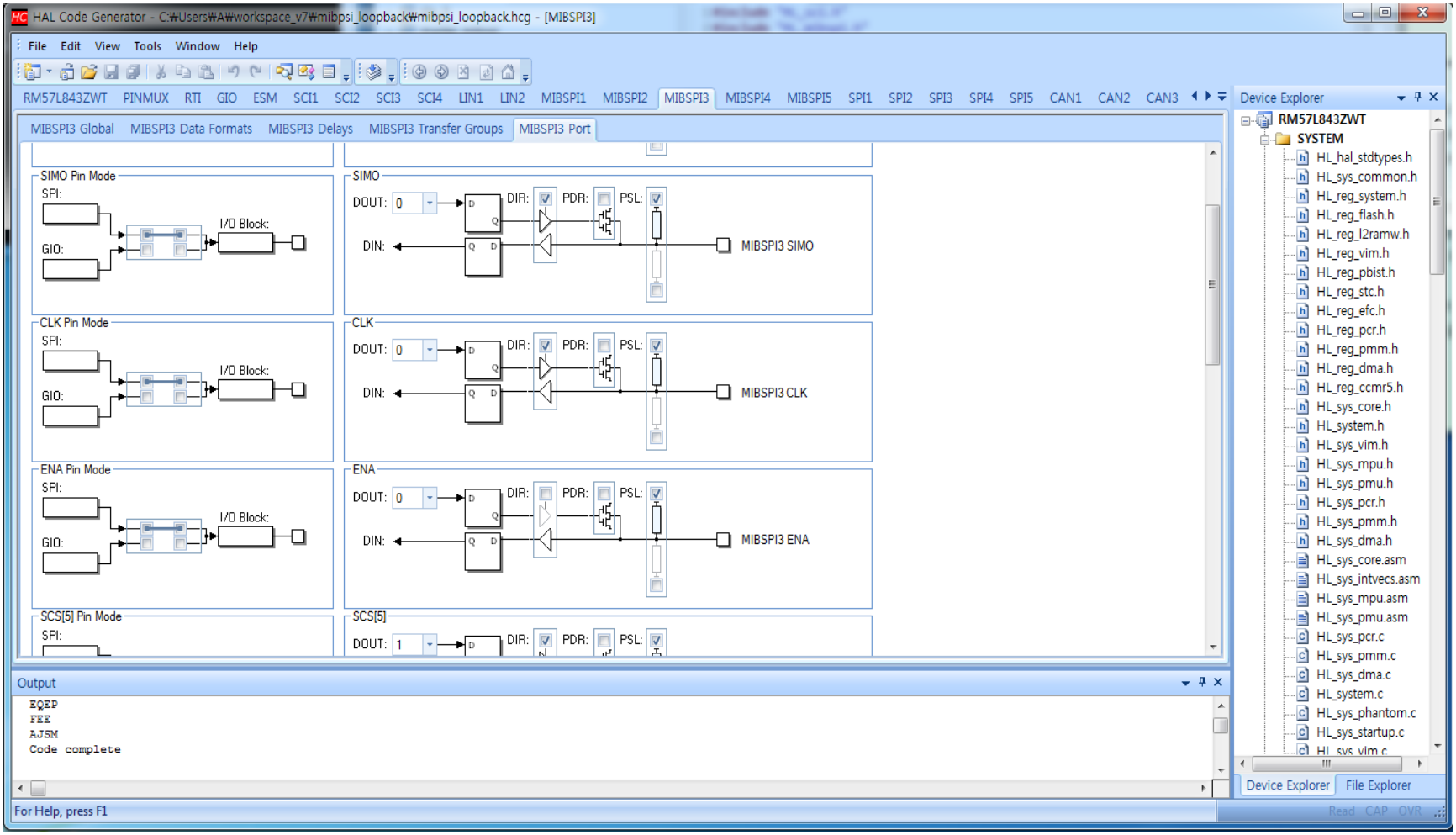
4.Halcogen Setting(Mibspi Data Formats)



5.Halcogen Setting(Mibspi Transfer Groups)



6.Halcogen Setting(Mibspi port)



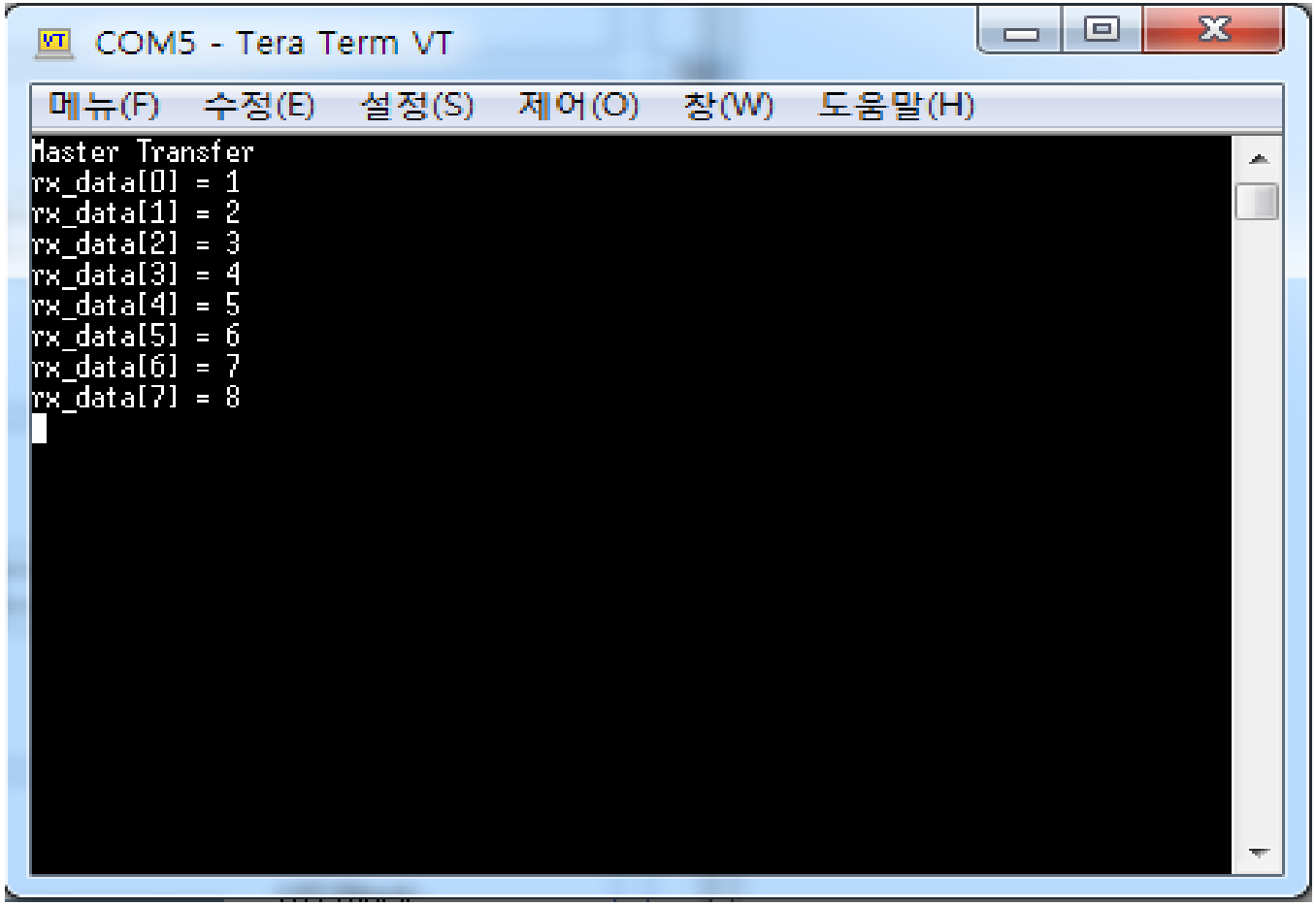
7.Code(Mibspi Loopback)

```c
#include "HL_sci.h"
#include "HL_mibspi.h"
#include "HL_sys_common.h"
#include "HL_system.h"
#include <stdio.h>
#include <string.h>

#define UART    sciREG1

uint16 tx_data[8] = {1,2,3,4,5,6,7,8};
uint16 rx_data[8] = {0};

void sciDisplayText(sciBASE_t *sci, uint8 *text, uint32 len);

void main(void)
{
    volatile i;
    char txt_buf[256];
    unsigned int buf_len;

    _enable_IRQ_interrupt_();

    sciInit();
    mibspiInit();

    mibspiEnableLoopback(mibspiREG3, Digital_Lbk);
    //mibspiEnableGroupNotification(mibspiREG3, 0, 1);
    mibspiSetData(mibspiREG3, 0, &tx_data[0]);
    mibspiTransfer(mibspiREG3, 0);
```

8.Code(Mibspi Loopback)

```
30
31      sprintf(txt_buf, "Master Transfer\n\r\0");
32      buf_len = strlen(txt_buf);
33      sciDisplayText(sciREG1, (uint8 *)txt_buf, buf_len);
34
35      while(!(mibspiIsTransferComplete(mibspiREG3, 0)))
36      {
37          ;
38      }
39      mibspiGetData(mibspiREG3, 0, &rx_data[0]);
40
41      for(i=0;i<8;i++)
42          {
43              sprintf(txt_buf, "rx_data[%d] = %d\n\r\0", i,(char)rx_data[i]);
44              buf_len = strlen(txt_buf);
45              sciDisplayText(sciREG1, (uint8 *)txt_buf, buf_len);
46          }
47
48      while(1);
49
50
51
52  }
53
```

9.Tera Term(Mibspi Loopback)



10.CCS(Expressions)