

Hrishikesh Shedekar

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OBJECTIVE

To work in a competitive and energetic setting that requires a high level of self-motivation and commitment, allowing me to effectively manage my own professional development and contribute my skills successfully.

EDUCATION

Pursuing B.E. in Electronics & Telecommunications Engg. *June 2015 – present*

Degree	College/ School	University	Passing Year	Pass Percentage
B.E.	Sardar Patel Institute of Technology	Mumbai University	2019	9.66 CGPA upto Semester 5
HSC	Mulund Vidya Mandir Jr, College	Maharashtra State Board	2015	92.15%
SSC	St. Xavier's High School	Maharashtra State Board	2013	94.36%

PROJECTS

- 1. Vehicular Accident prevention system based on Cognitive driving Distraction** *Feb, 18 – April, 18*
Worked as the Technical Head, to develop a system that works towards prevention of accidents caused mainly due to human errors by monitoring the driver's behavioural and physiological features like Drowsiness, Stress, Anxiety and influence of Alcohol and then takes preventive actions.
- 2. Collector Bot: e-Yantra Robotics Competition** *Oct, 17 – March, 18*
Worked on Image Processing using Python OpenCV, Path Planning and motion planning in V-Rep, implementing PID algorithm and Robot Structure design. The theme of this project is to detect and collect fruits in shortest possible time, using efficient paths to avoid collisions with obstacles.
- 3. High-Resolution Microstepping Controller** *Dec, 2017 – Jan, 2018*
This project involves designing a High-resolution Microstepping Controller to implement Microstepping which is a way of moving the stator flux of a stepper more smoothly than in full- or half-step drive modes, used to achieve higher resolution or smoother motion at low speeds.

4. **4G Technology Based Data Acquisition System** *Aug, 2017 – Nov, 2017*
The system involves a SIM7100 4G module interfaced to an Atmega8535 Microcontroller, where the current output of a remote system is measured using a Hall effect based sensor and this data is logged & stored in an SD card, and the efficiency of the Machines is calculated and relevant SMS alerts are sent using 4G technology.
5. **e-Nirogya : an IoT based Health Care System** *Jan, 2017 – July, 2017*
e-Nirogya is a remote health monitoring system for Rural Areas which uses a Raspberry Pi as the Central processor to collect Critical Health parameter data for remote diagnosis of diseases and immediate medical consultation from Urban Doctors.
Worked as the Technical Head, developed and implemented the Hardware System and mechanism for uploading data from remote locations and performing Online Data Analytics.

TRAINING & INTERNSHIPS

- Winter **Internship** at TATA INSTITUTE OF FUNDAMENTAL RESEARCH (TIFR) *Duration: 1 Month (Dec,17 - Jan,18)*
Worked as an intern at ECR lab, Department of Nuclear and Atomic Physics, TIFR
- **Embedded Systems Design Training Course** *Duration: 3 Months (Nov,16 - Jan,17)*
Worked on AVR, ARM, MSP based micro controllers systems and interfacing these with wireless communication modules, ESP-8266, SIM908

TECHNICAL SKILLS

1. **Programming Languages** : C | Embedded C | Python | JAVA (Basics)
2. **Softwares**: AVR Studio | V-Rep | XCTU | MATLAB | Keil | Eagle PCB Design | Linux | AutoCAD | Cisco Packet Tracer | Scilab | PSPICE

SOFT SKILLS

- Good Communication Skills
- Leadership & Teamwork
- Time Management
- Creativity & Adaptability

EXTRA-CURRICULAR ACTIVITIES

1. Organized an event **TechXplosion** for College Technical Festival- MATRIX 2017.
2. Participated in **Cleanliness Drive** at **Juhu Beach** organised by SPIT and OYE Organization in March 2017.
3. Contributed in the **Blood Donation Drive** organized by the **Rotary DG Goenka Blood Bank**.

CO-CURRICULAR ACTIVITIES

- Completed a course on **EMBEDDED SYSTEMS** conducted by Dr. Y.S.Rao. *(May,2017)*
- Attended Workshop on **Microstrip Patch Antenna Design** using IE3D & Testing. *(Oct,2016)*
- Participated in MSP-FPGA software and hardware co-design workshop. *(Sept,2016)*