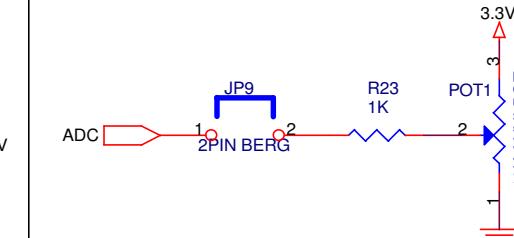


Circuit diagram showing three pins labeled P1, P2, and P3. Pin P1 is connected to +5V. Pin P2 is connected to GND through a diode. Pin P3 is connected to 3.3V.

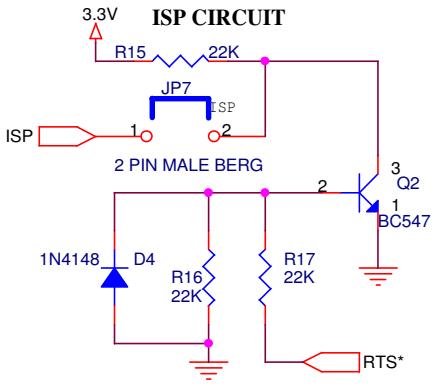
3 PIN POWERJACK

JP5 : BY DEFAULT : CLOSED

INTERNAL ADC (WITH
JP9 CLOSED)

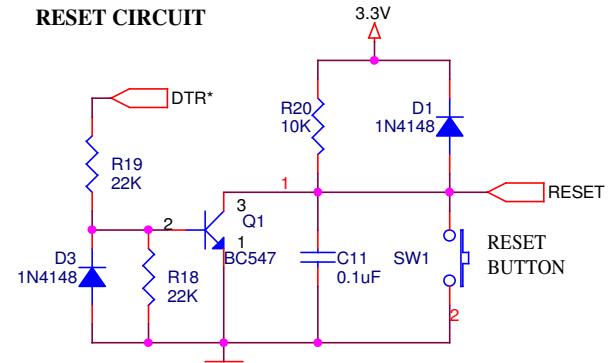


EXT INT0 for LPC2148 EXT INT3 for LPC1768

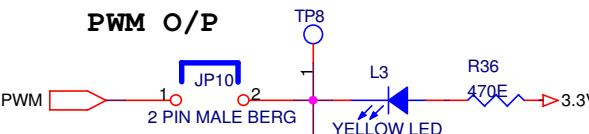


SW11 OF PINS 1 & 2 SHOULD BE ON FOR ISP & RESET

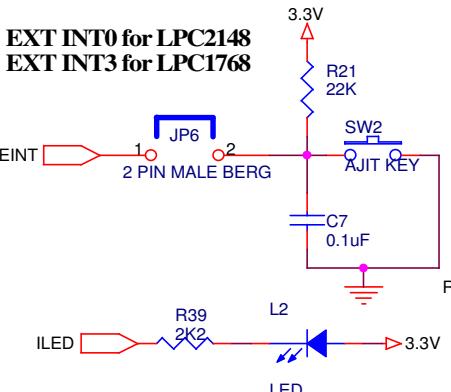
RESET CIRCUIT



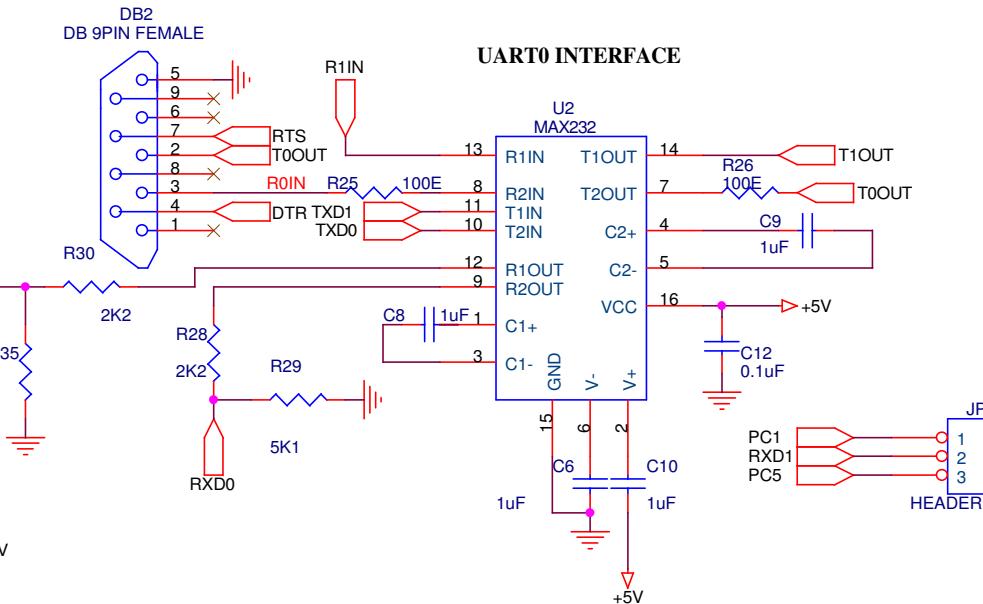
PWM O/P



R_2
22K



UART0 INTERFACE



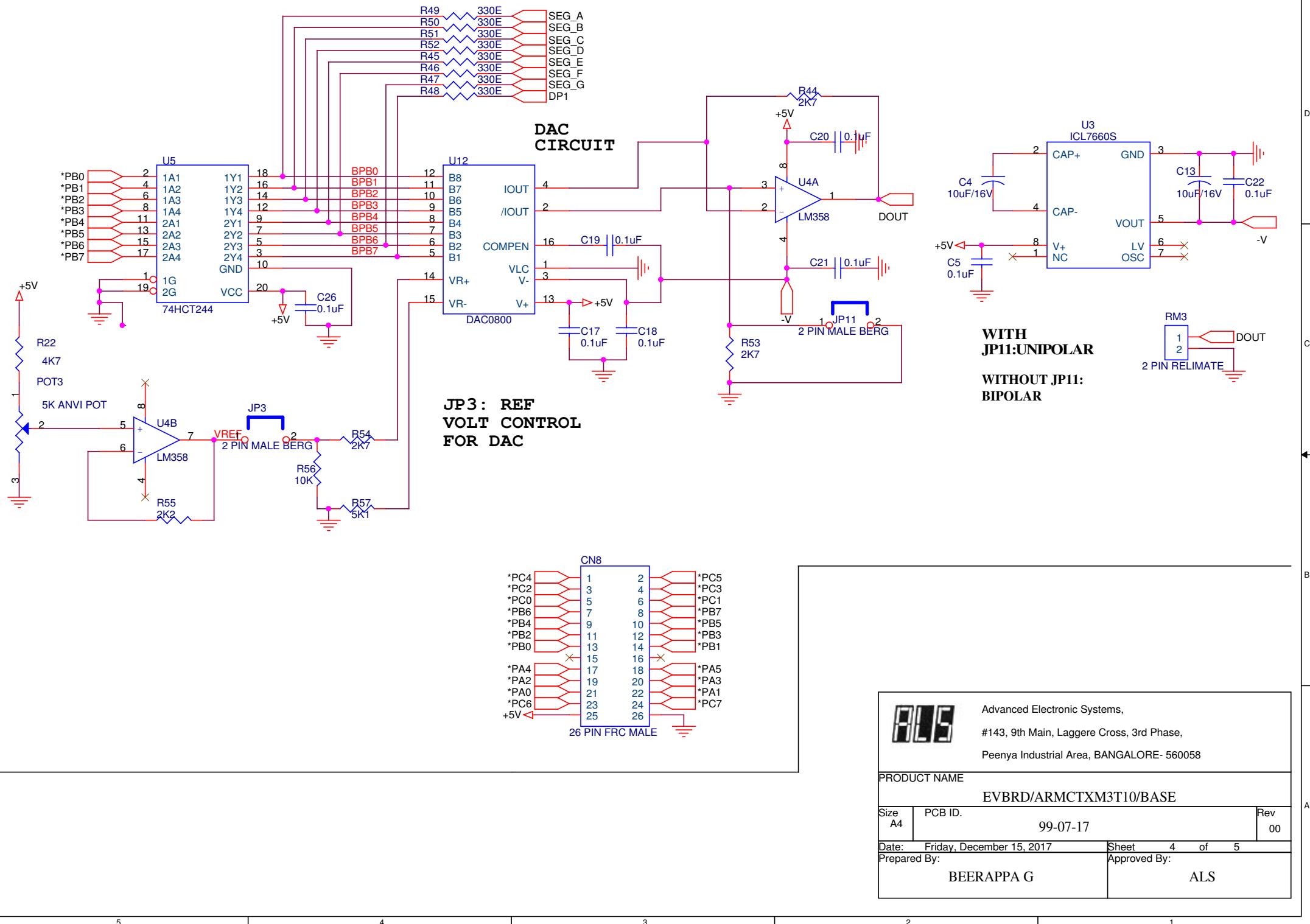
Advanced Electronic Systems,
#143, 9th Main, Laggere Cross, 3rd Phase,
Peenya Industrial Area, BANGALORE- 560058

PRODUCT NAME

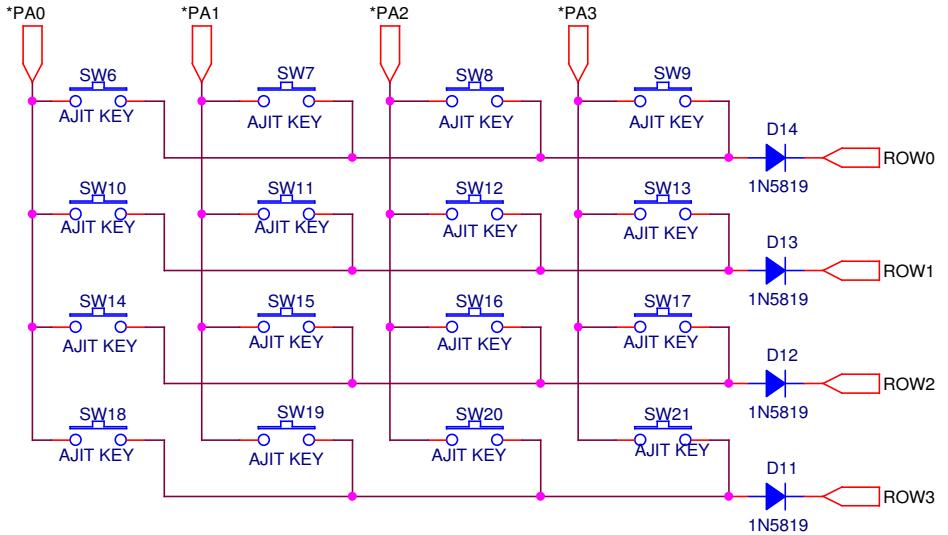
EVBRD/ARMCTXM3T10/BASE

PCB ID.		Rev
A4	99-07-17	00
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Prepared By:	Approved By:	

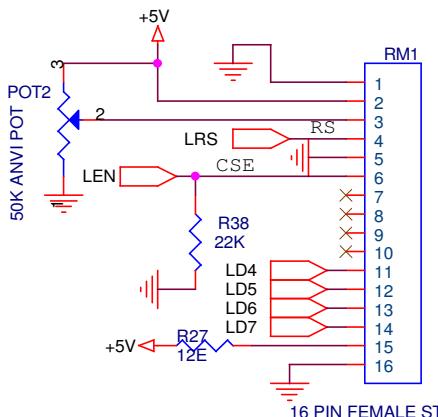
ERAPPA G



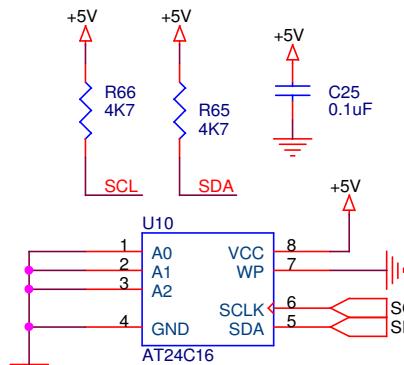
KEYPAD CIRCUIT



LCD

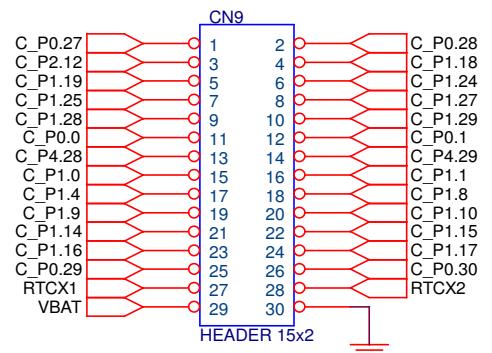
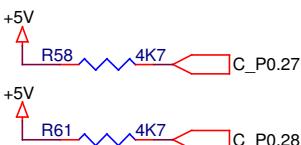
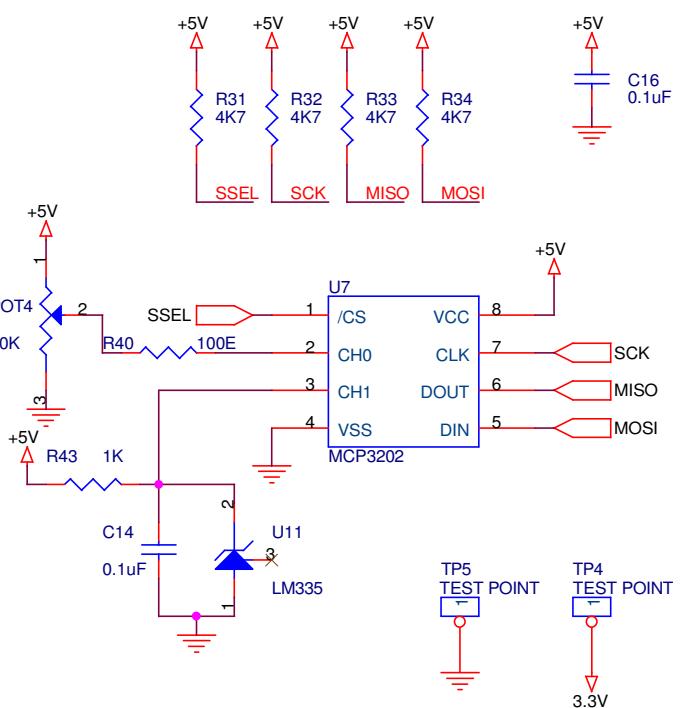


I2C CIRCUIT



FREE LINES OF LPC1768

SPI CIRCUIT



Advanced Electronic Systems,
#143, 9th Main, Laggere Cross, 3rd Phase,
Peenya Industrial Area, BANGALORE- 560058

PRODUCT NAME

EVBRD/ARMCTXM3T10/BASE

Size	PCB ID.	Rev
A4	99-07-17	00

Date: Friday, December 15, 2017 Sheet 5 of 5
Prepared By: Approved By:

BEERAPPA G

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