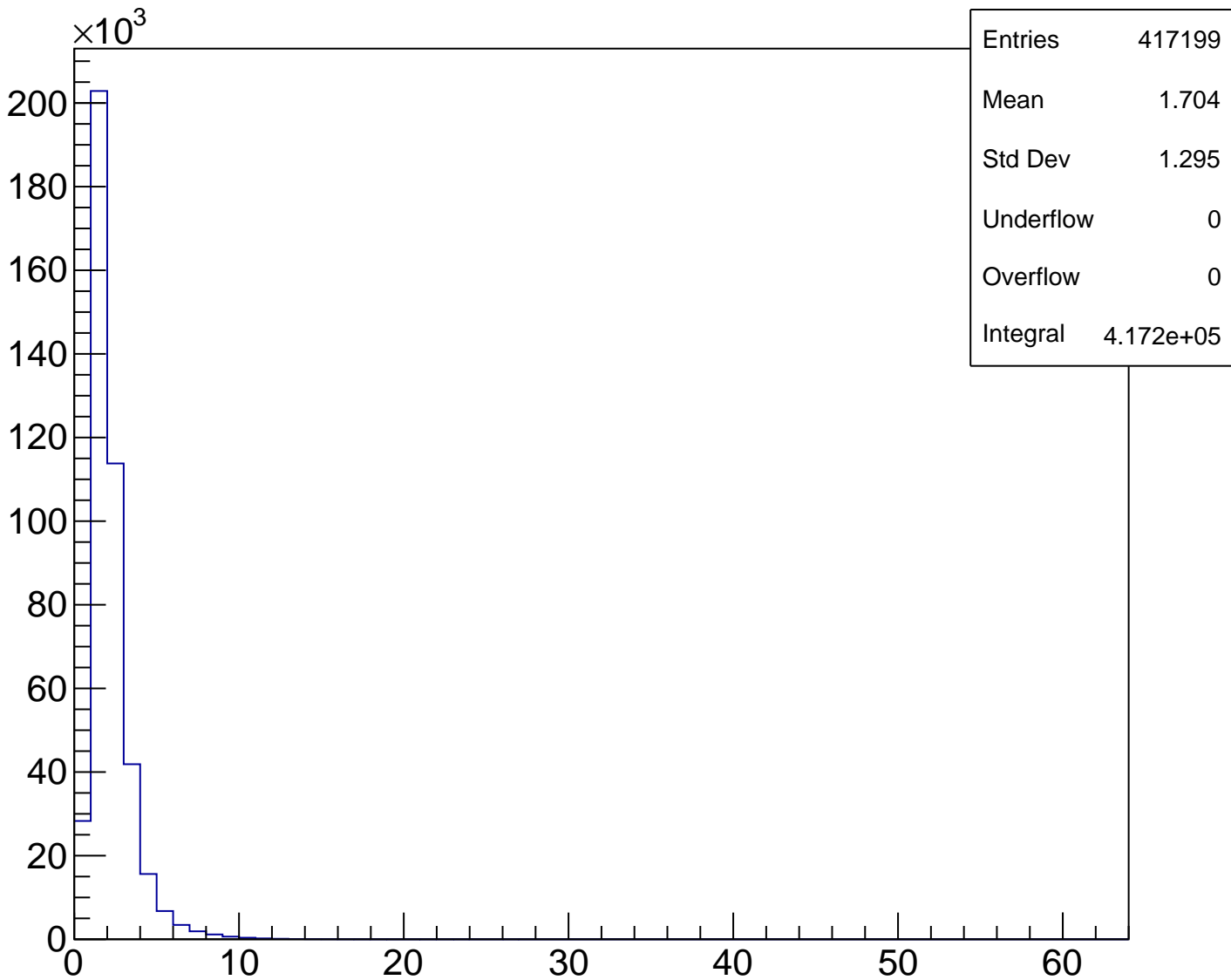
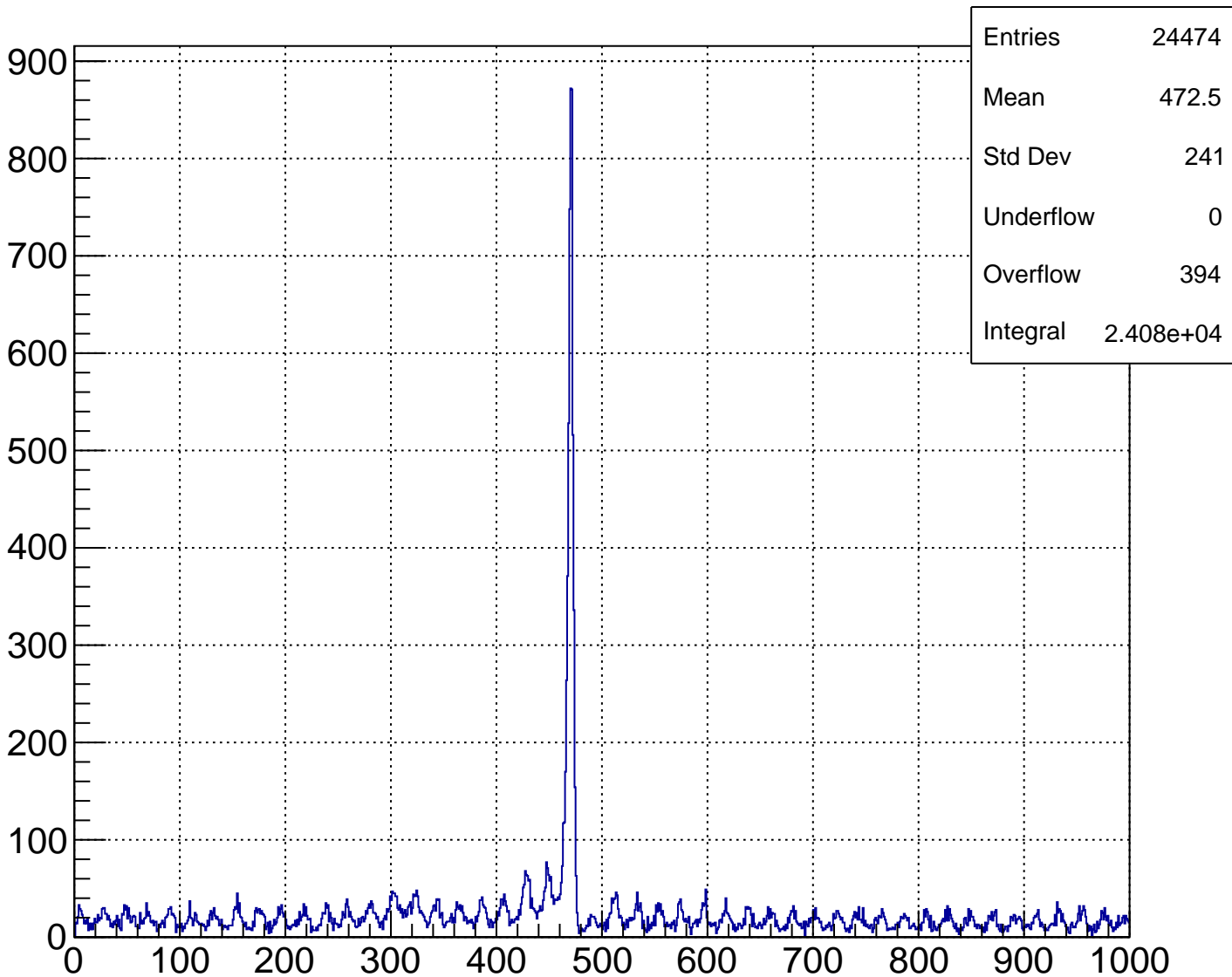


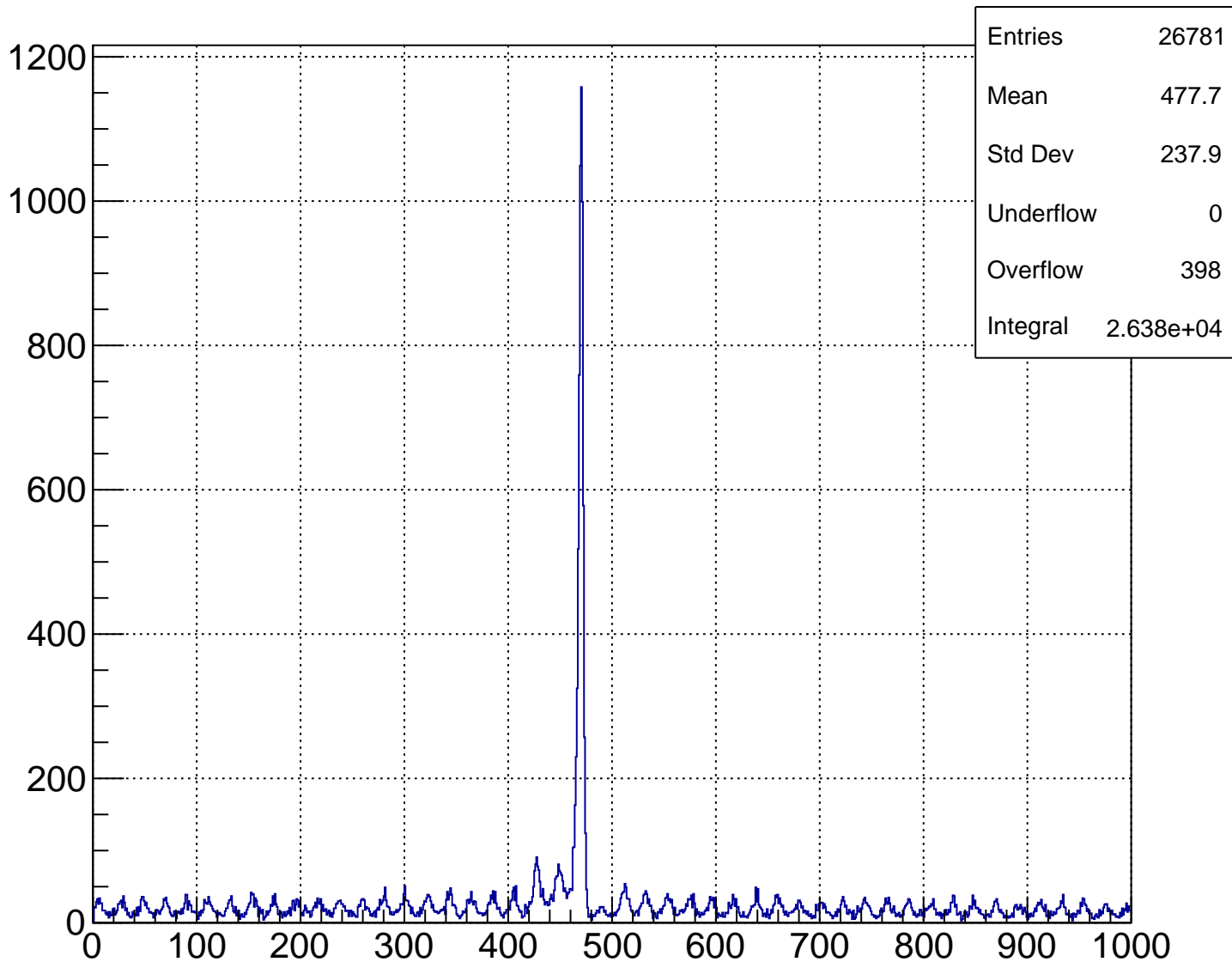
# SCH Nhits



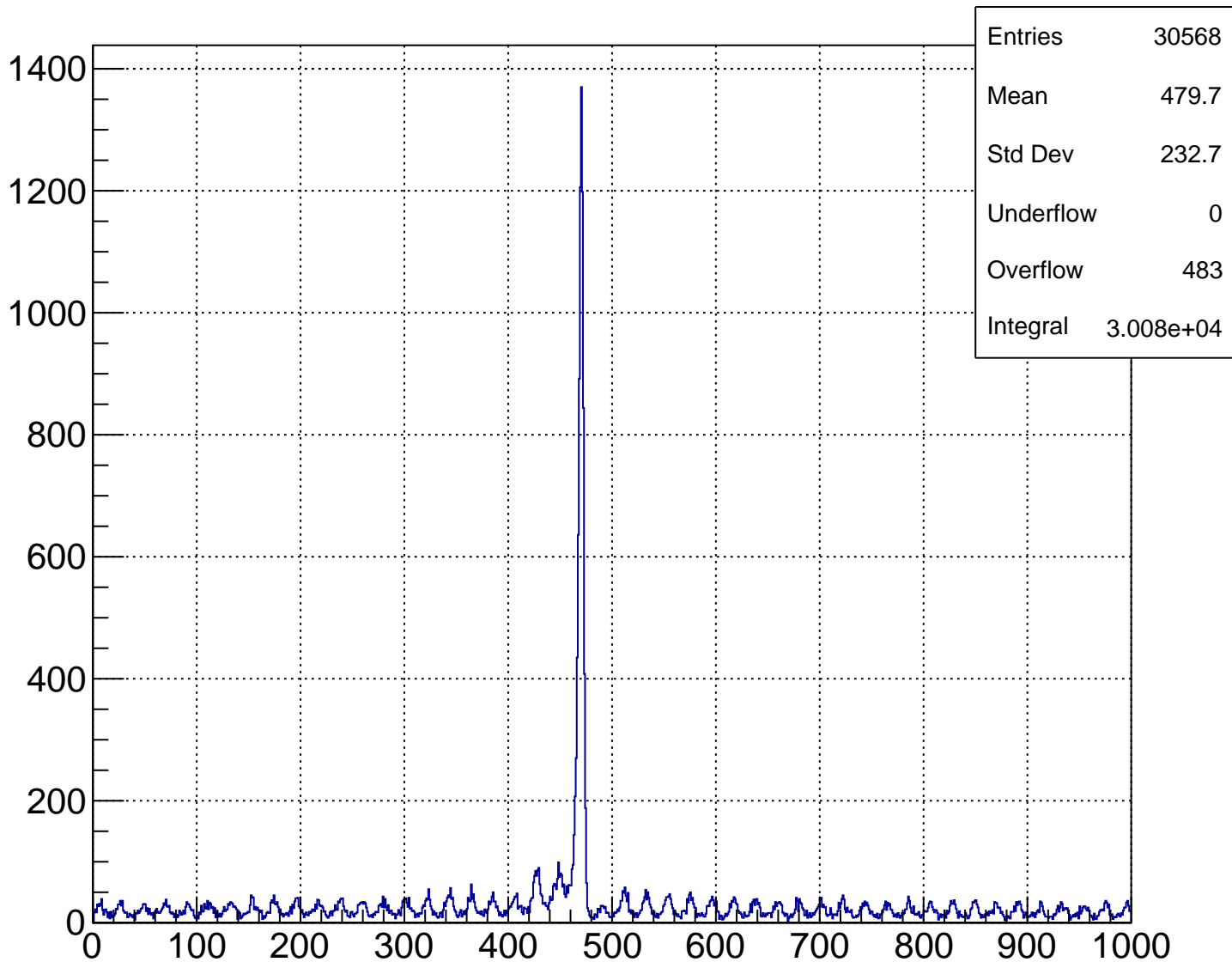
# SCH Tdc 1



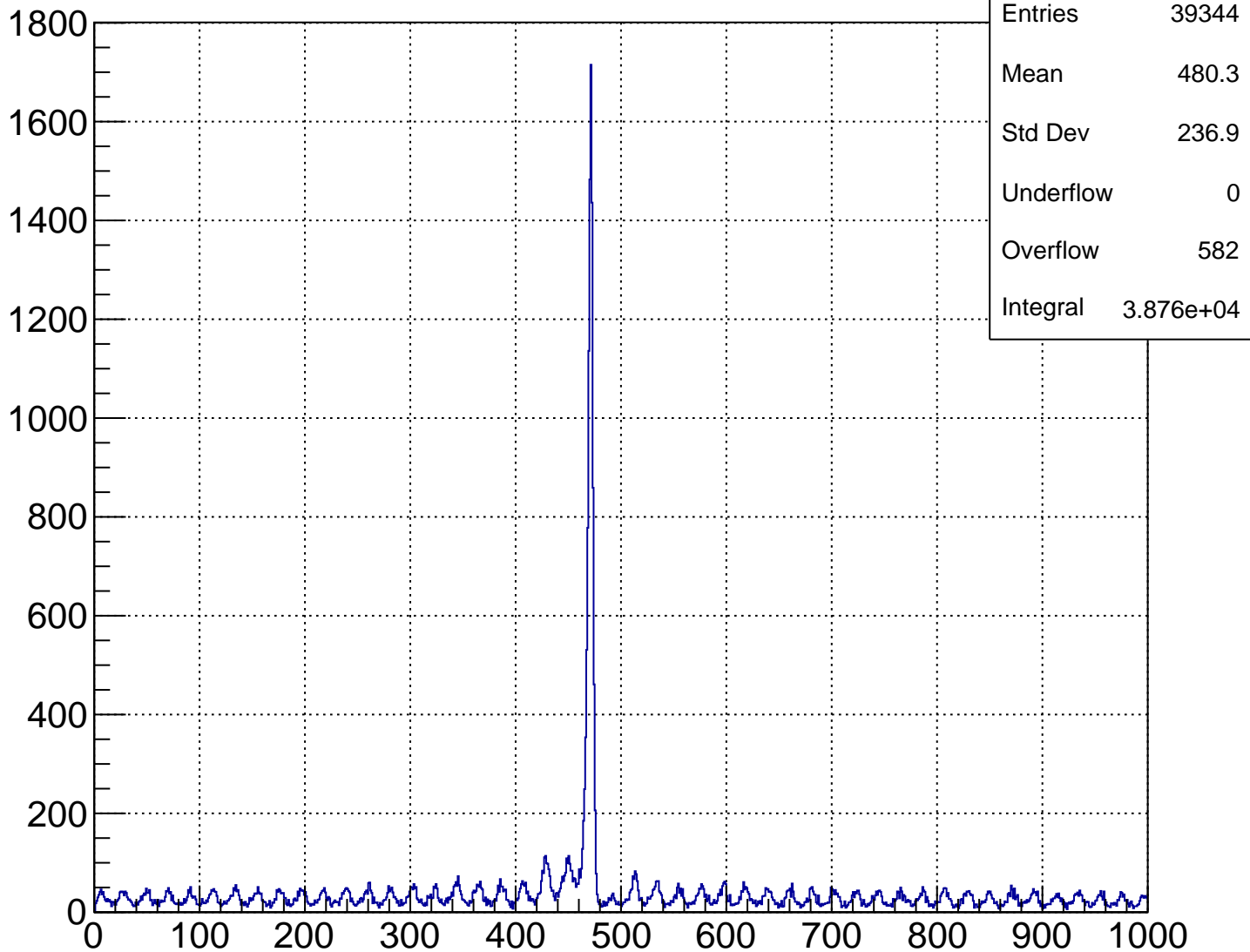
# SCH Tdc 2



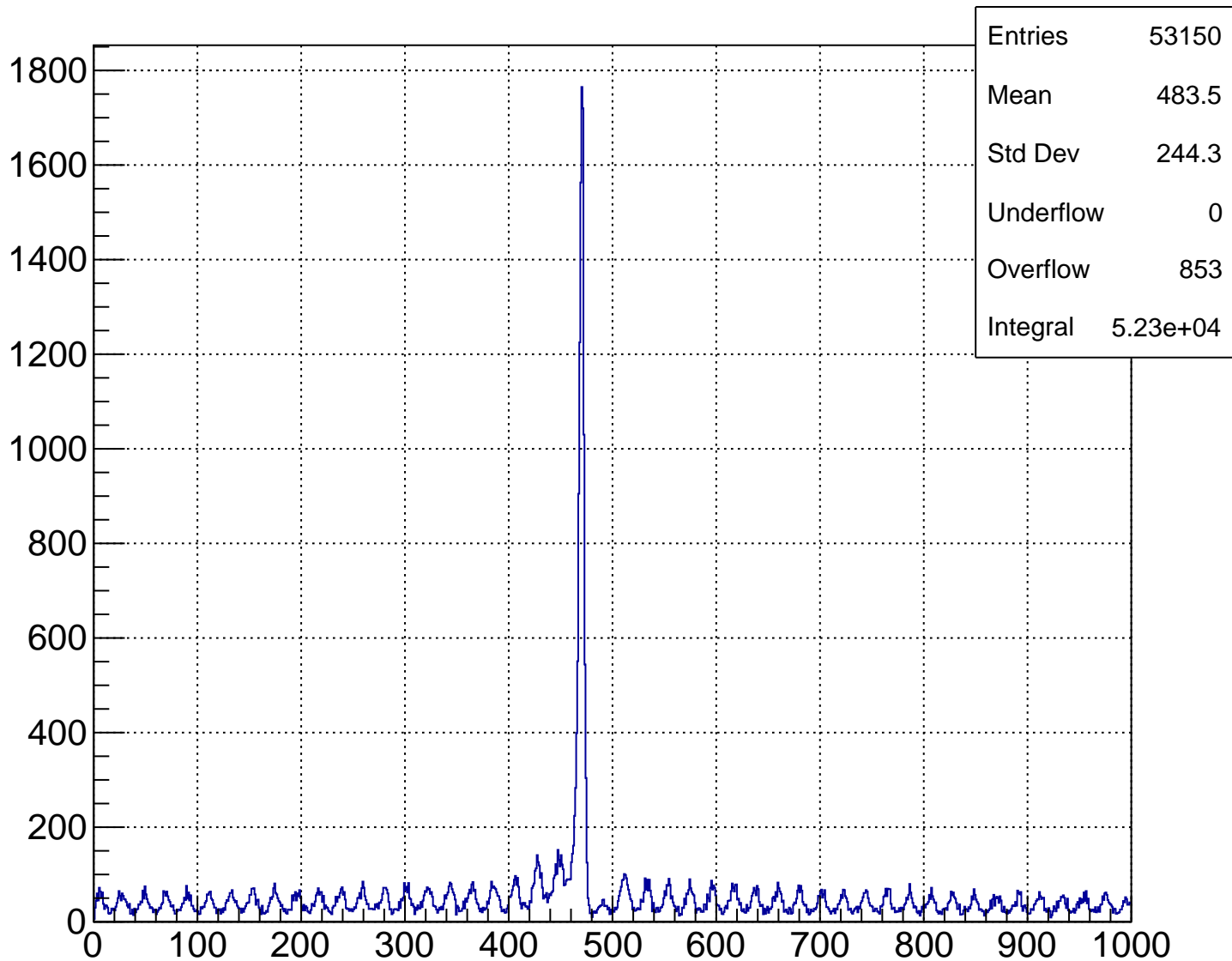
# SCH Tdc 3



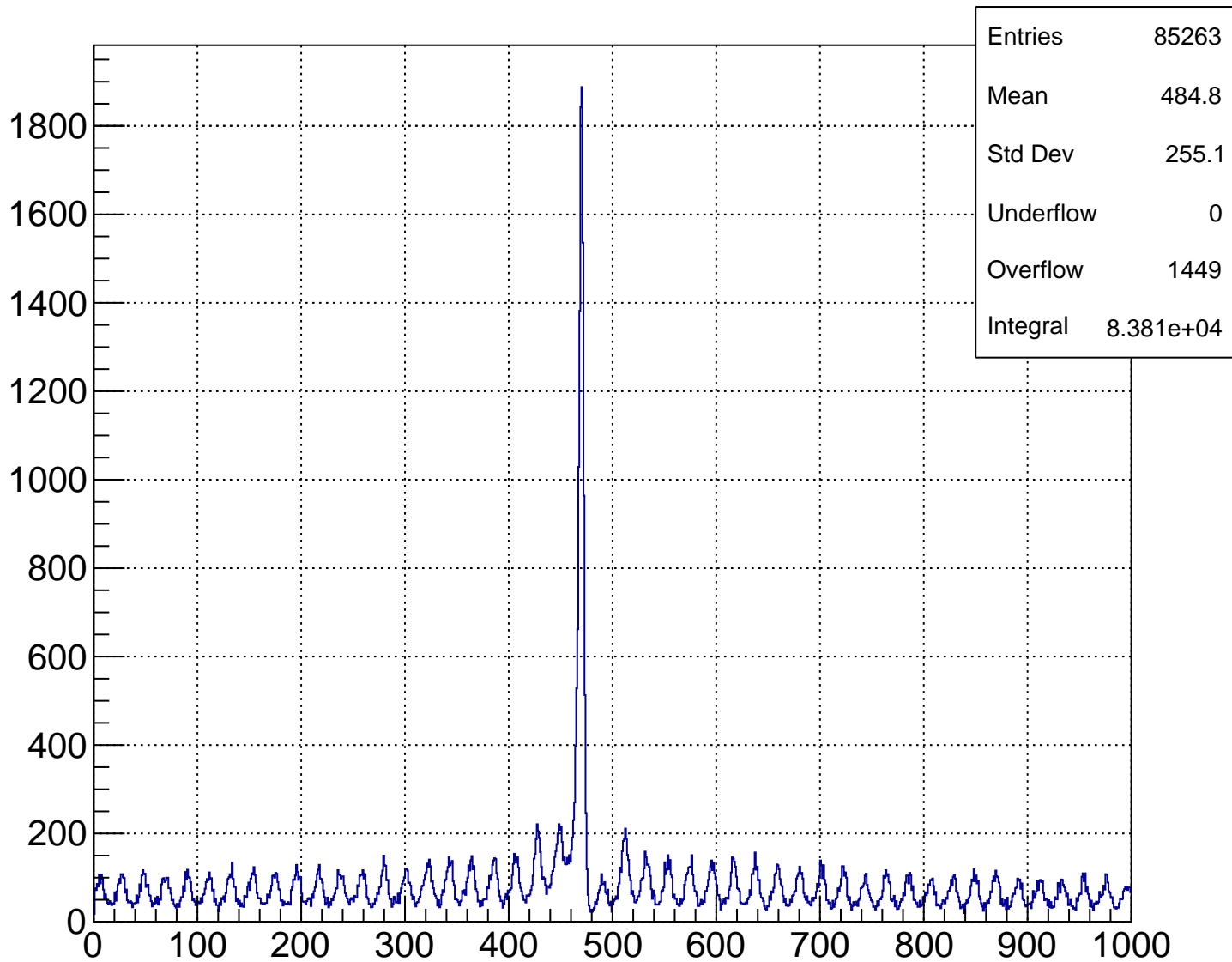
# SCH Tdc 4



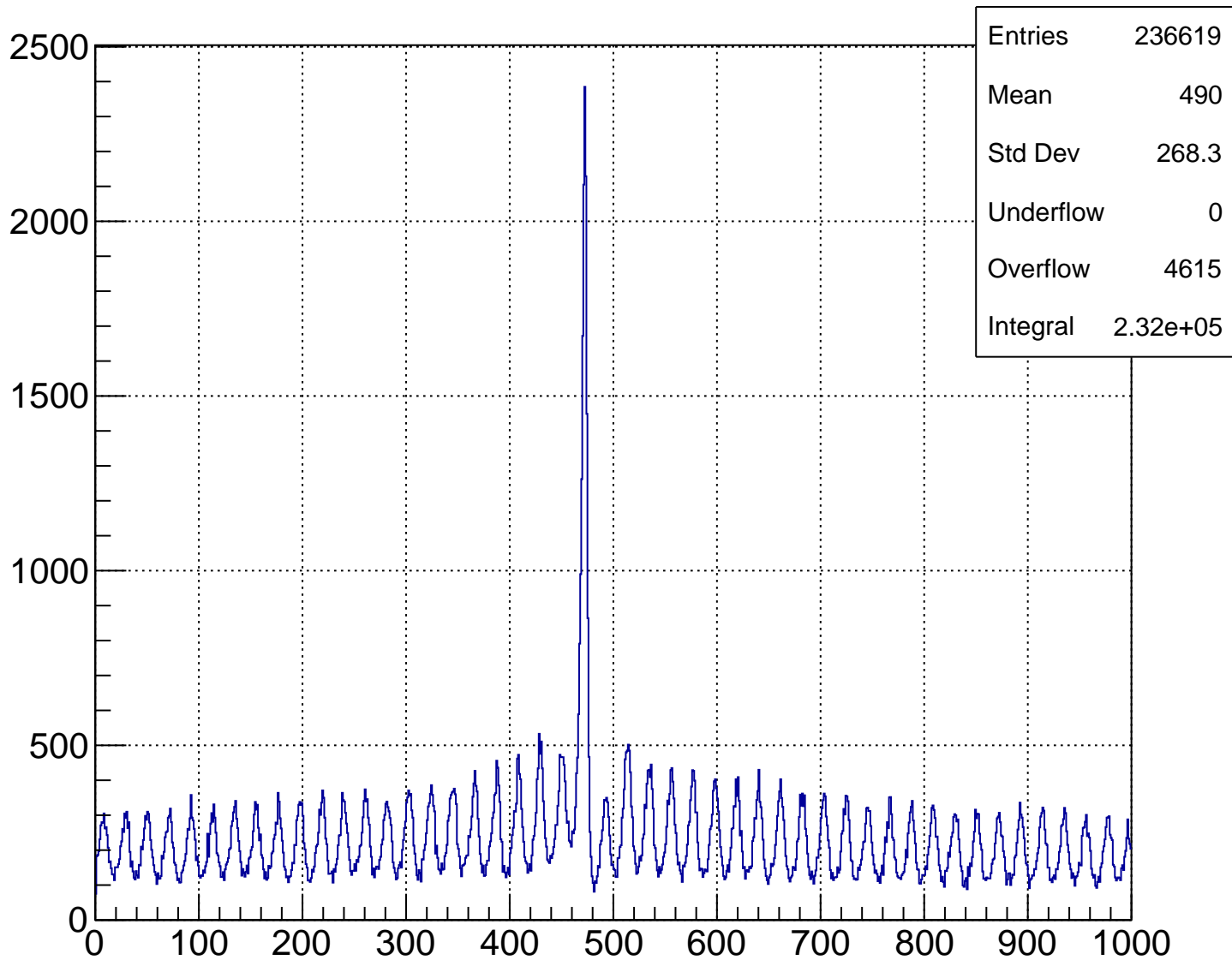
# SCH Tdc 5



# SCH Tdc 6

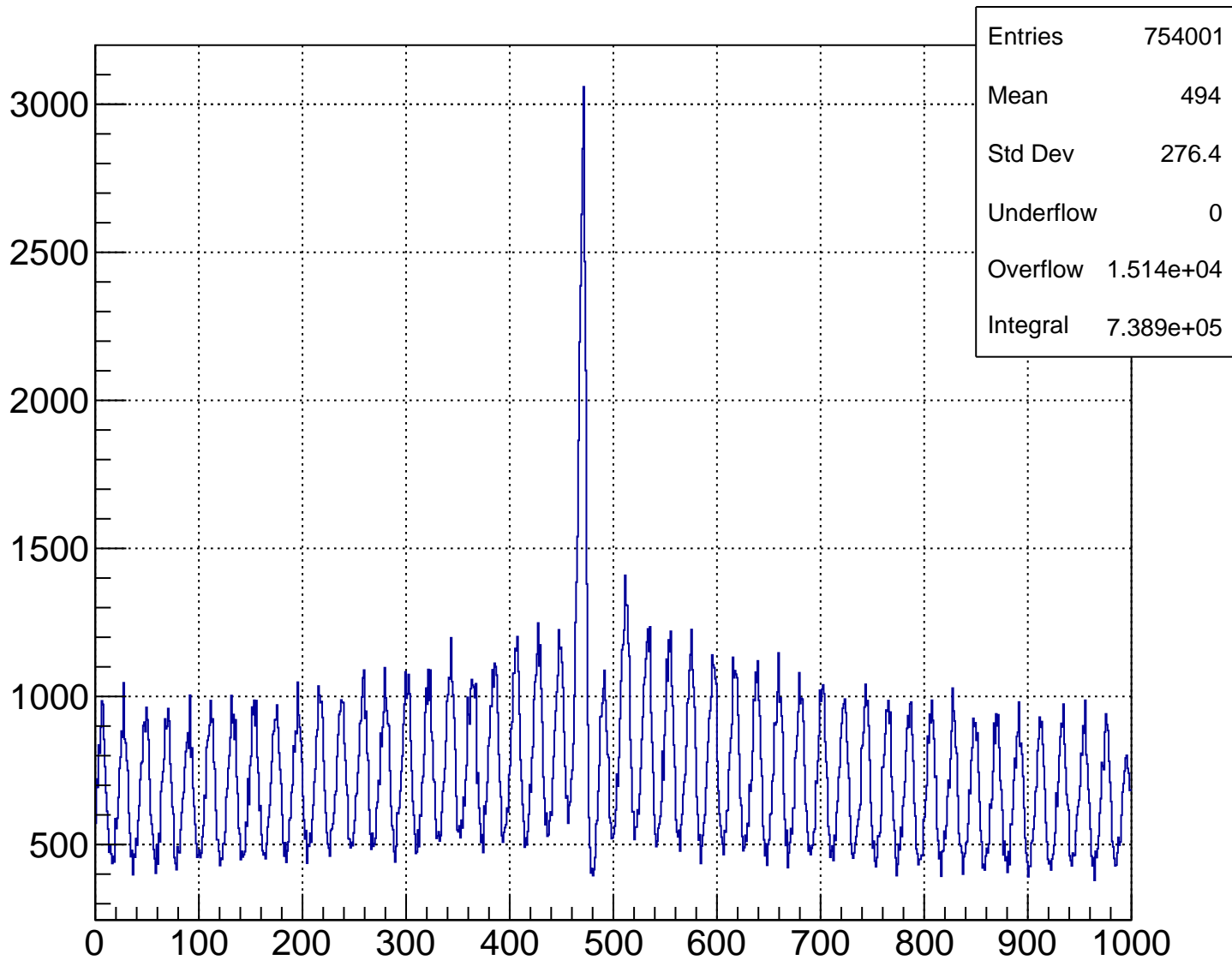


# SCH Tdc 7

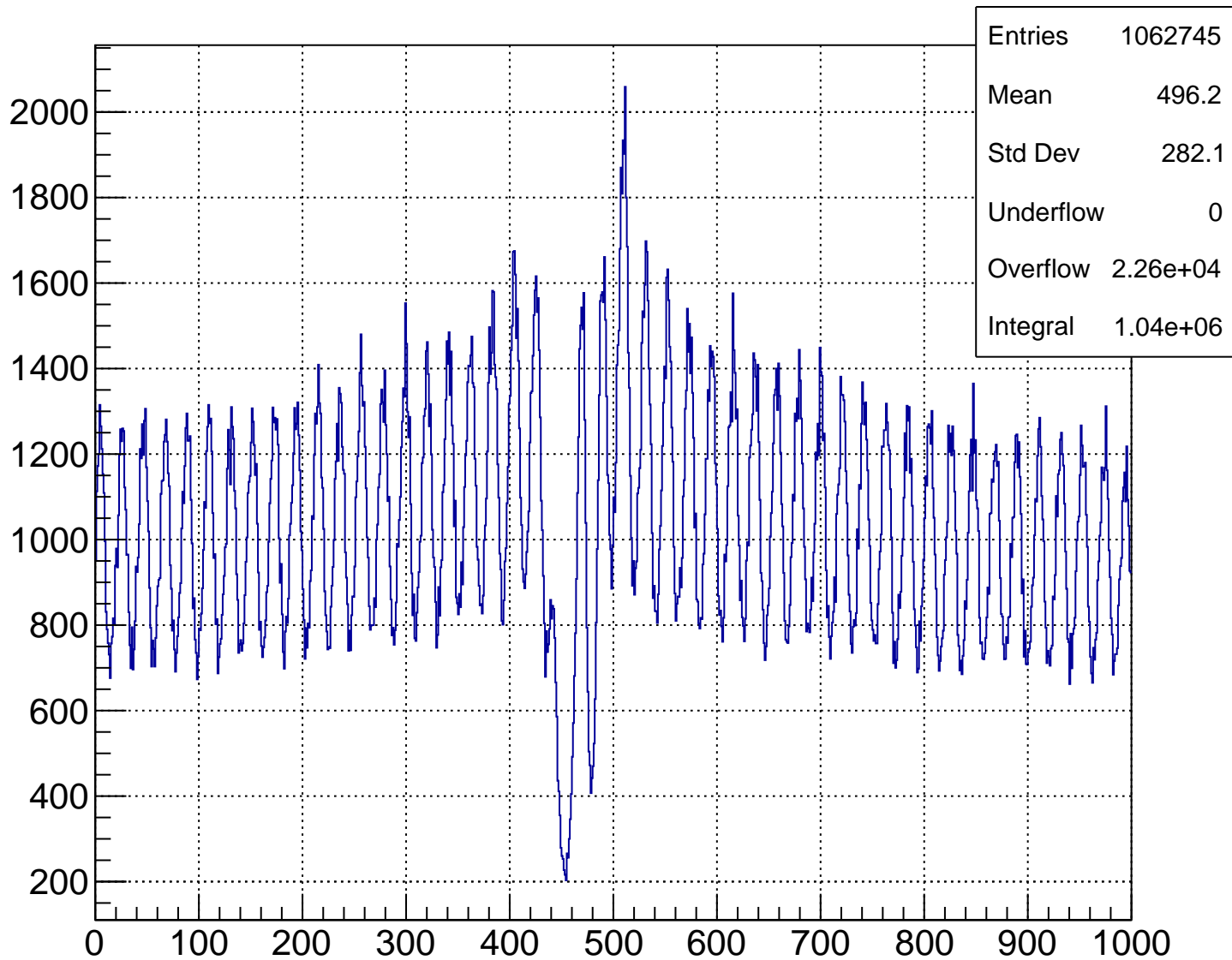




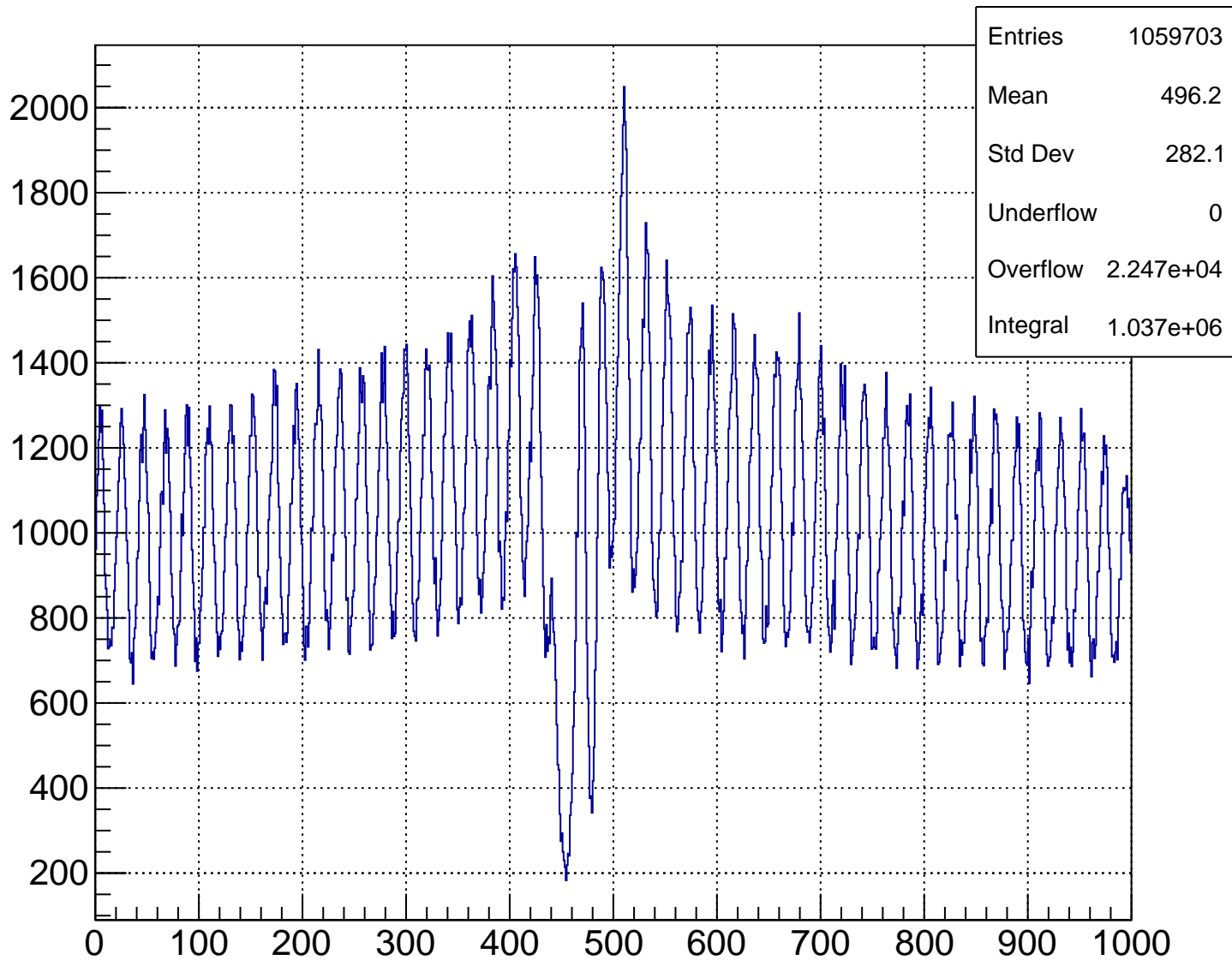
# SCH Tdc 8



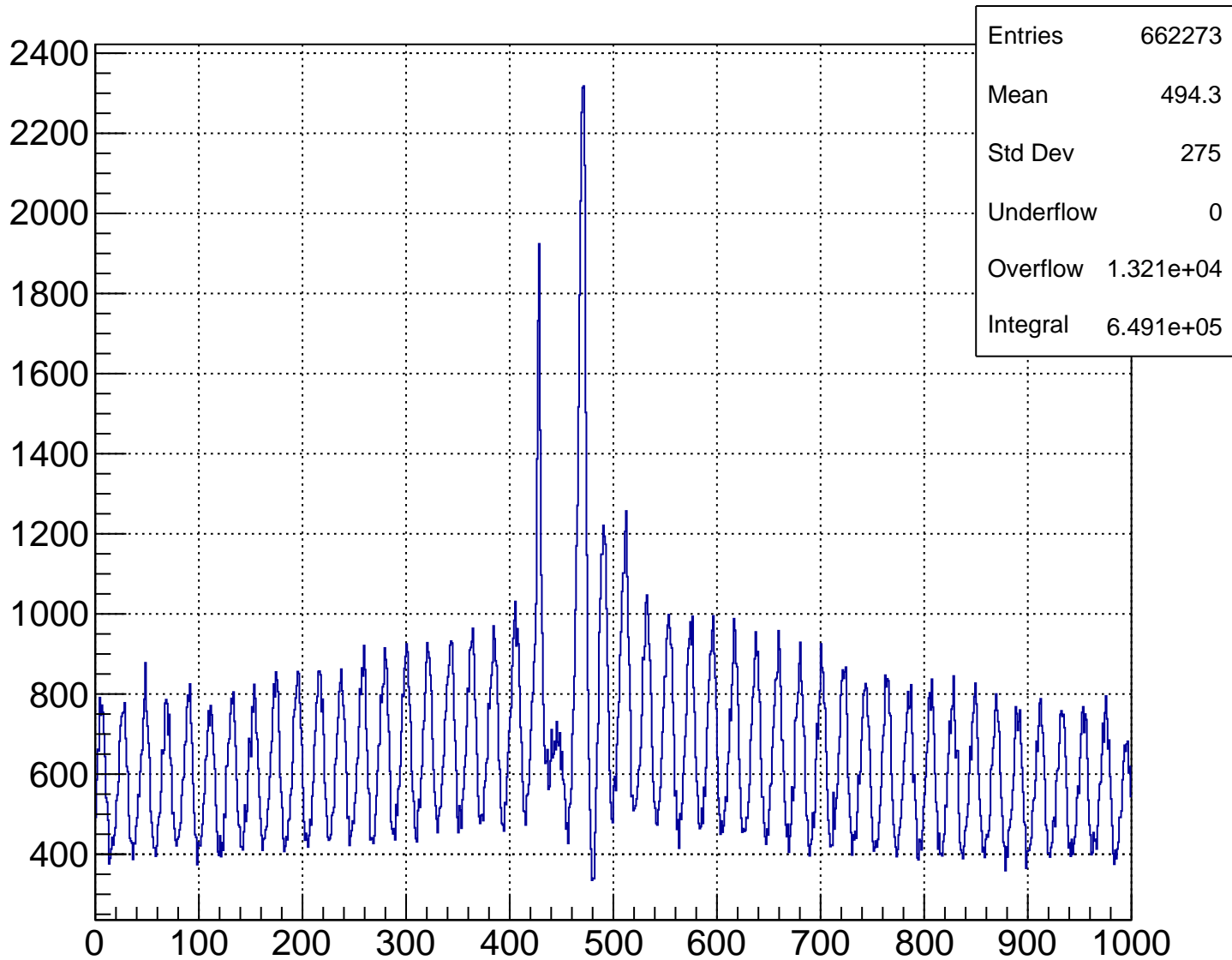
# SCH Tdc 9



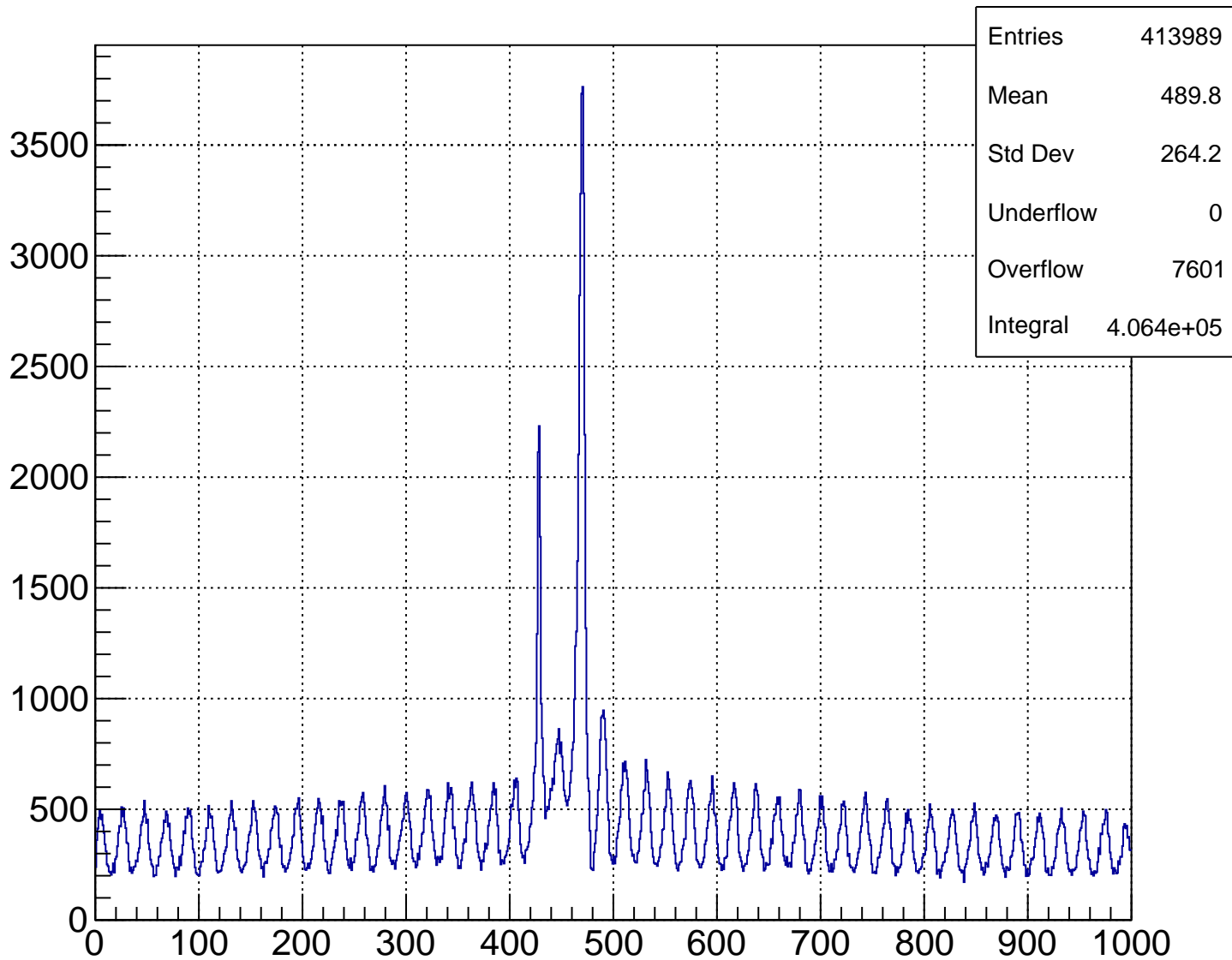
# SCH Tdc 10



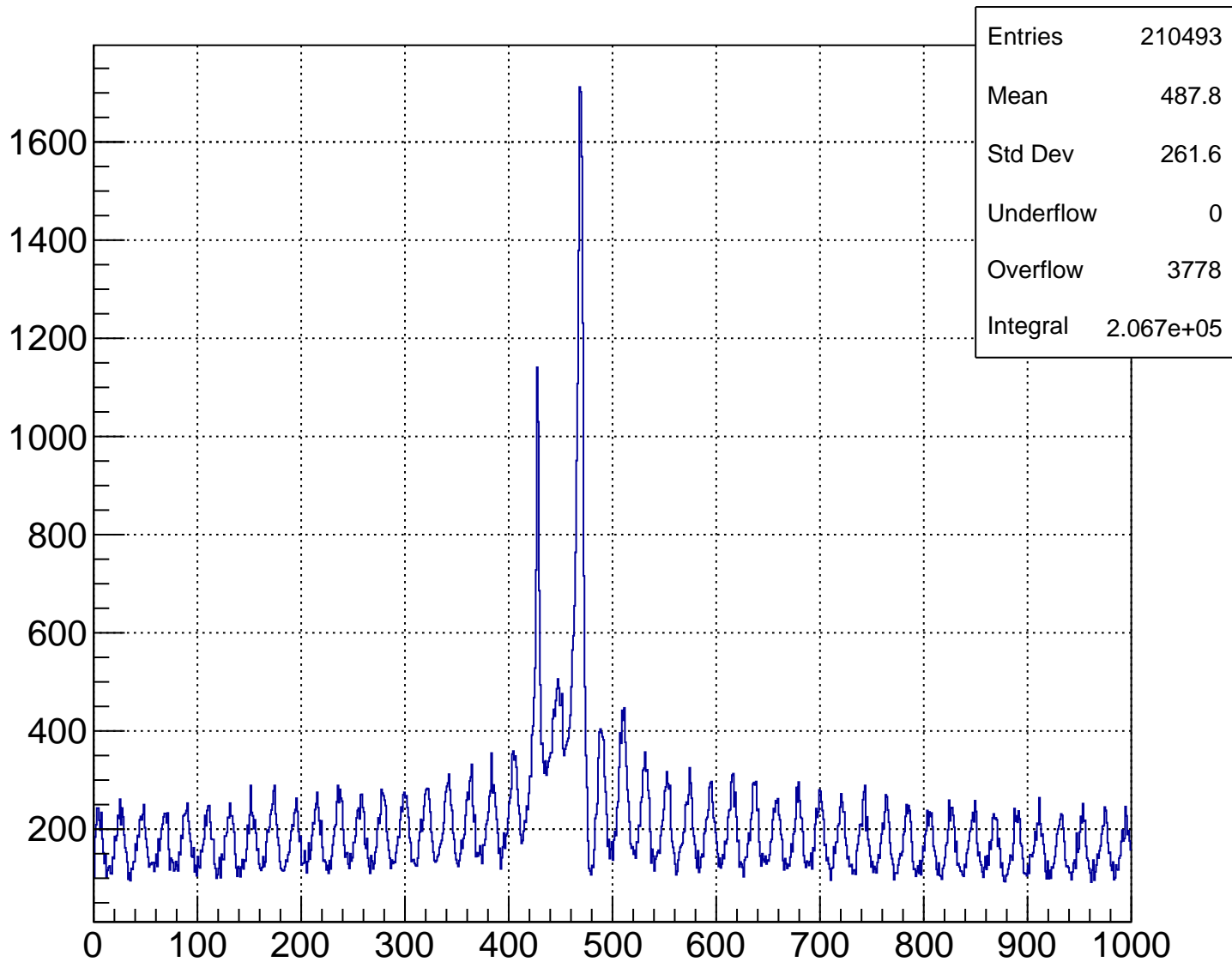
# SCH Tdc 11



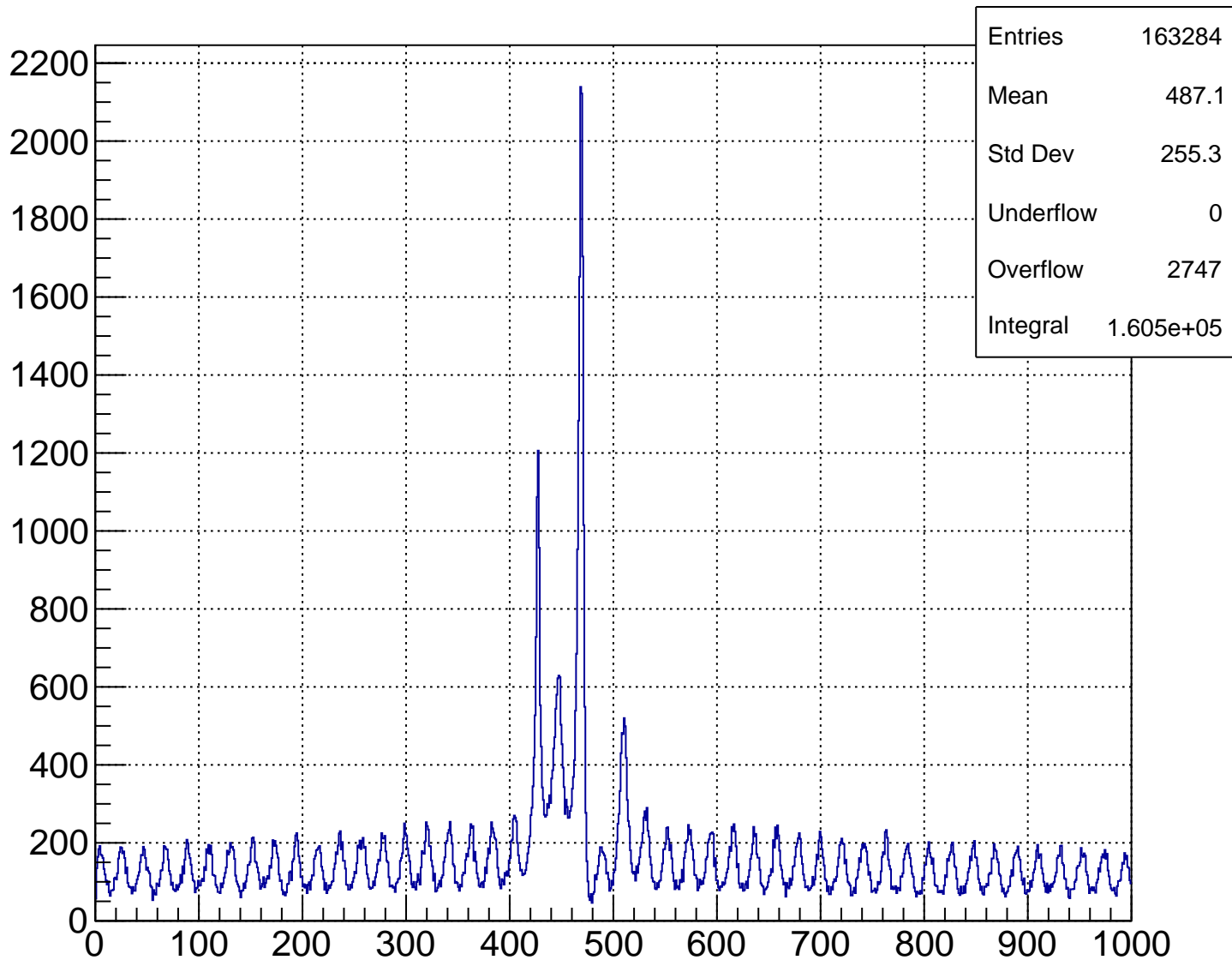
# SCH Tdc 12



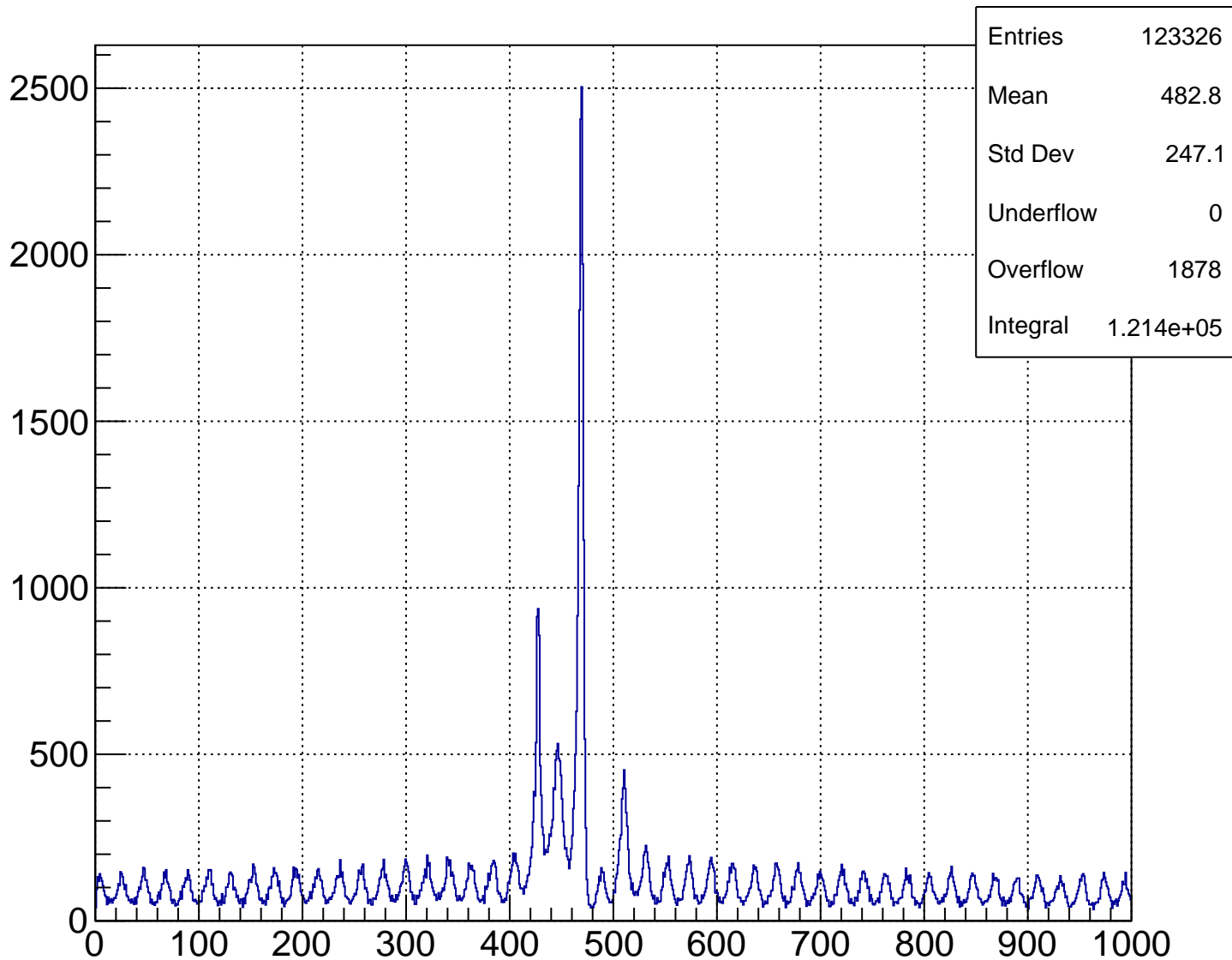
# SCH Tdc 13



# SCH Tdc 14

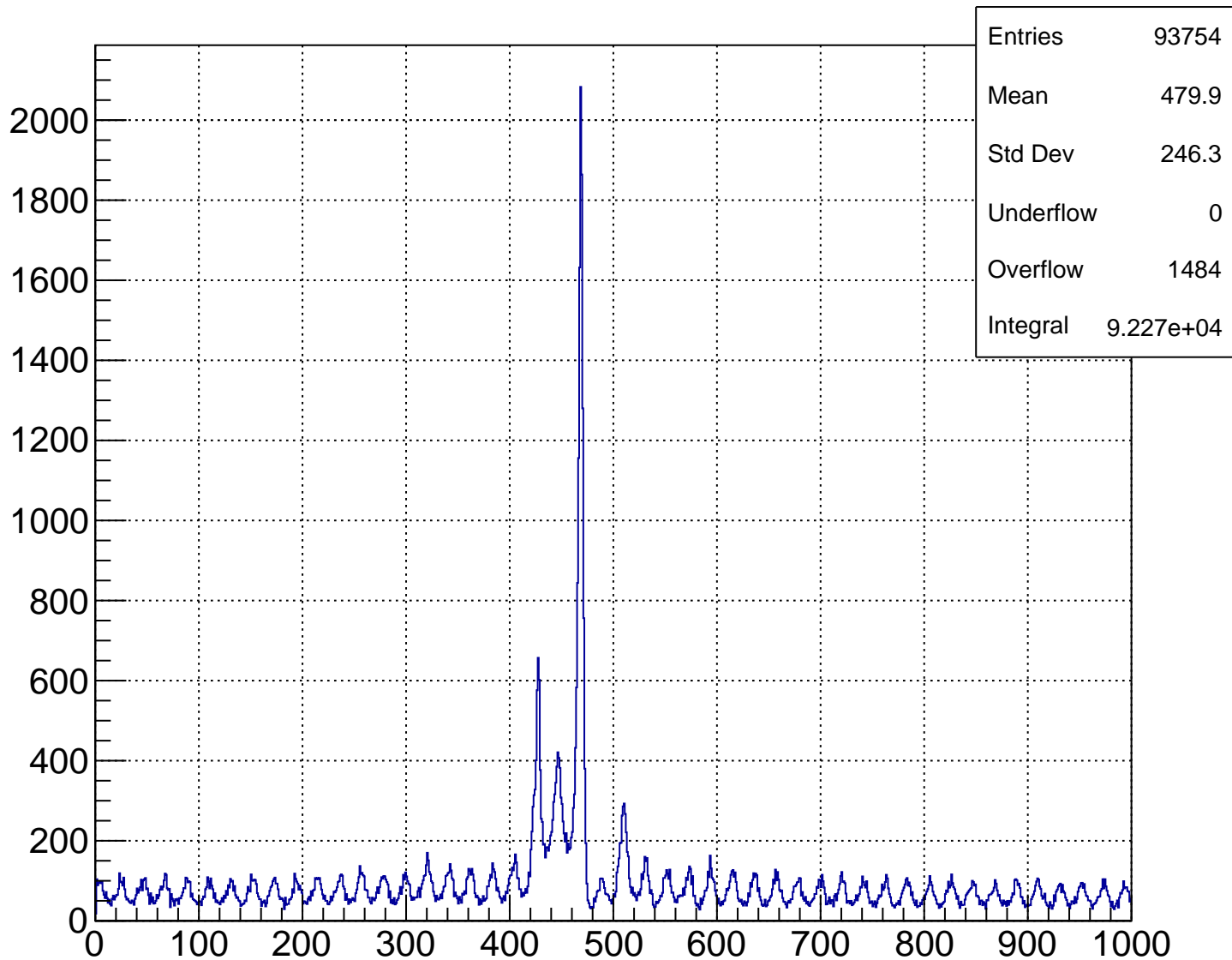


# SCH Tdc 15

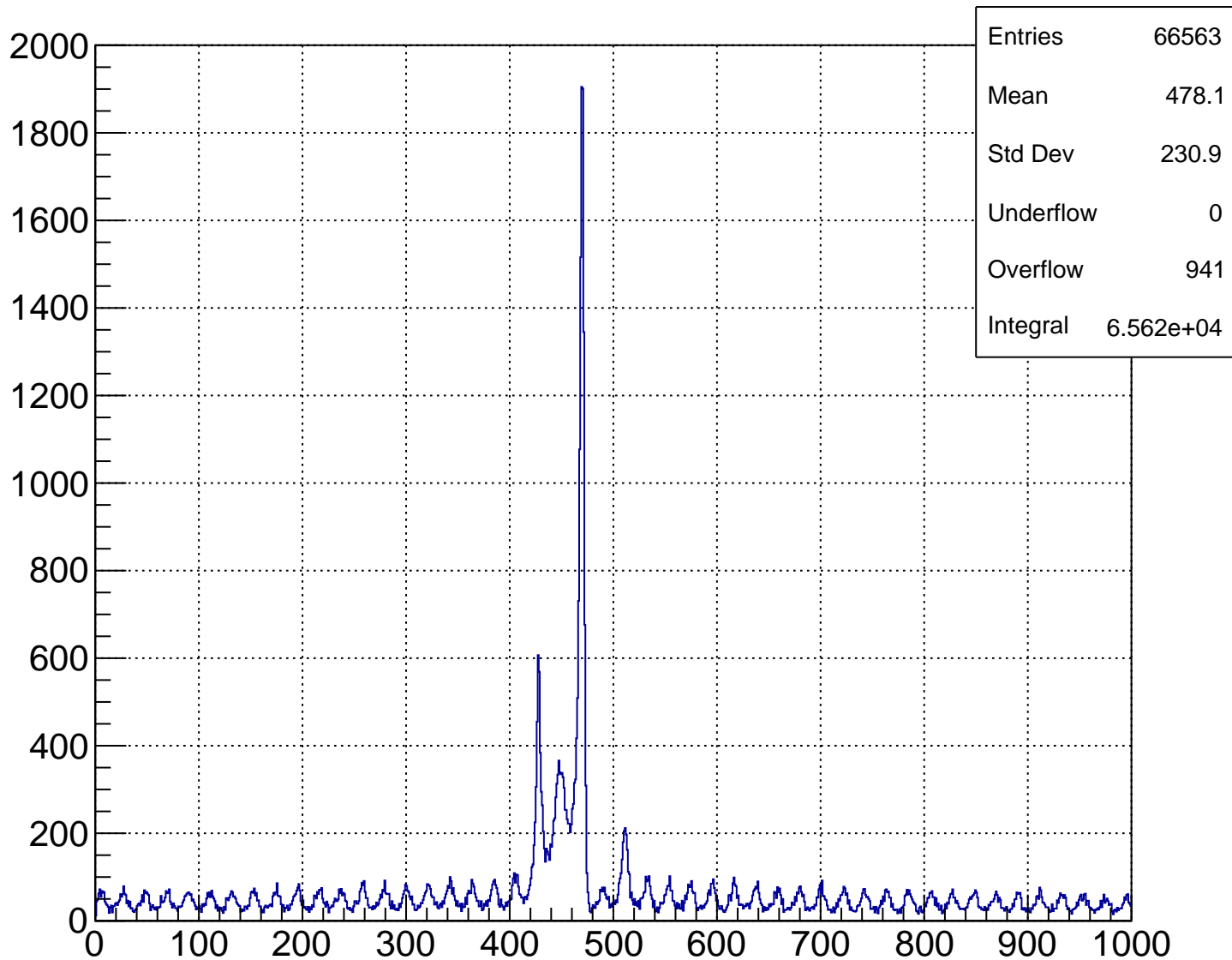




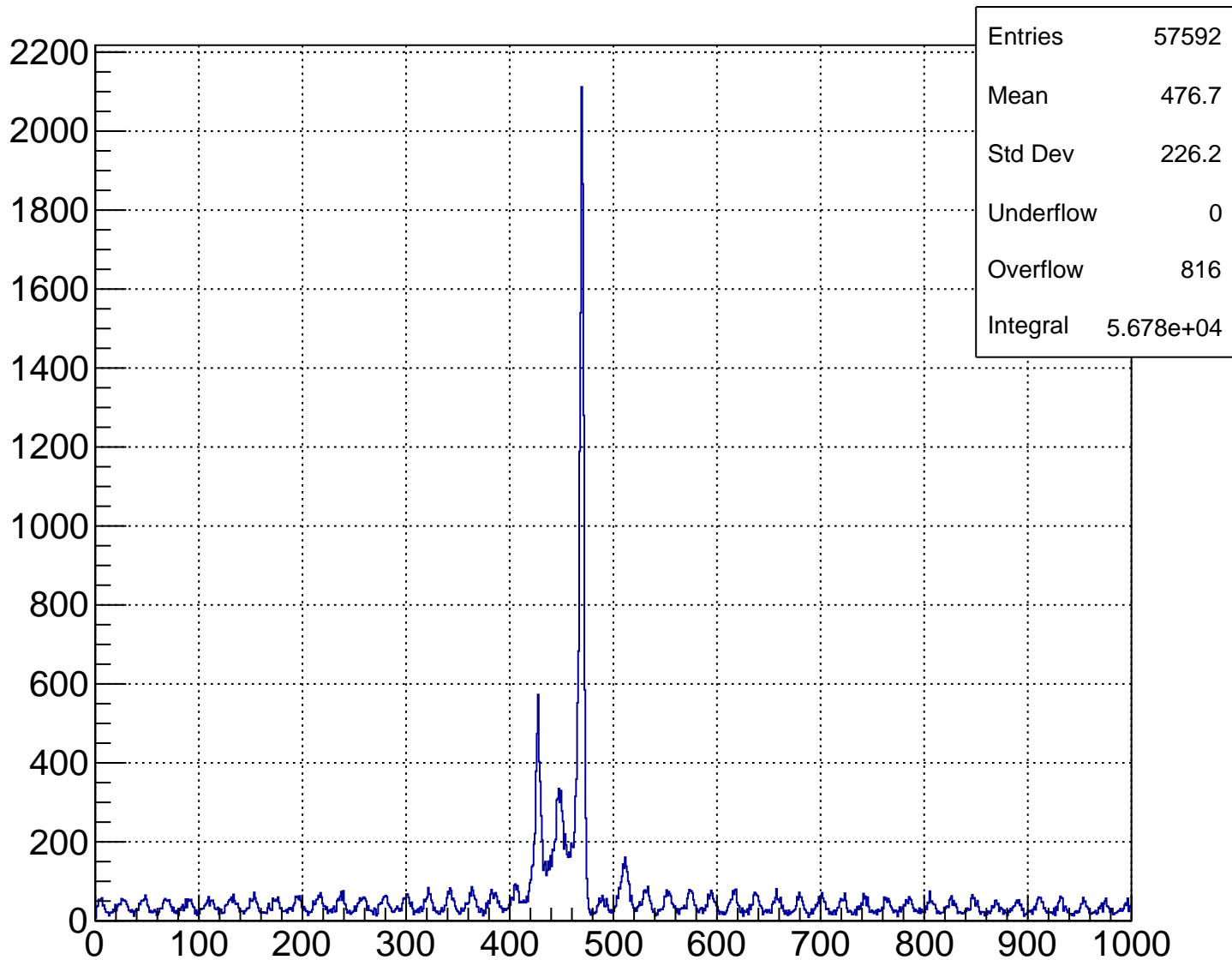
# SCH Tdc 16



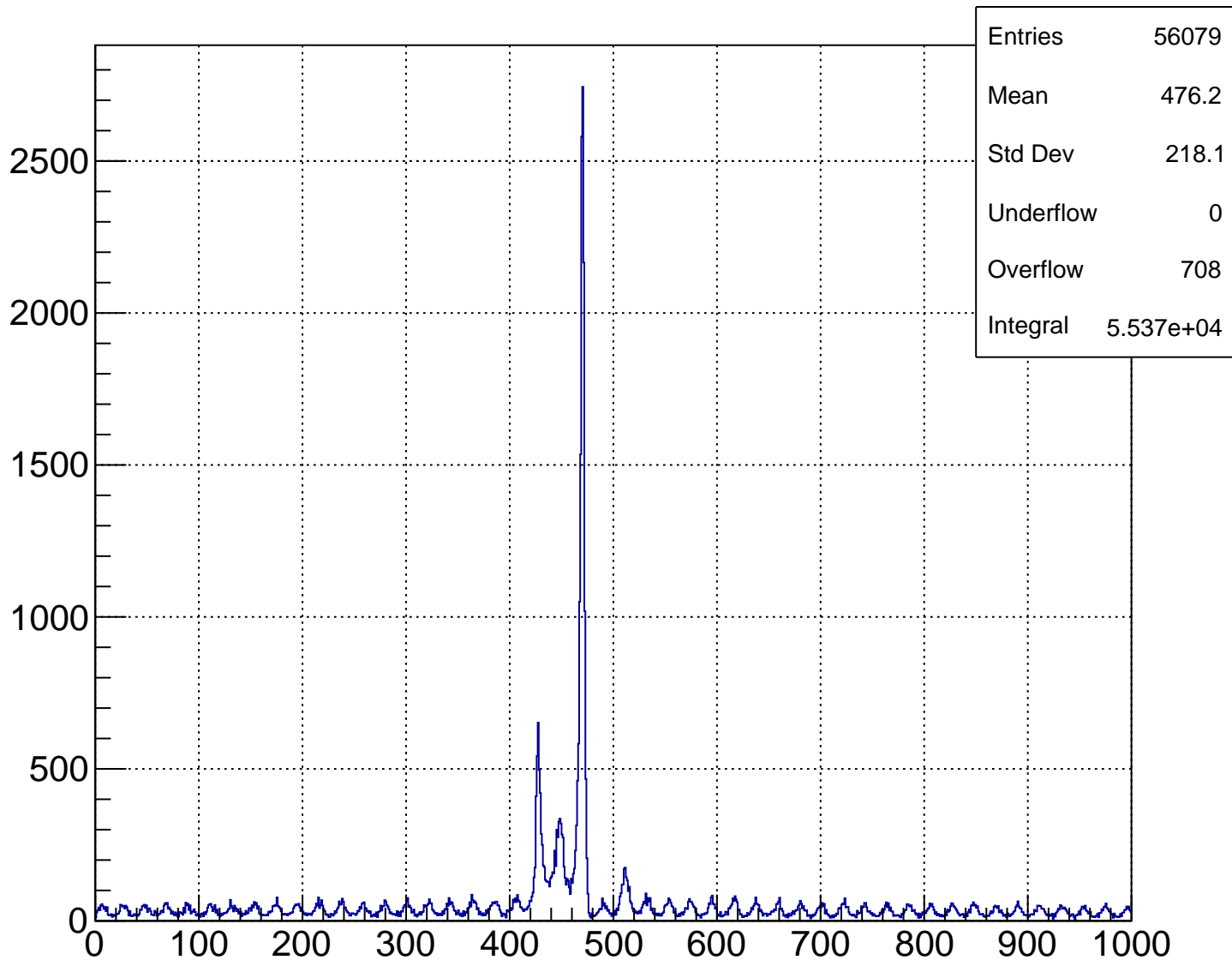
# SCH Tdc 17



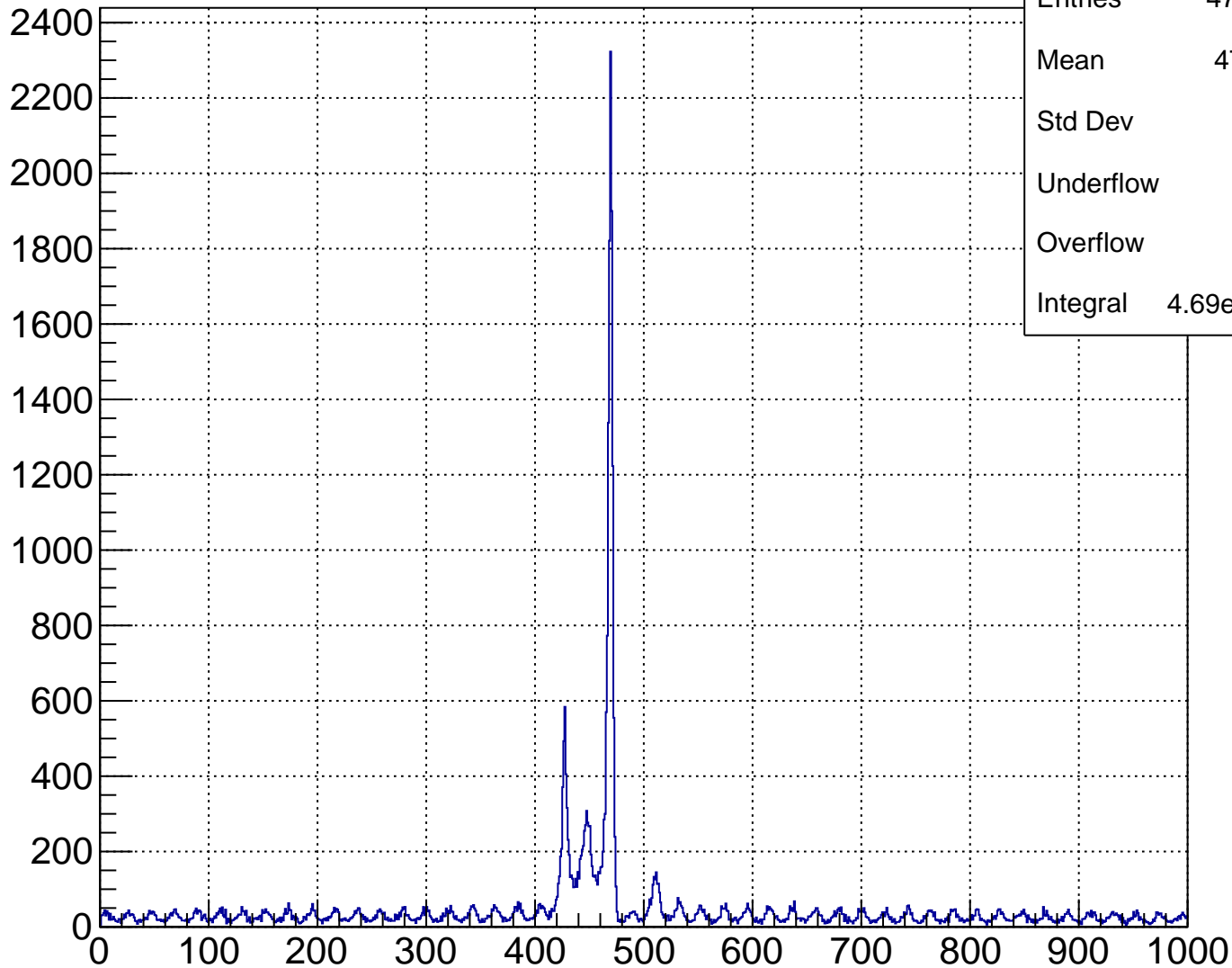
# SCH Tdc 18



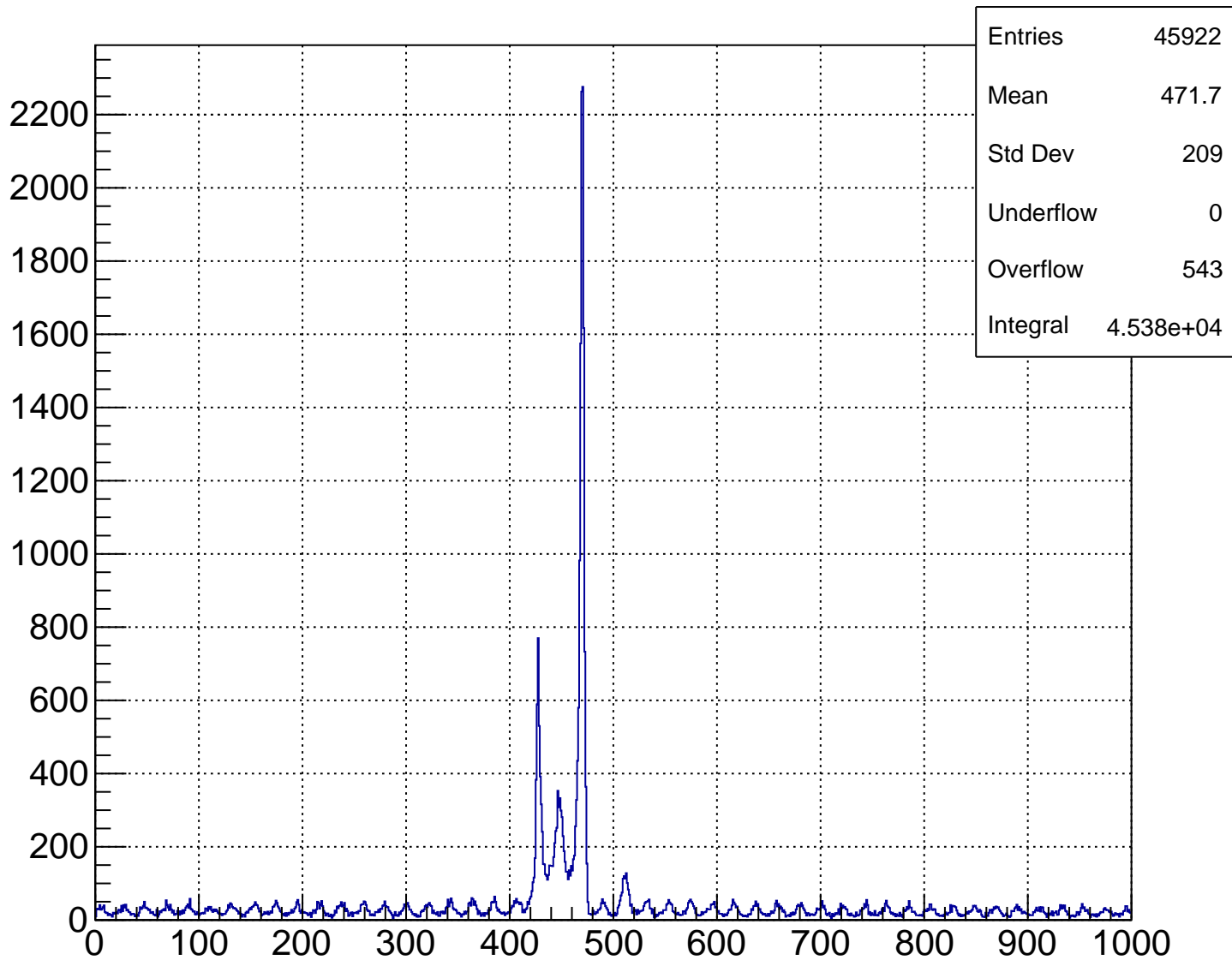
# SCH Tdc 19



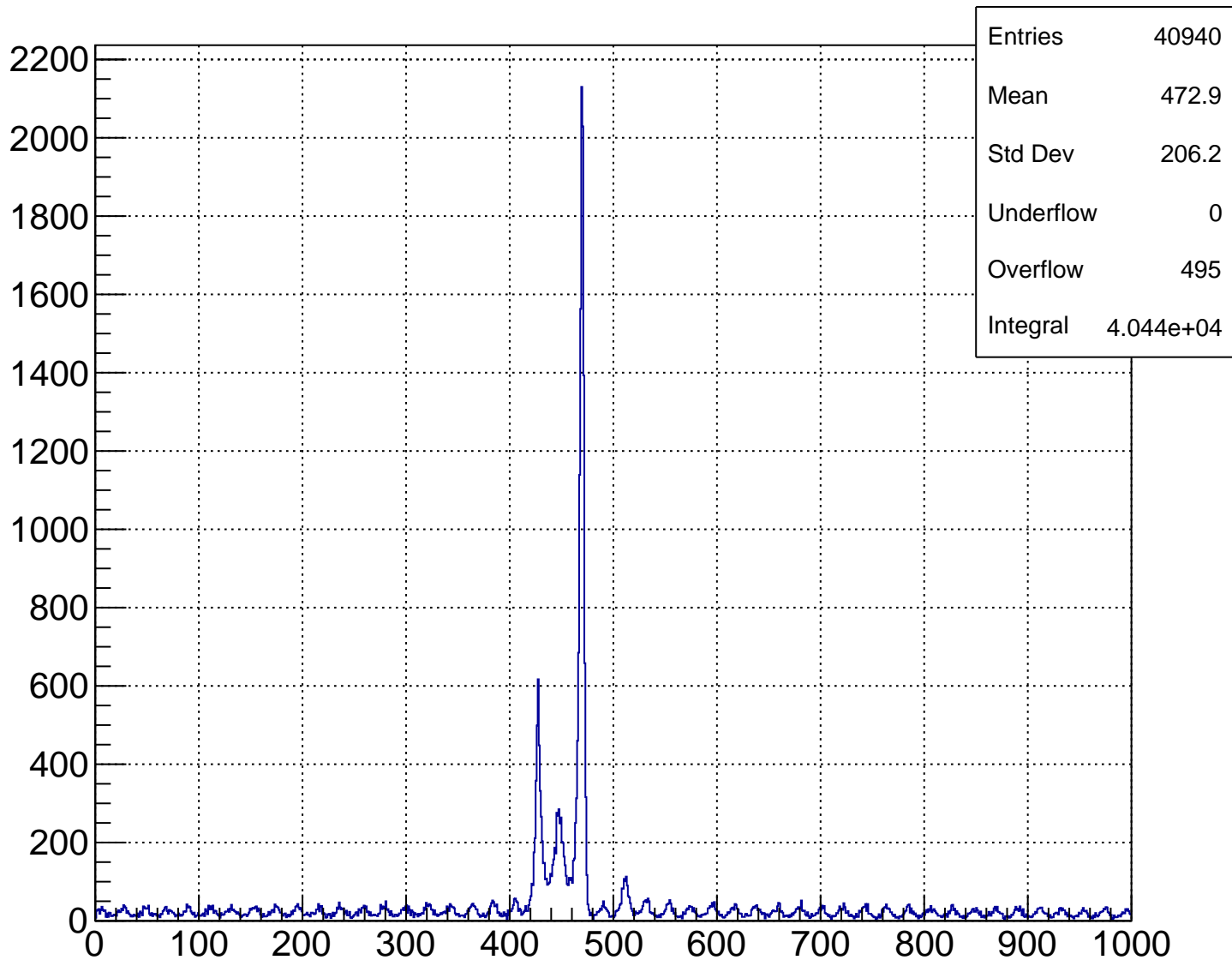
# SCH Tdc 20



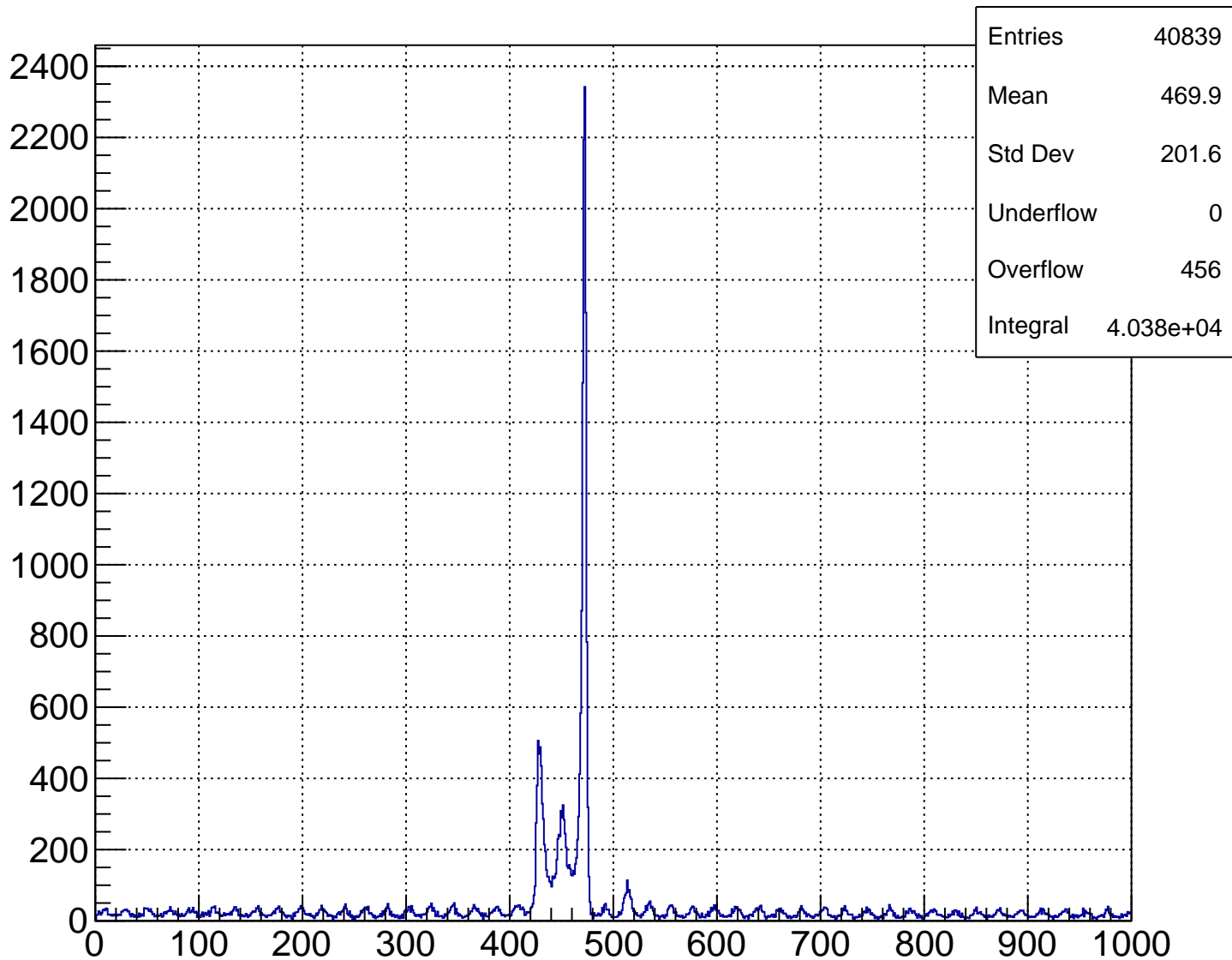
# SCH Tdc 21



# SCH Tdc 22

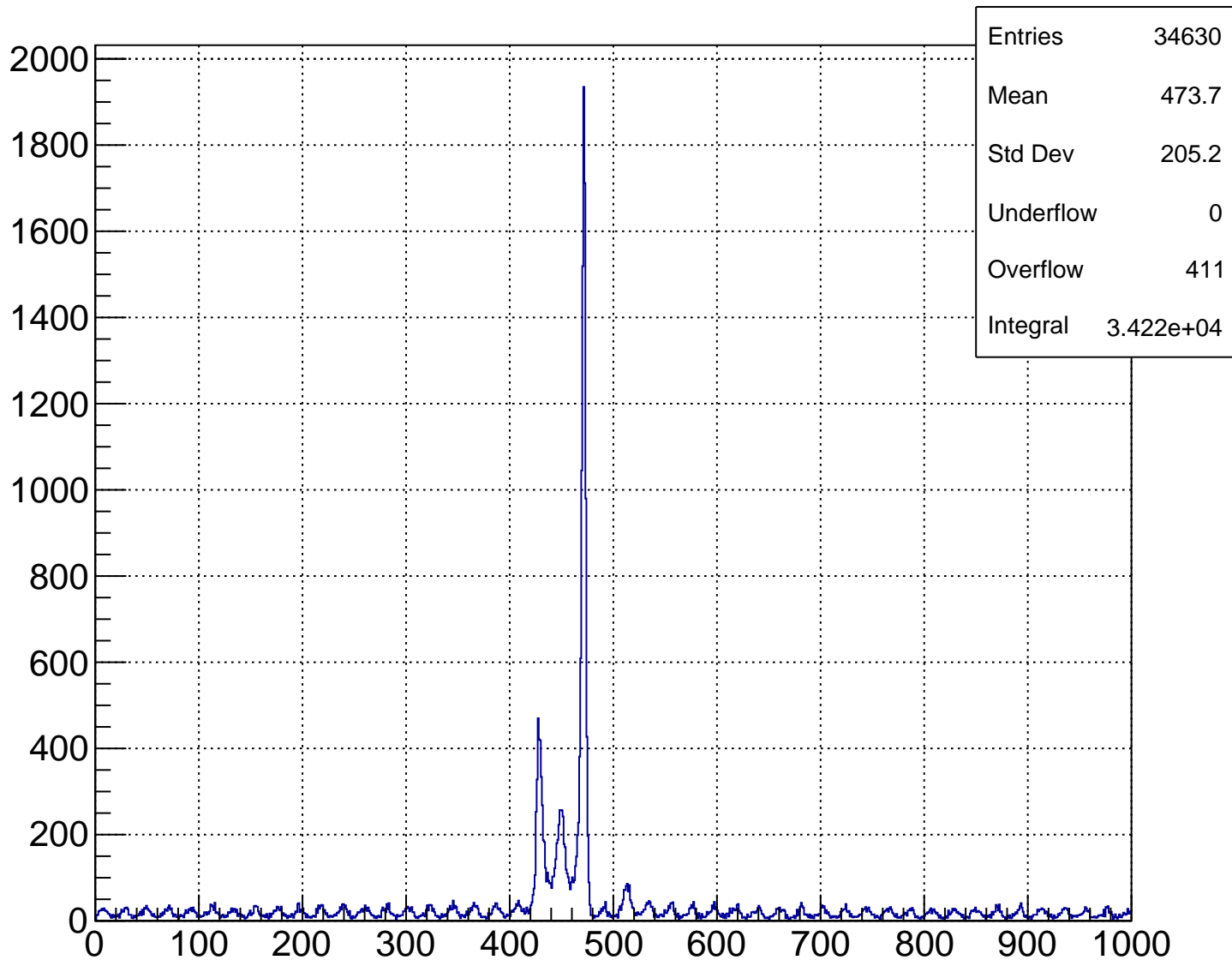


# SCH Tdc 23

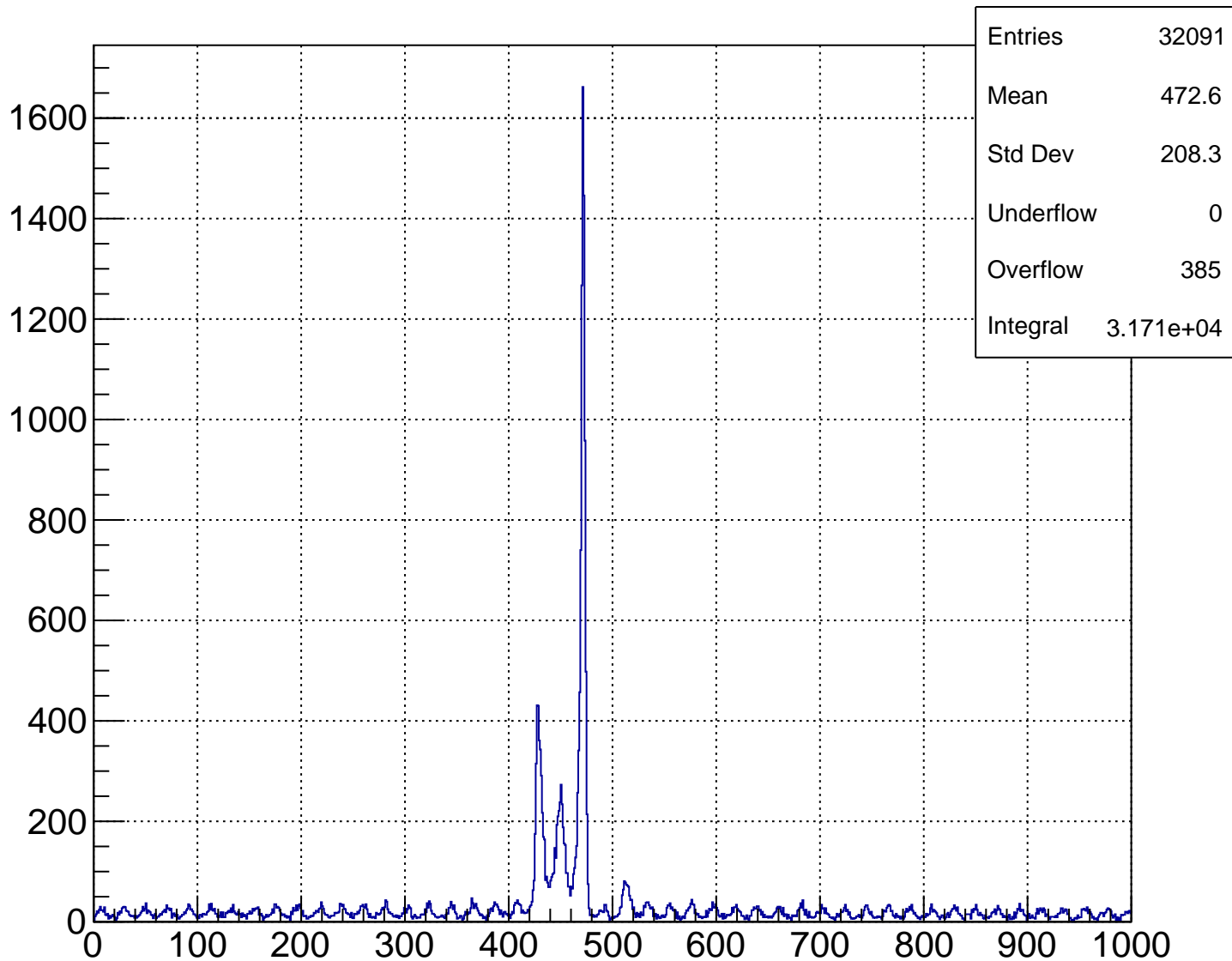




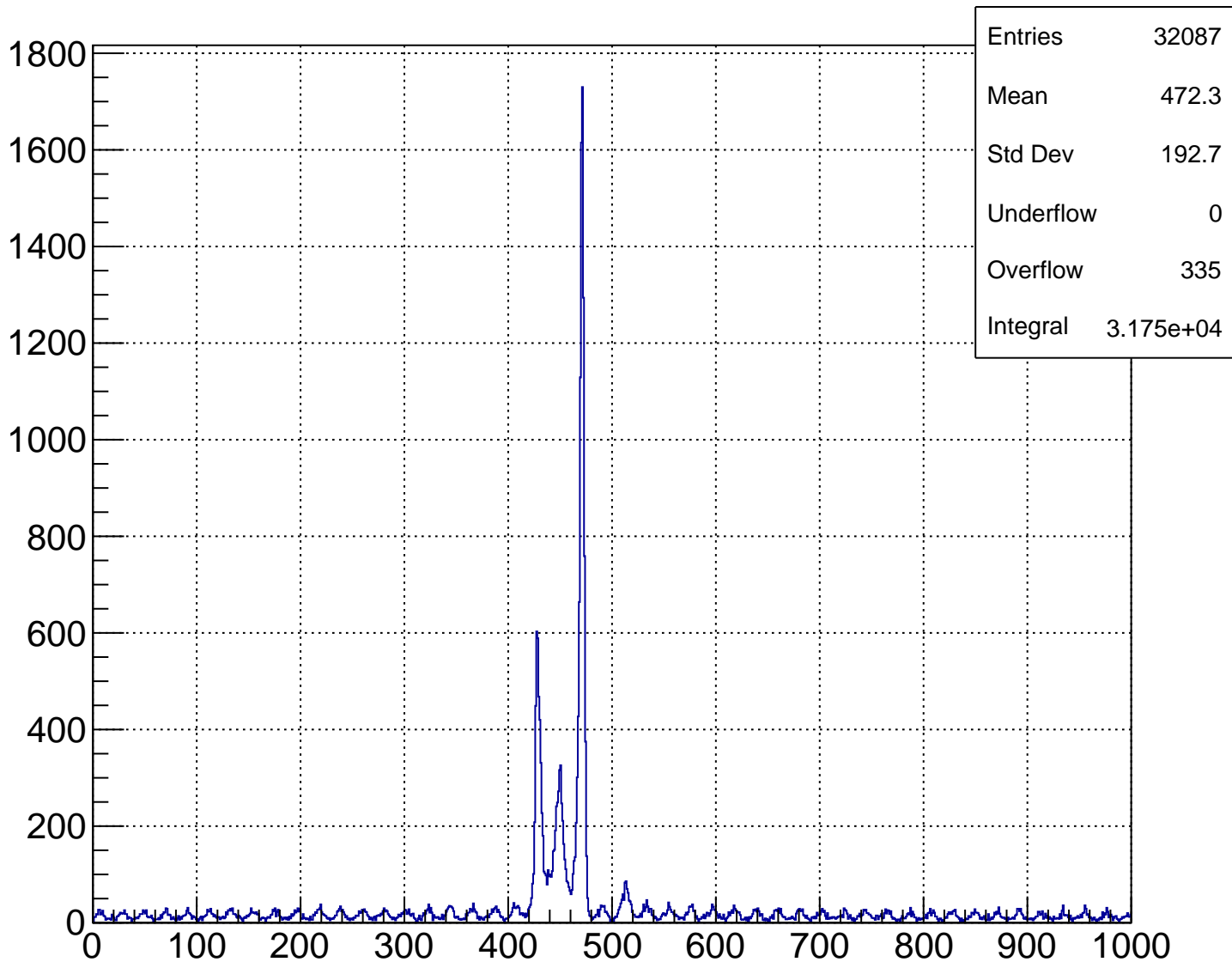
# SCH Tdc 24



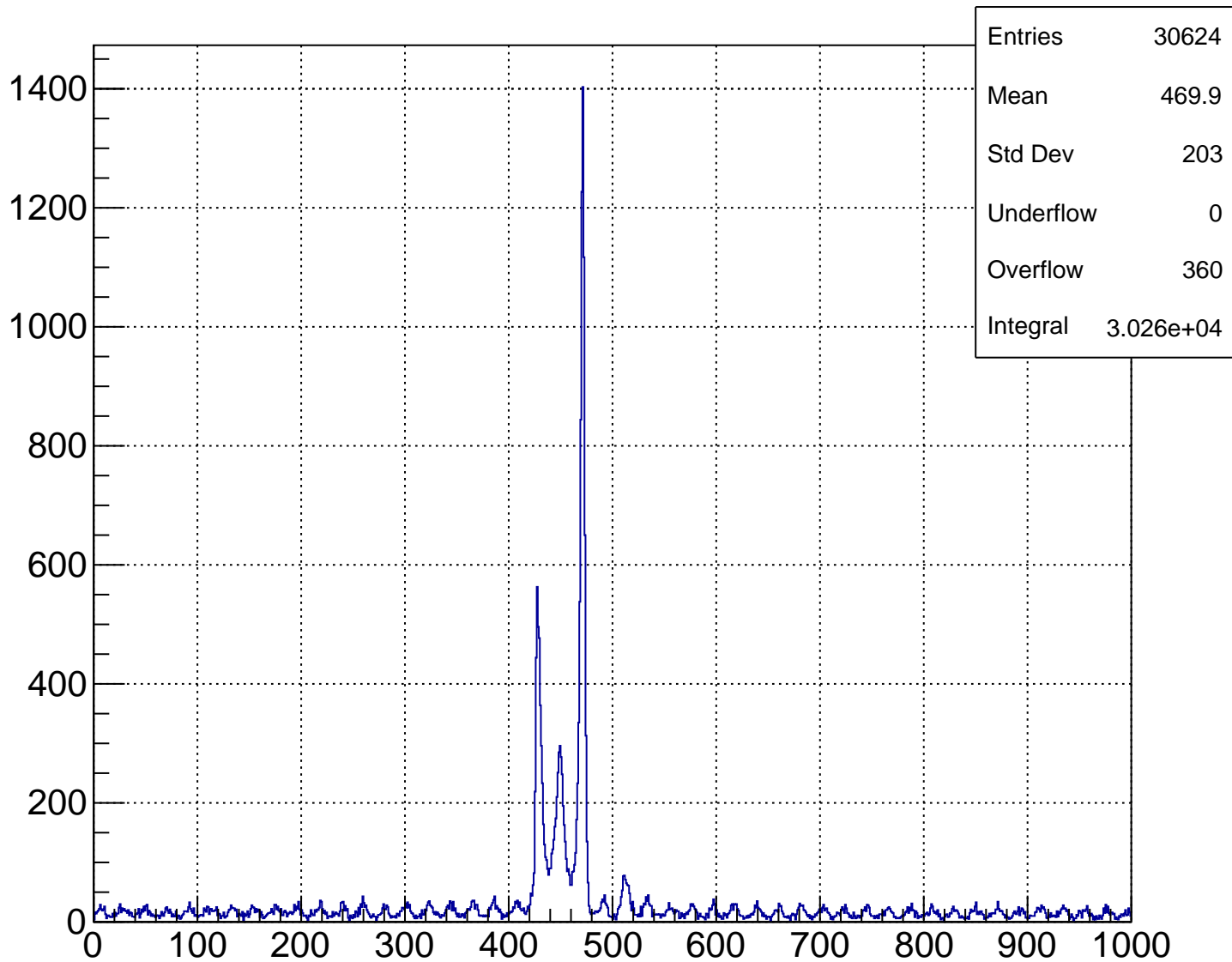
# SCH Tdc 25



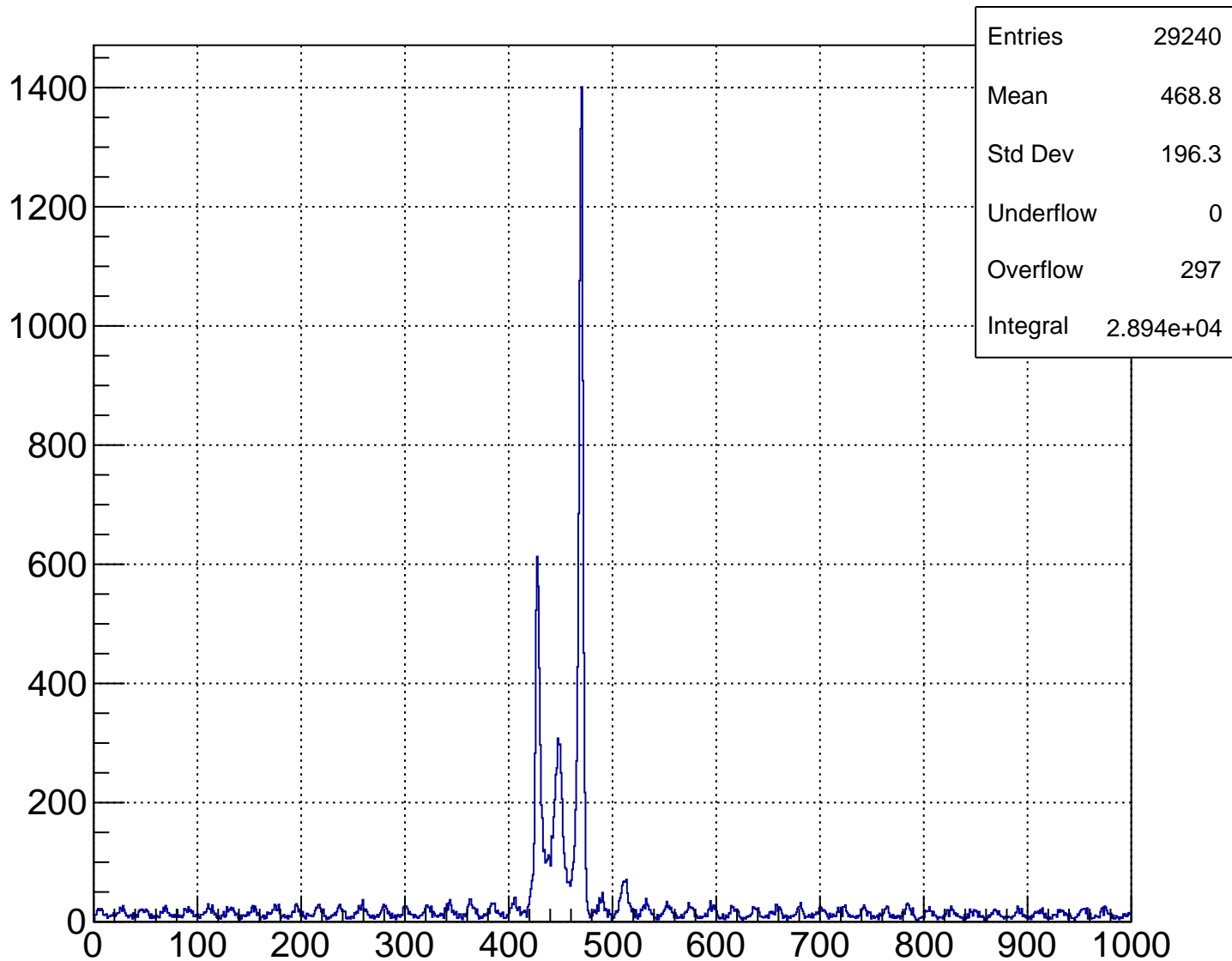
# SCH Tdc 26



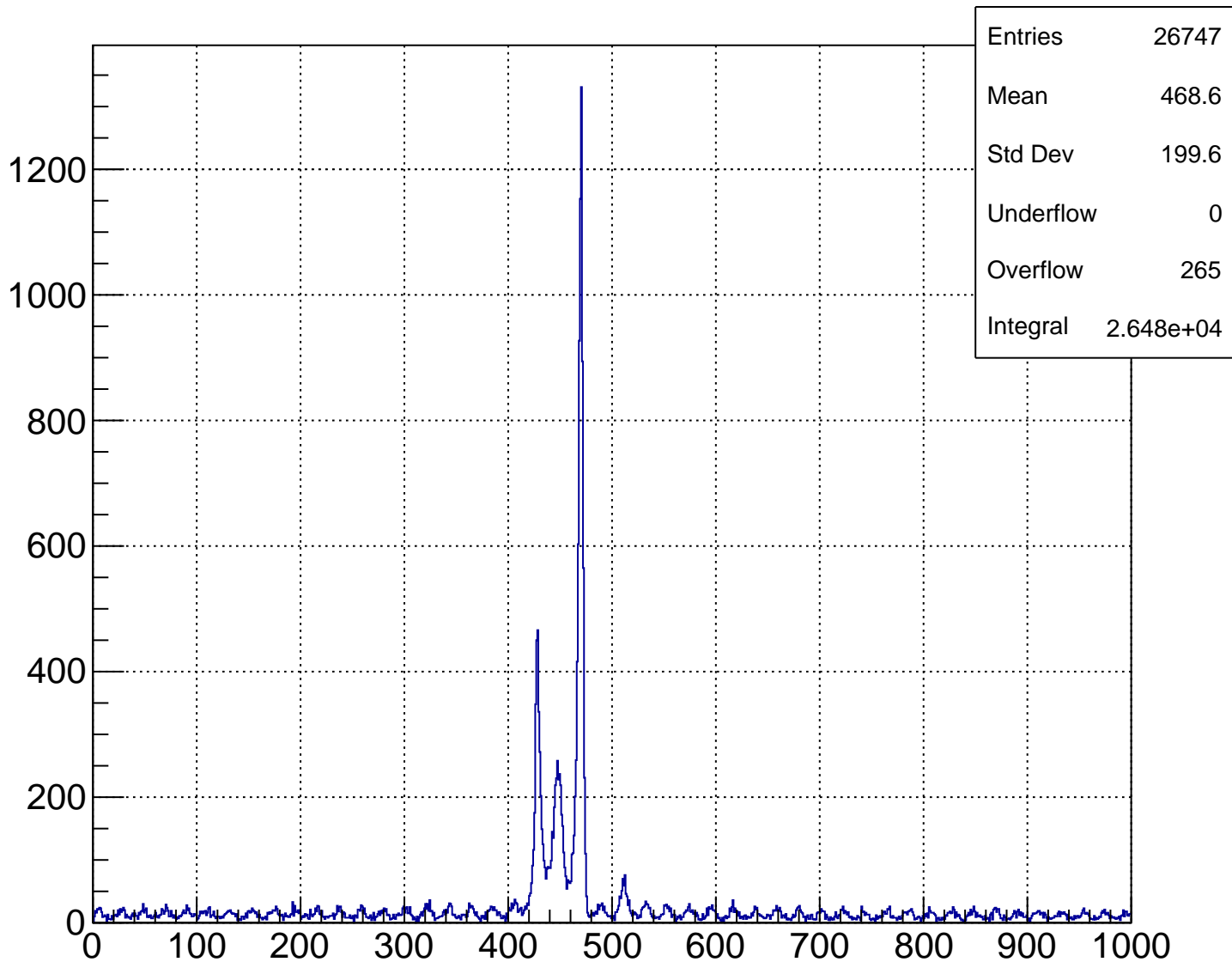
# SCH Tdc 27



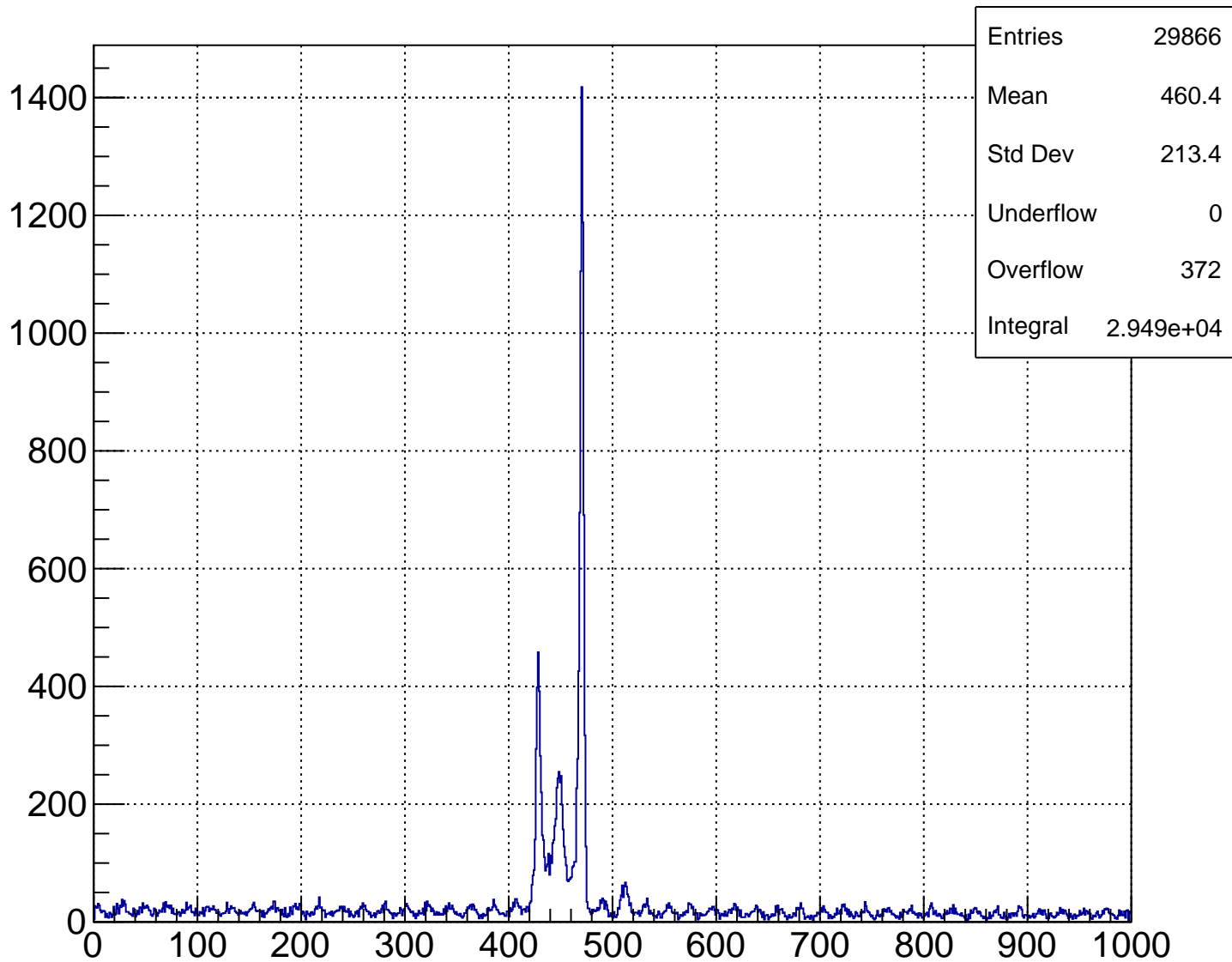
# SCH Tdc 28



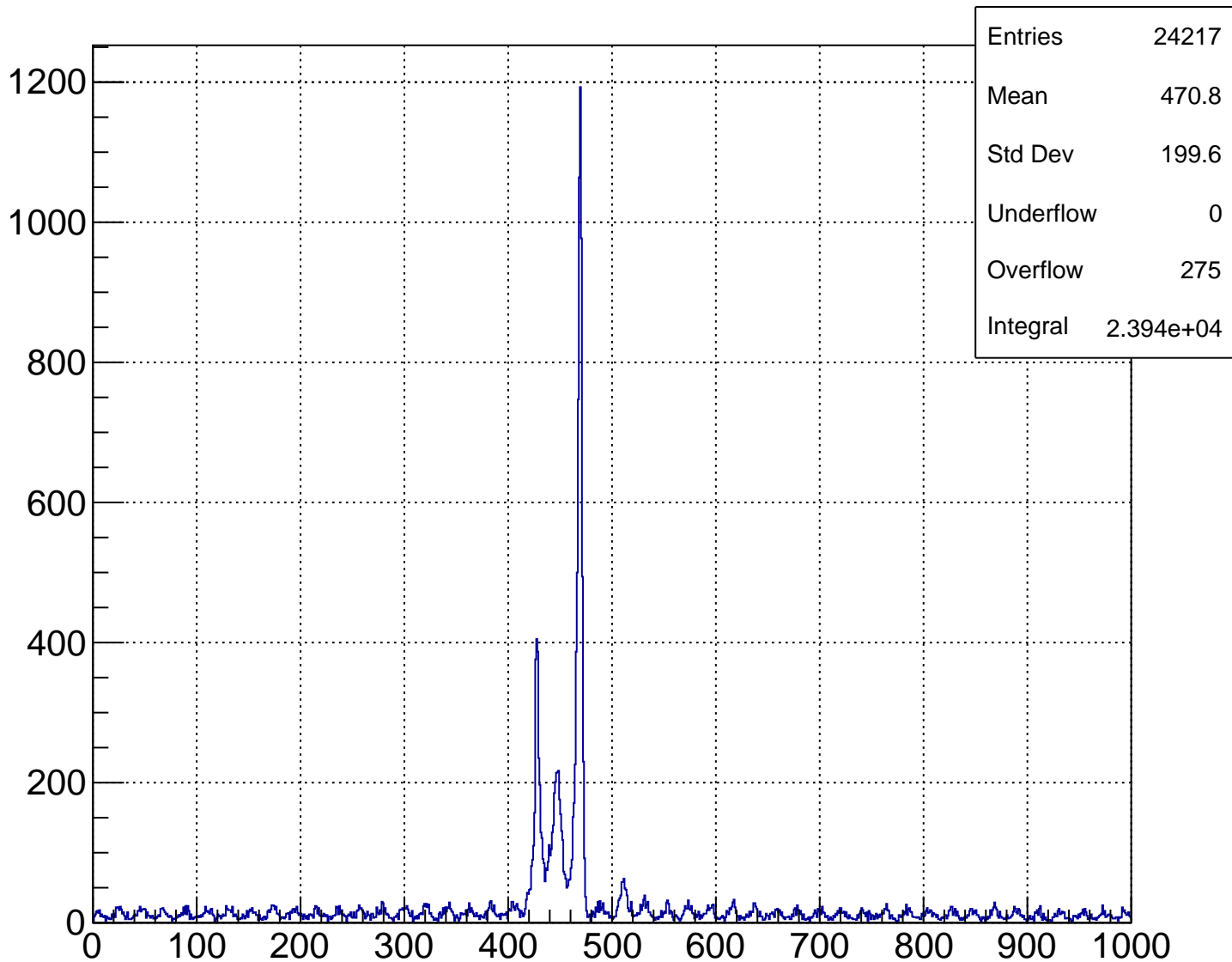
# SCH Tdc 29



# SCH Tdc 30

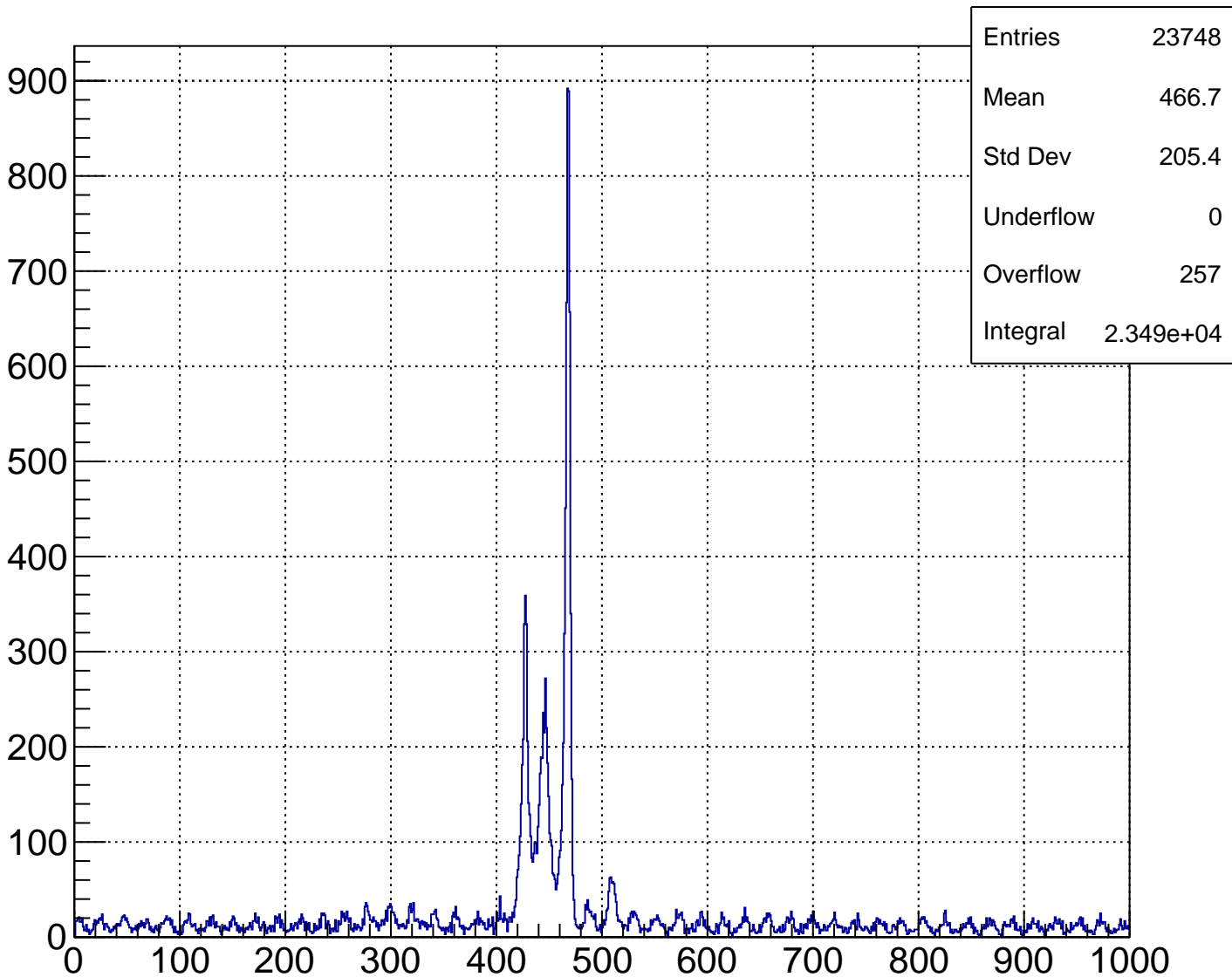


# SCH Tdc 31

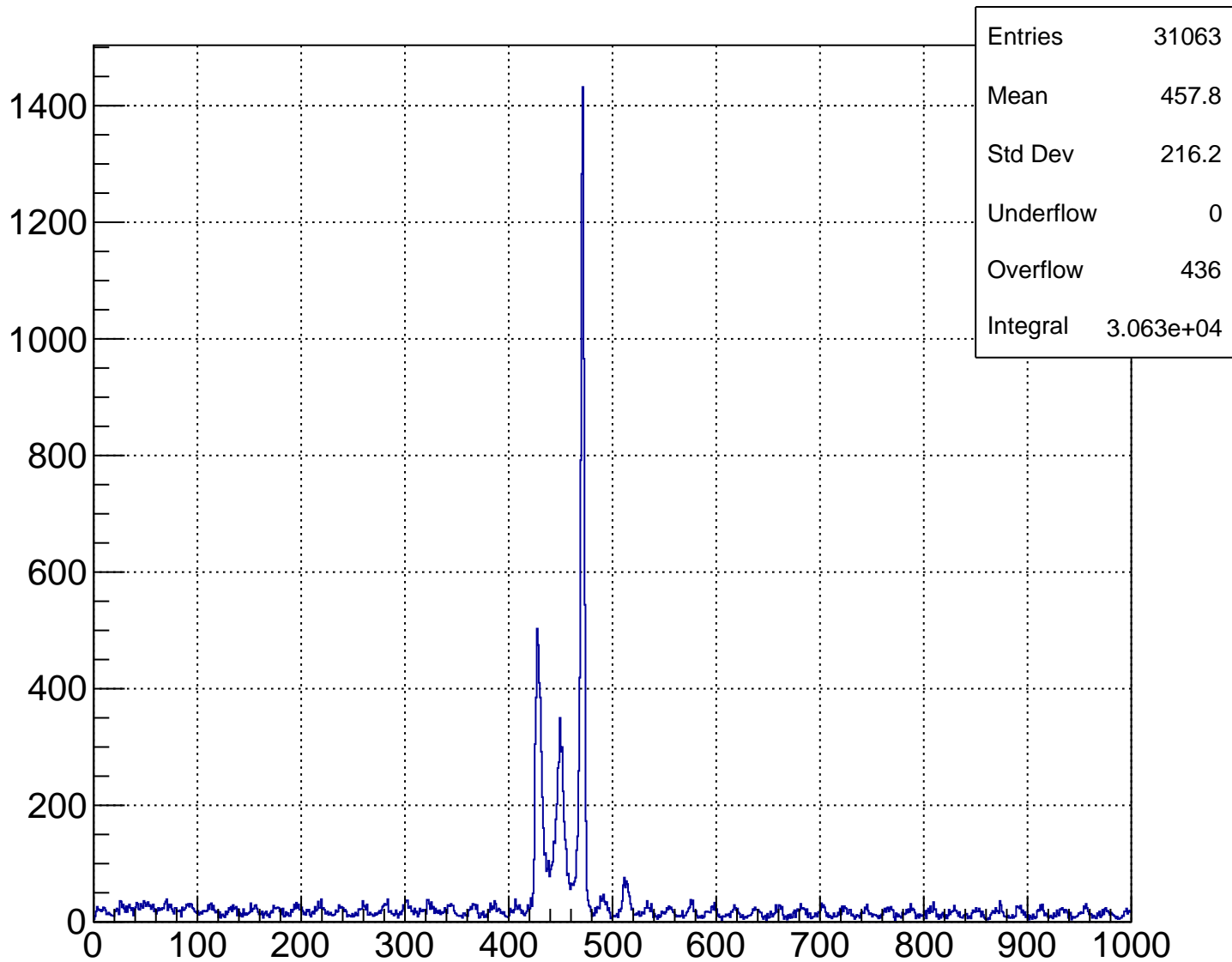




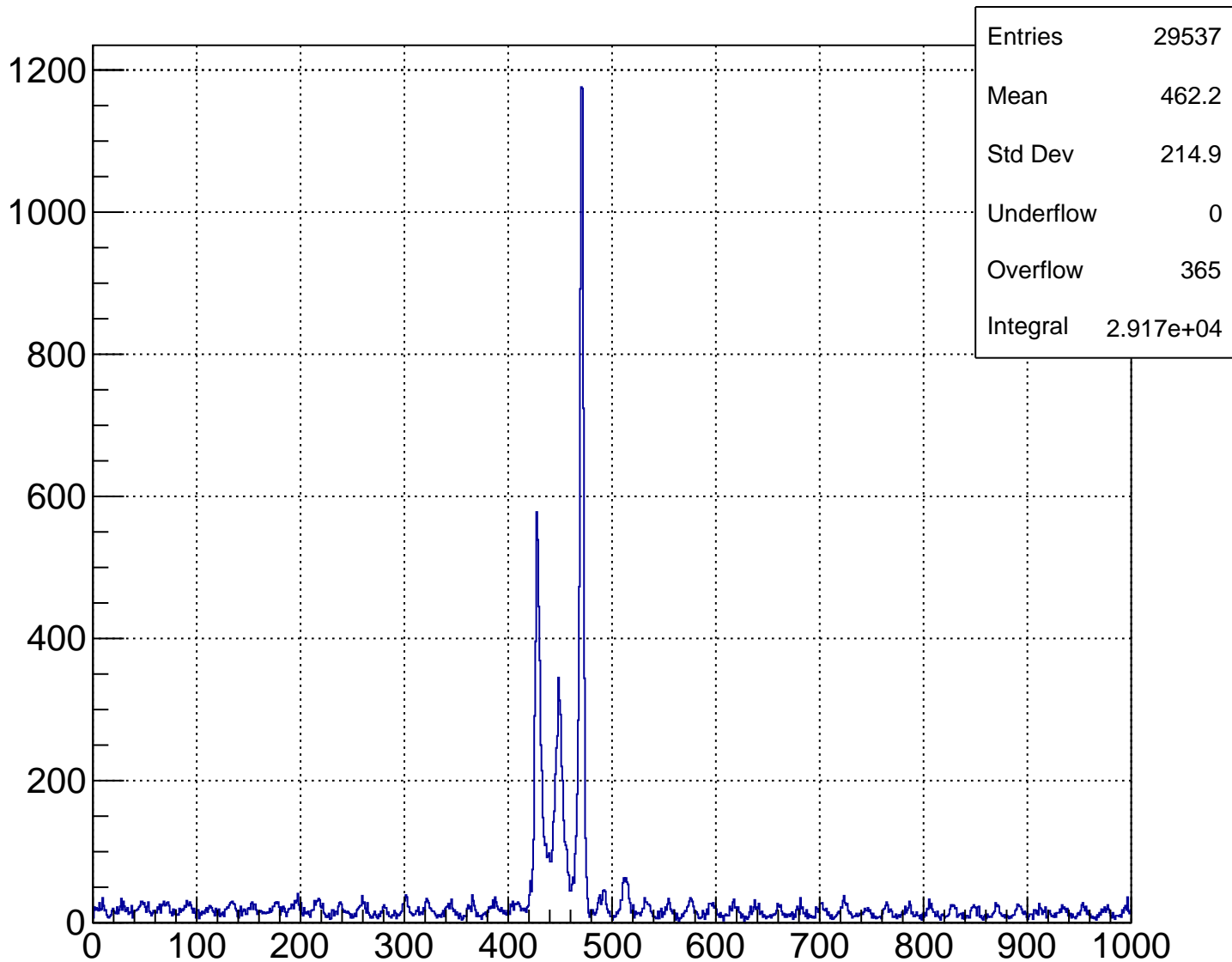
# SCH Tdc 32



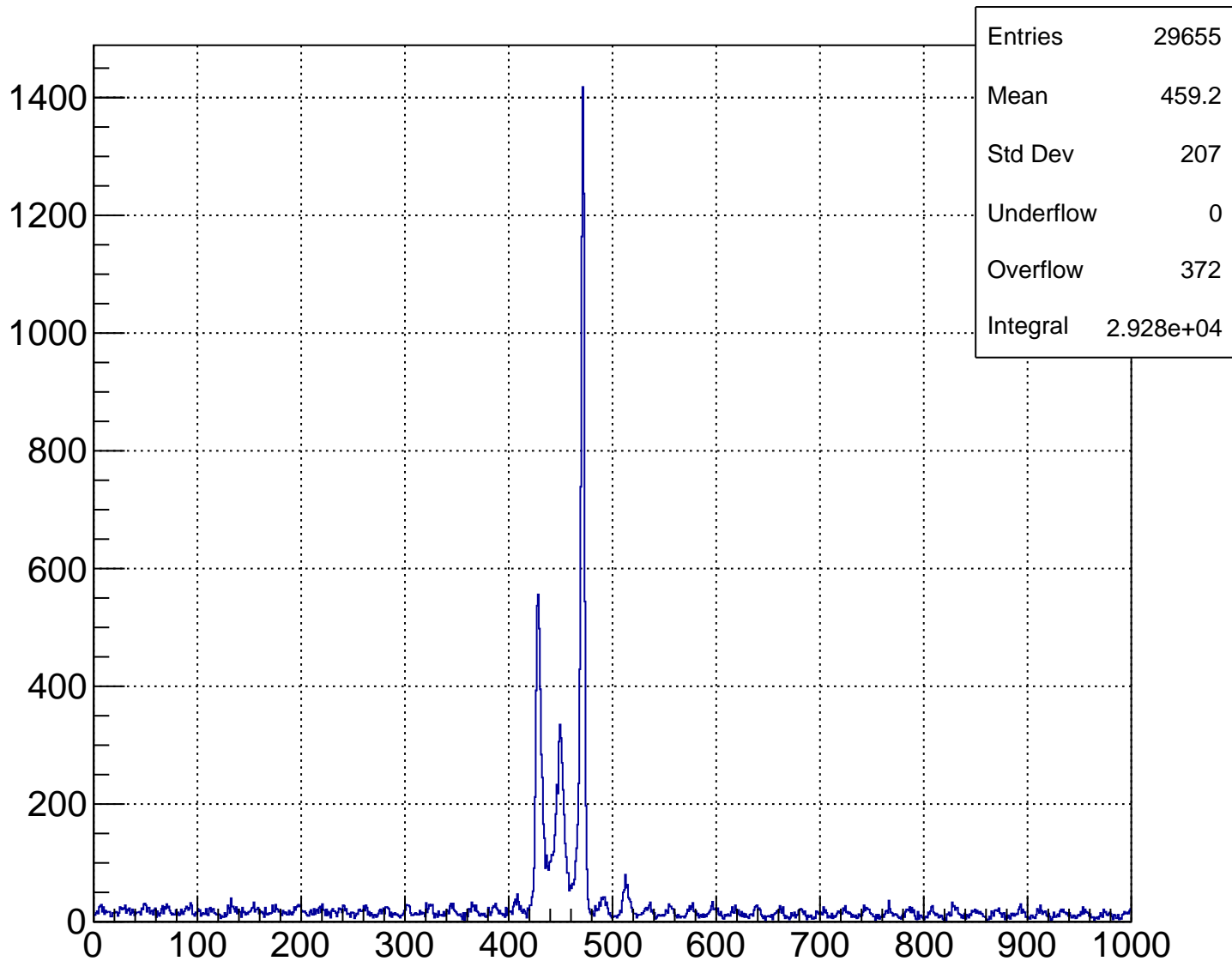
# SCH Tdc 33



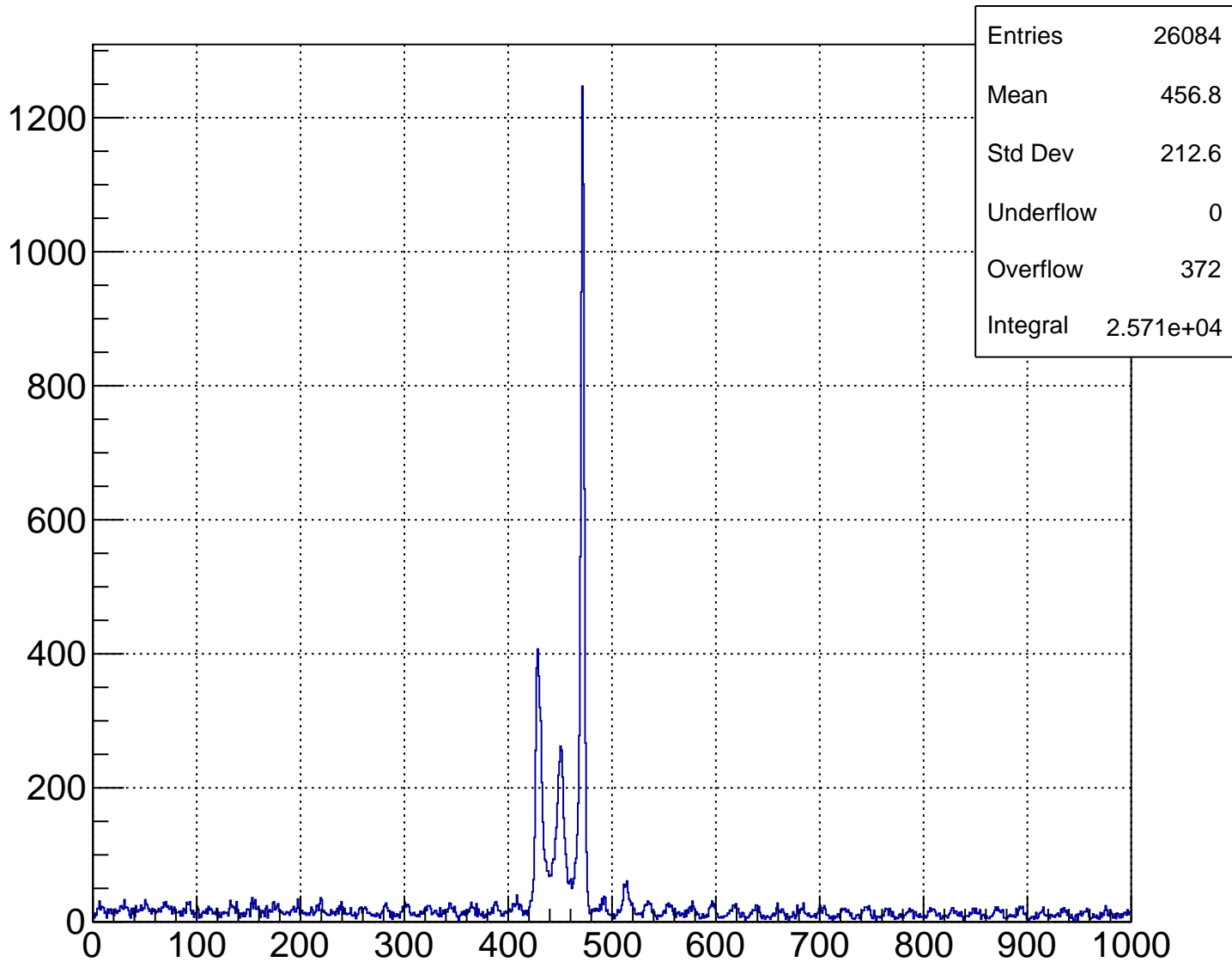
# SCH Tdc 34



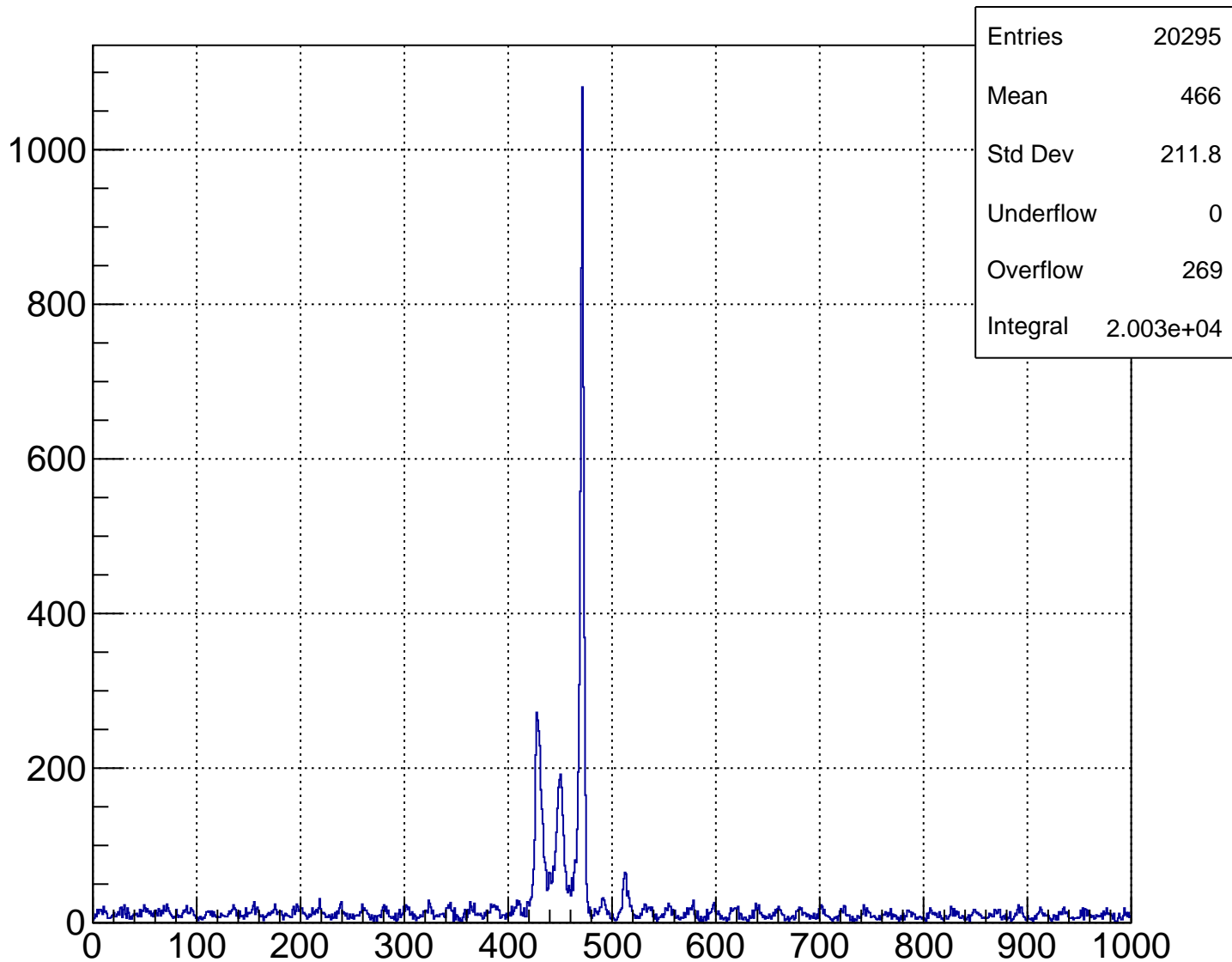
# SCH Tdc 35



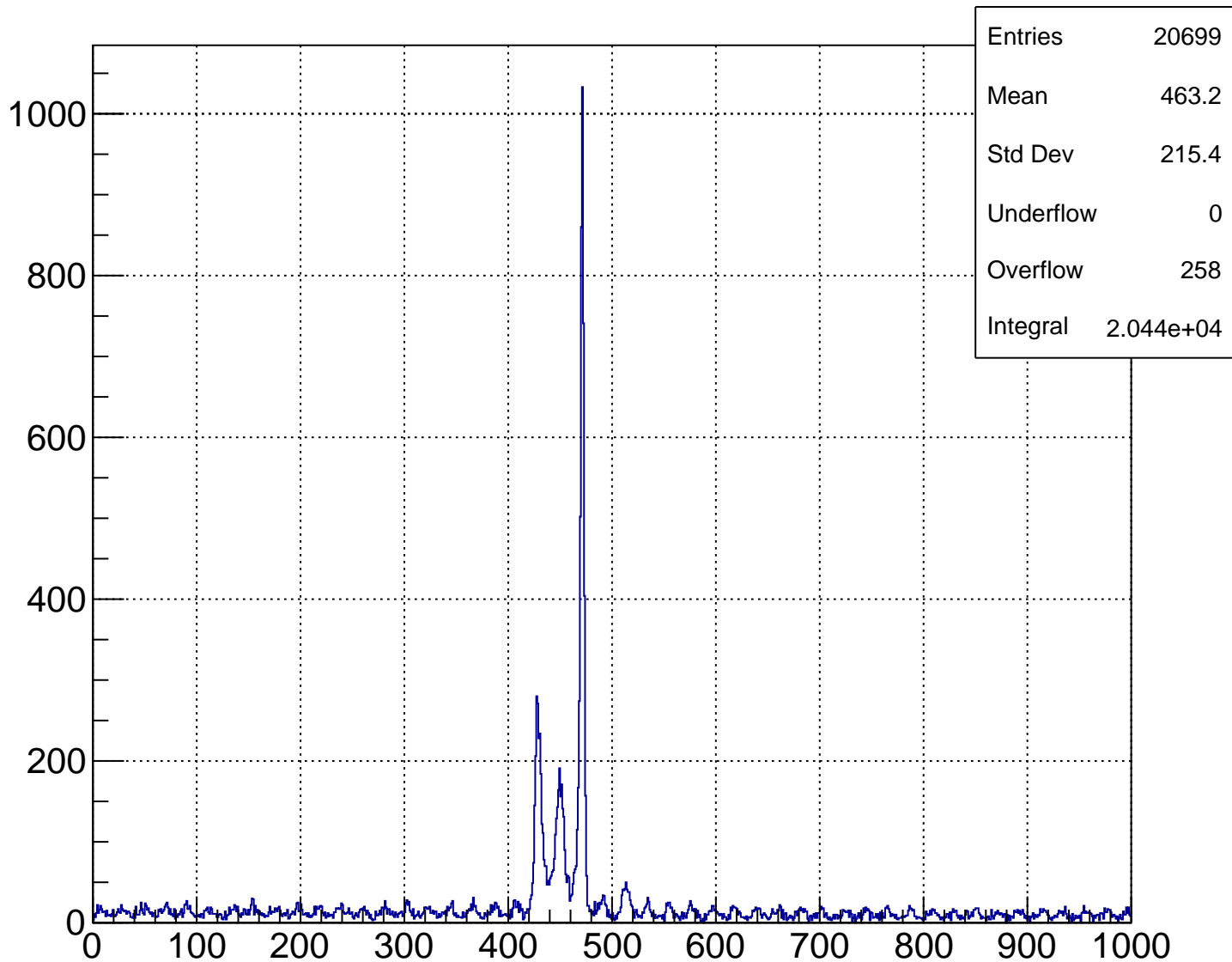
# SCH Tdc 36



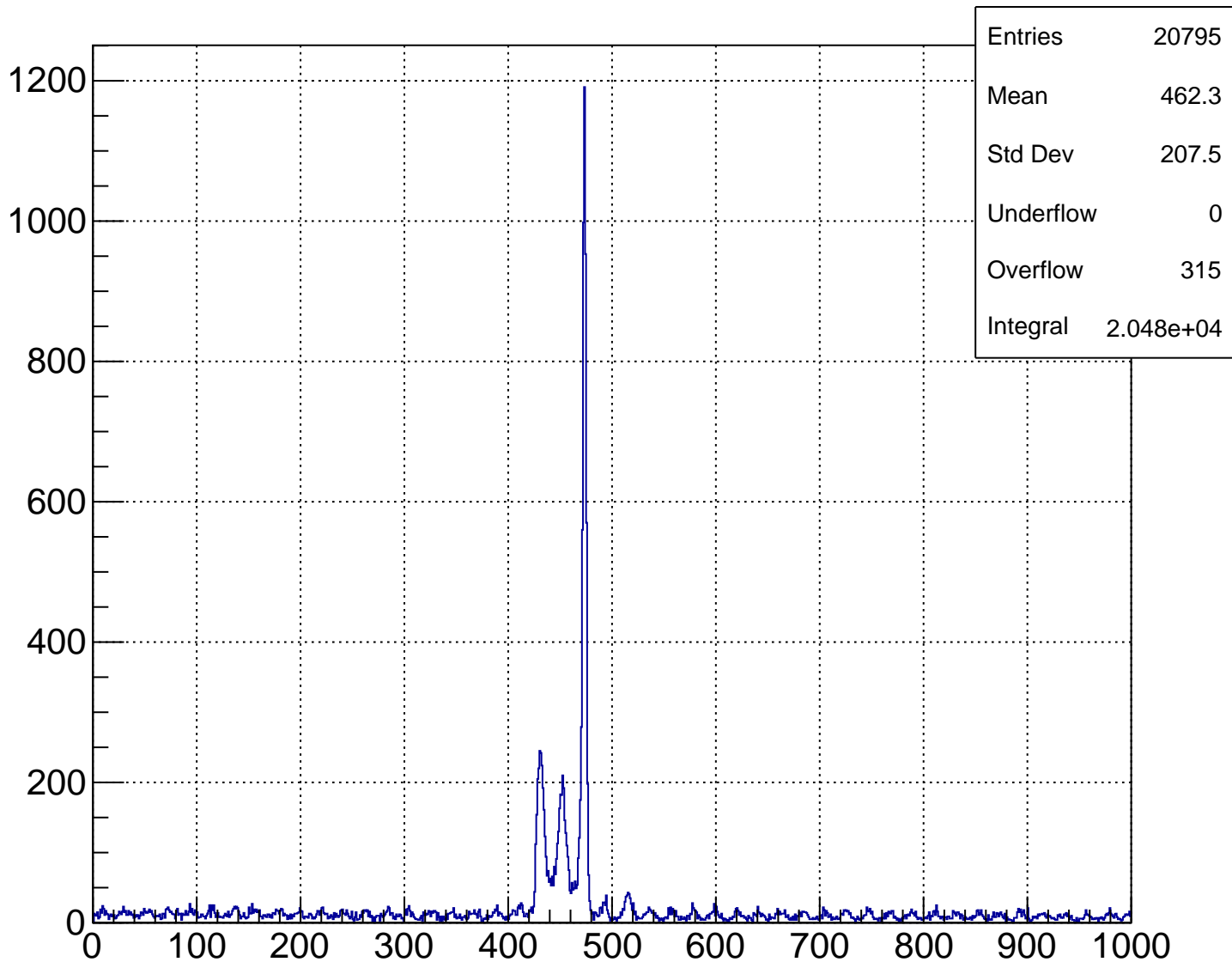
# SCH Tdc 37



# SCH Tdc 38

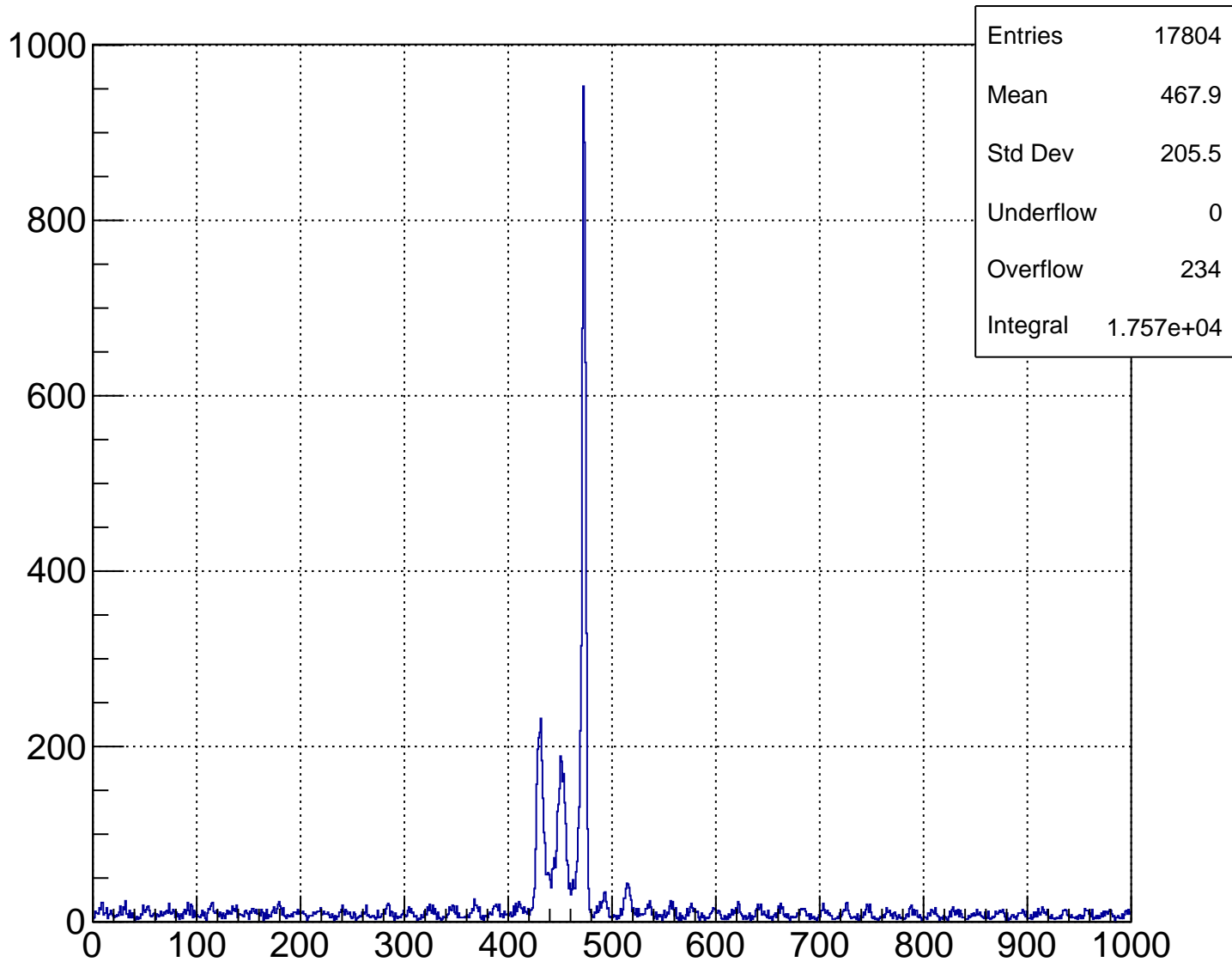


# SCH Tdc 39

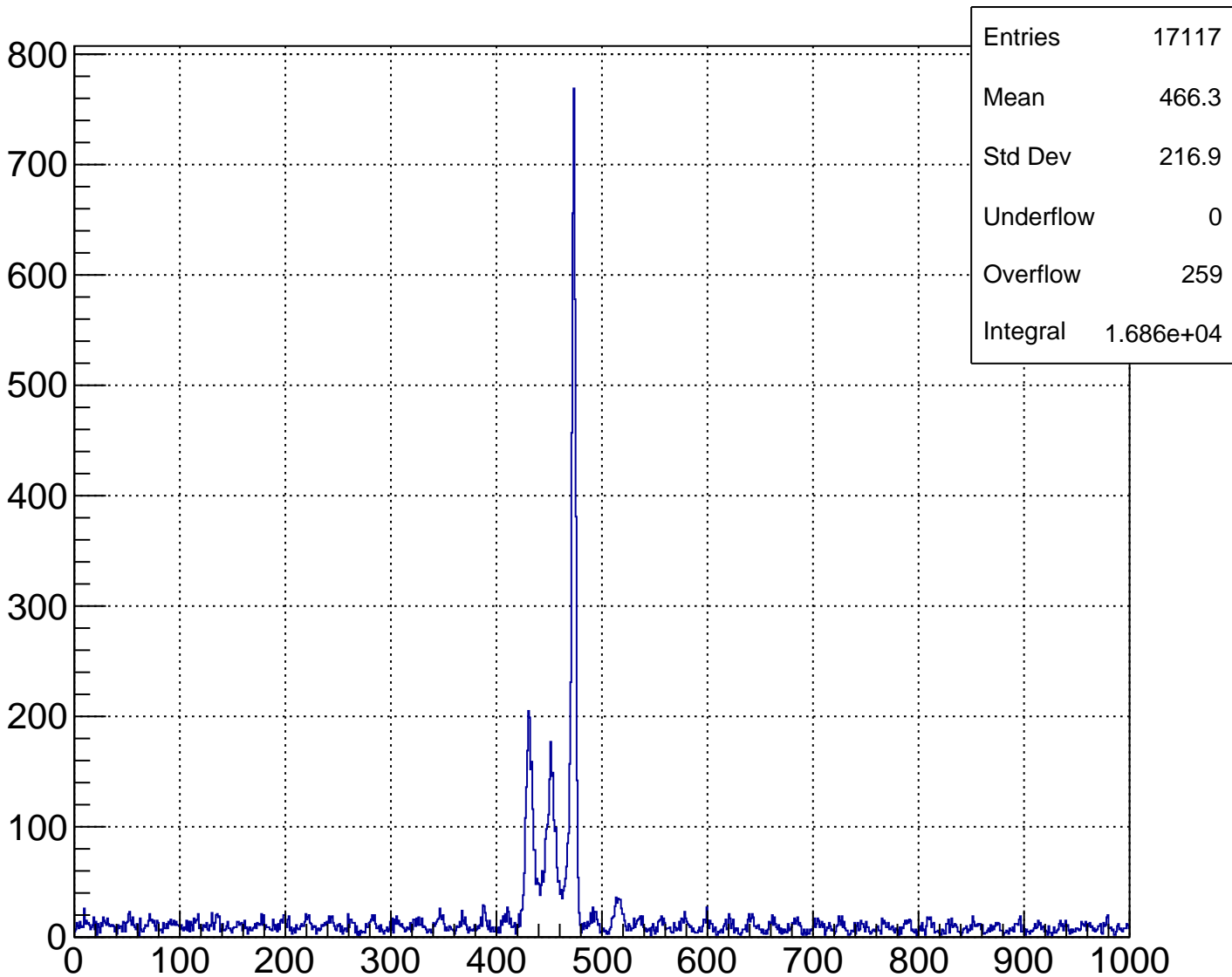




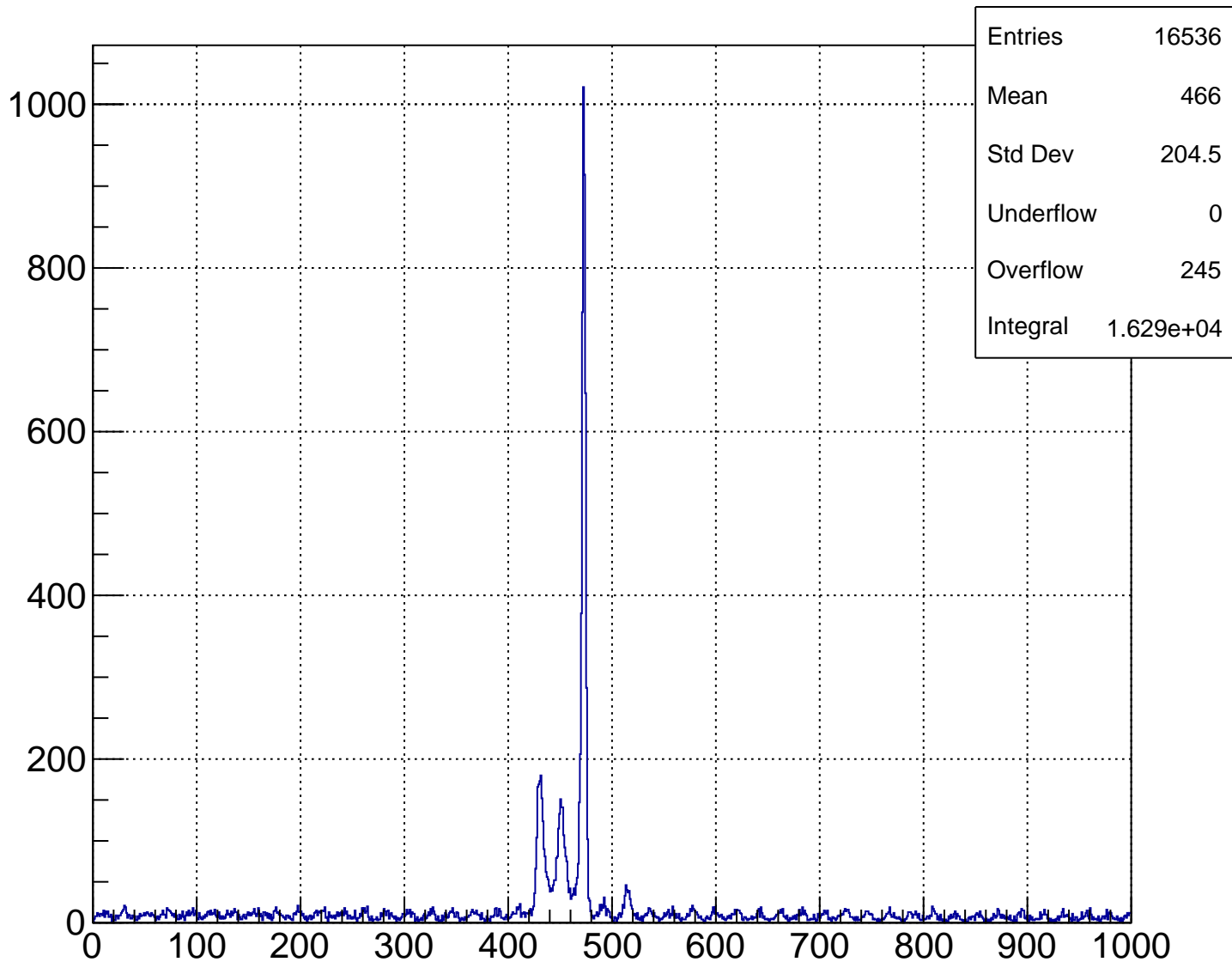
# SCH Tdc 40



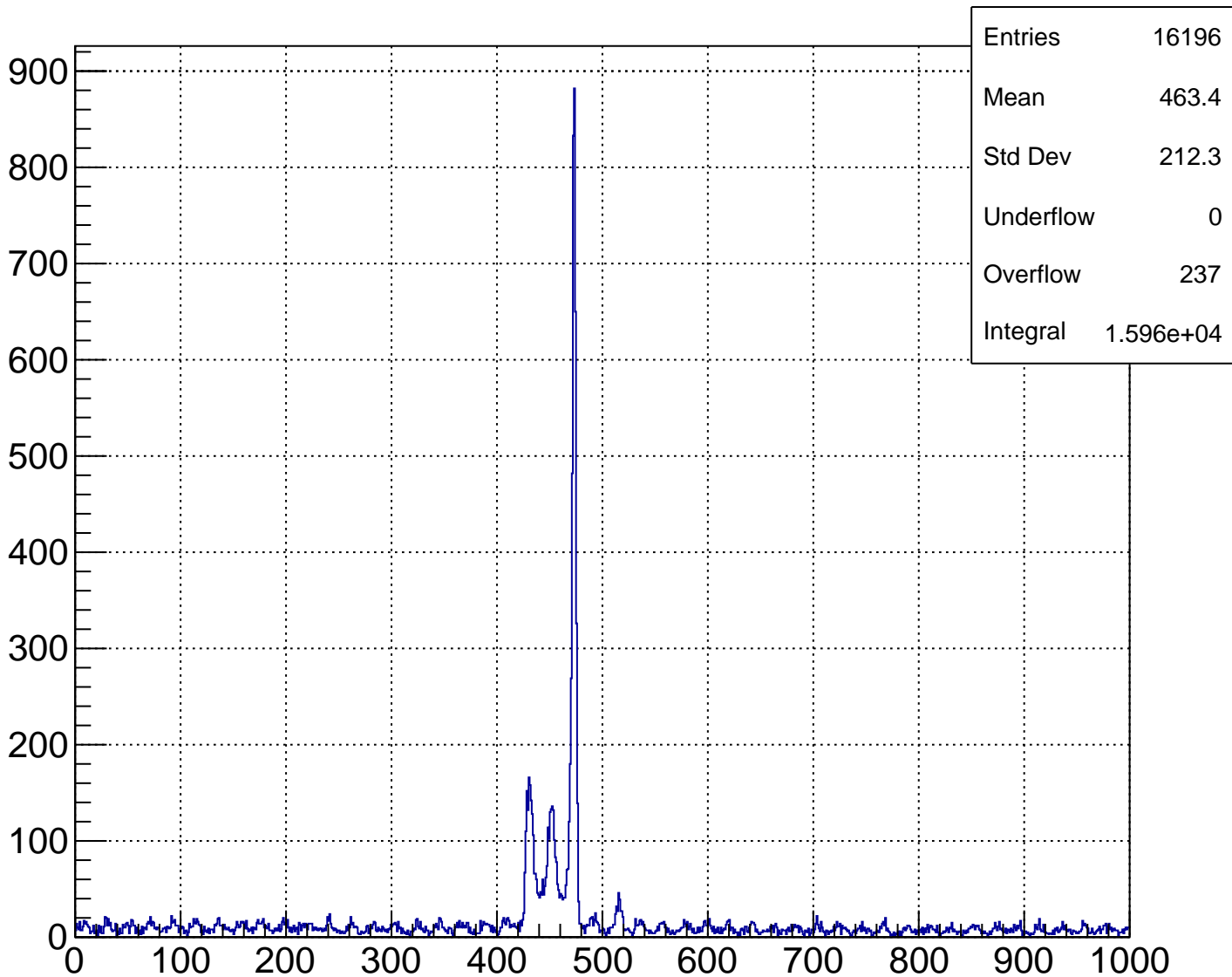
# SCH Tdc 41



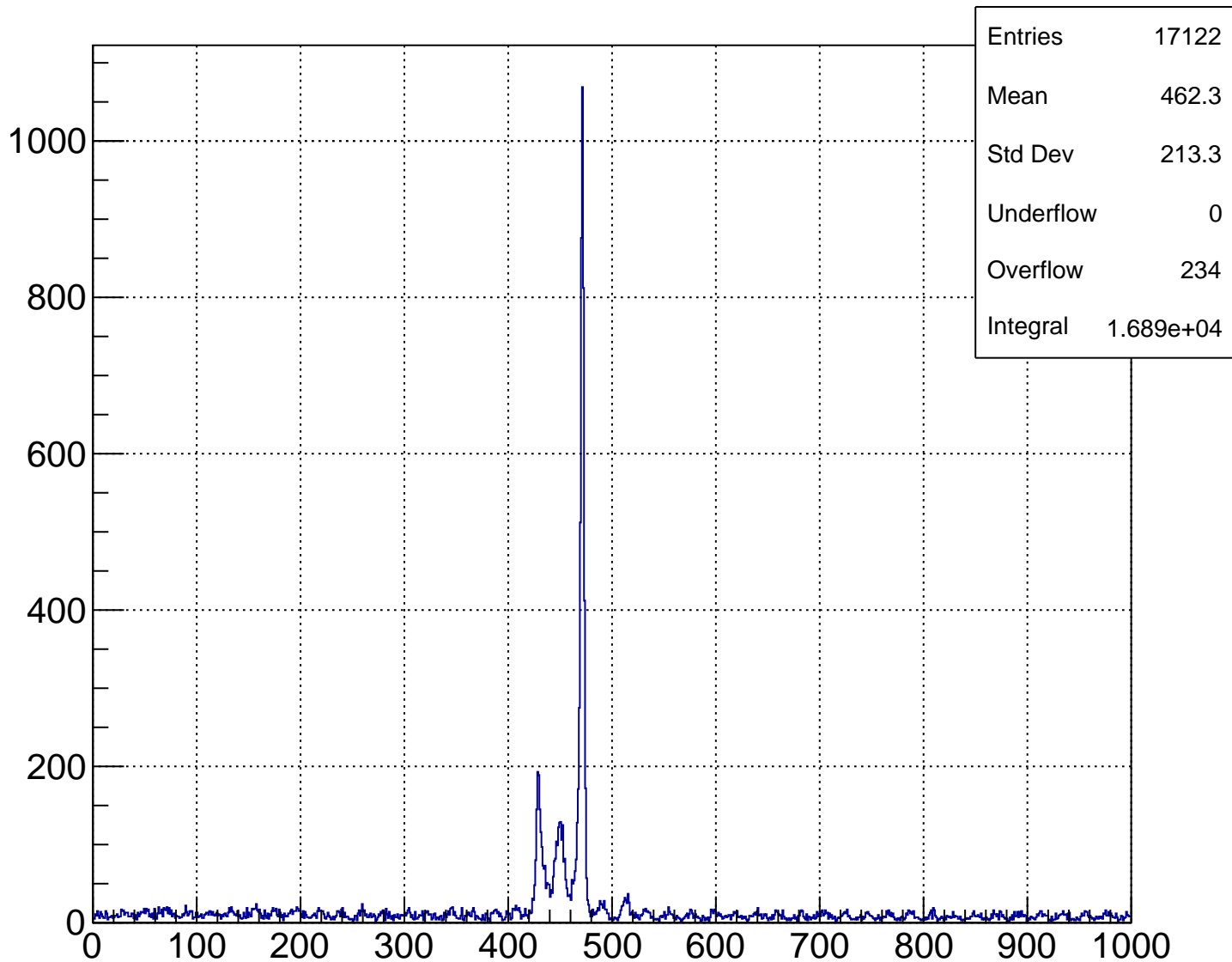
# SCH Tdc 42



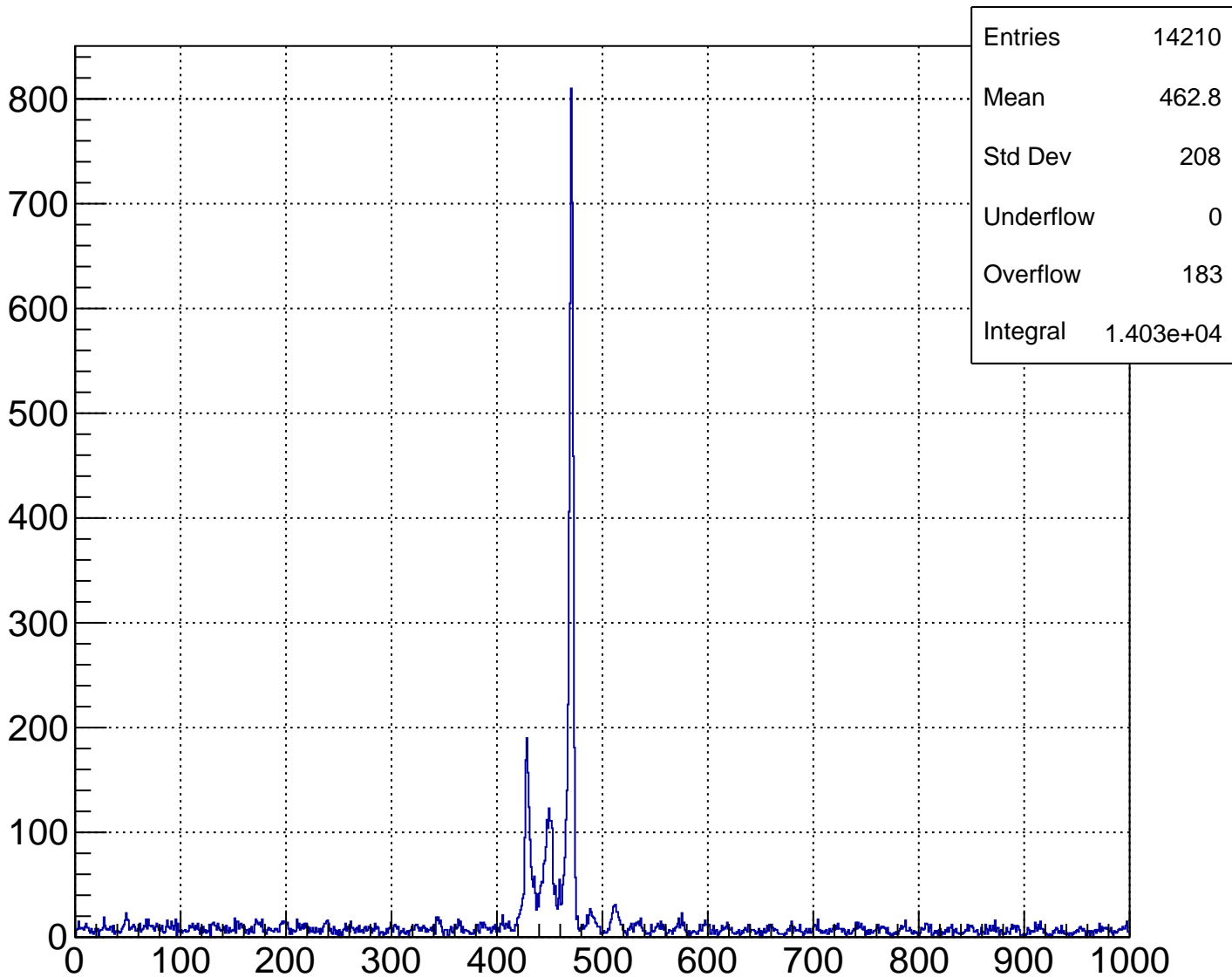
# SCH Tdc 43



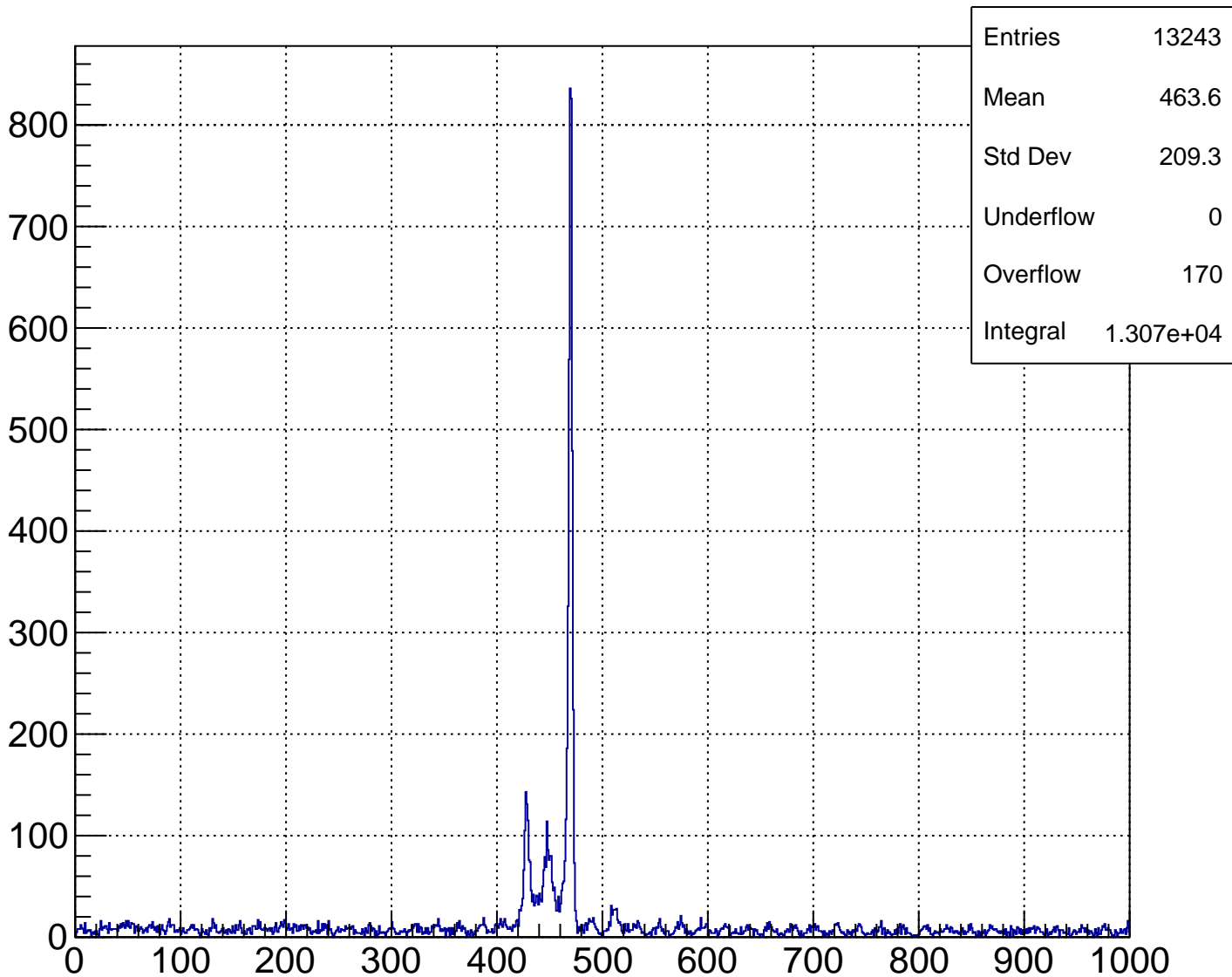
# SCH Tdc 44



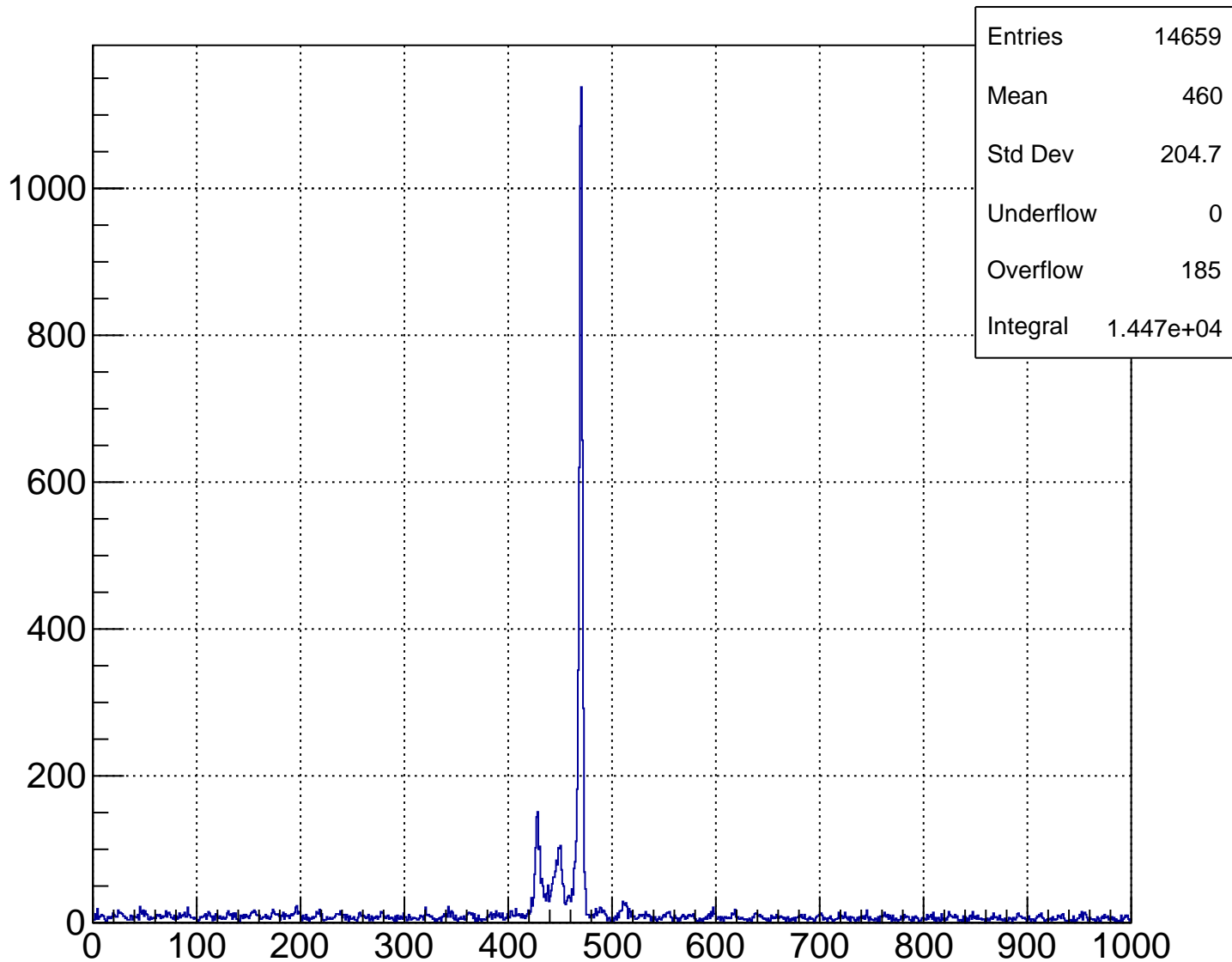
# SCH Tdc 45



# SCH Tdc 46

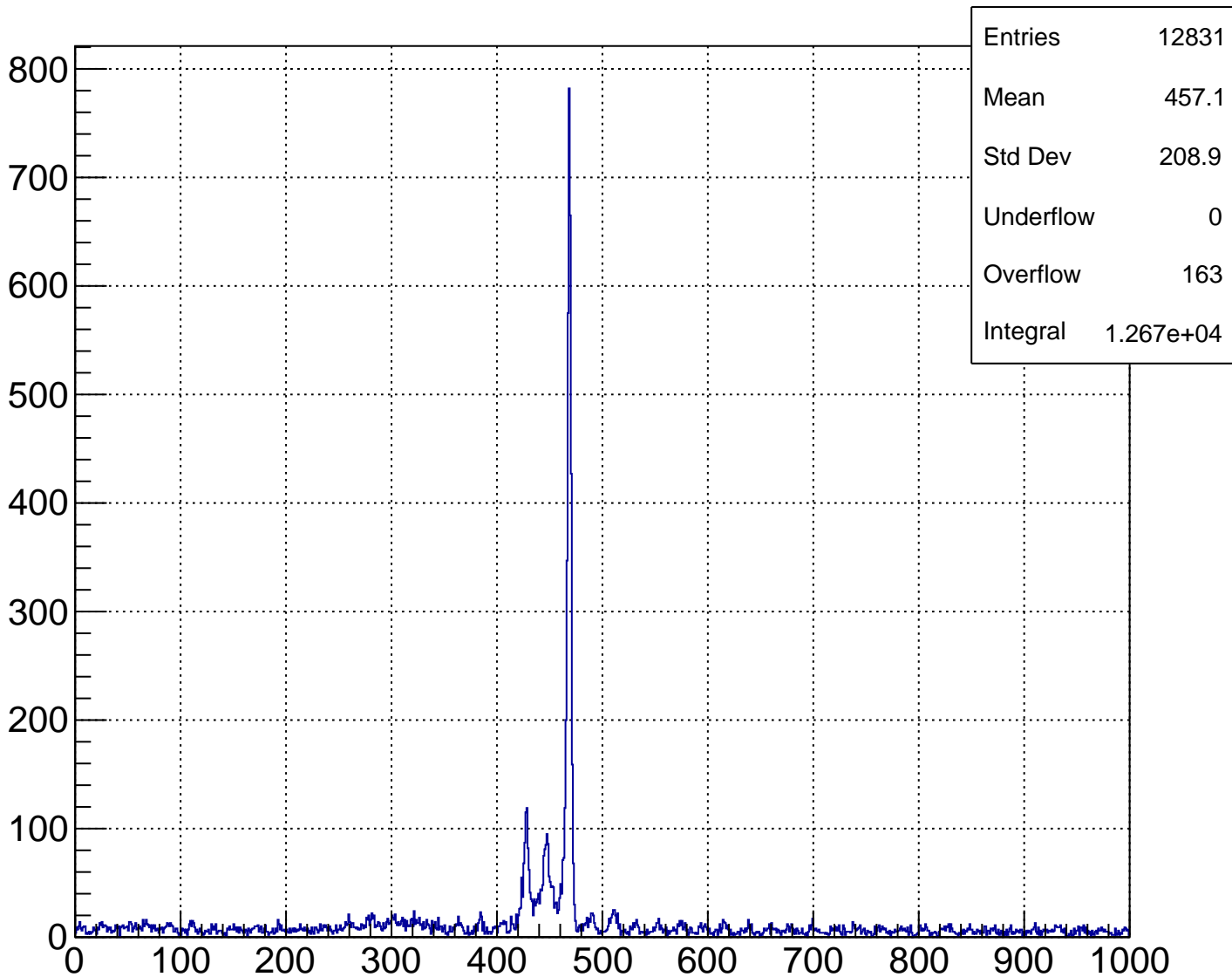


# SCH Tdc 47

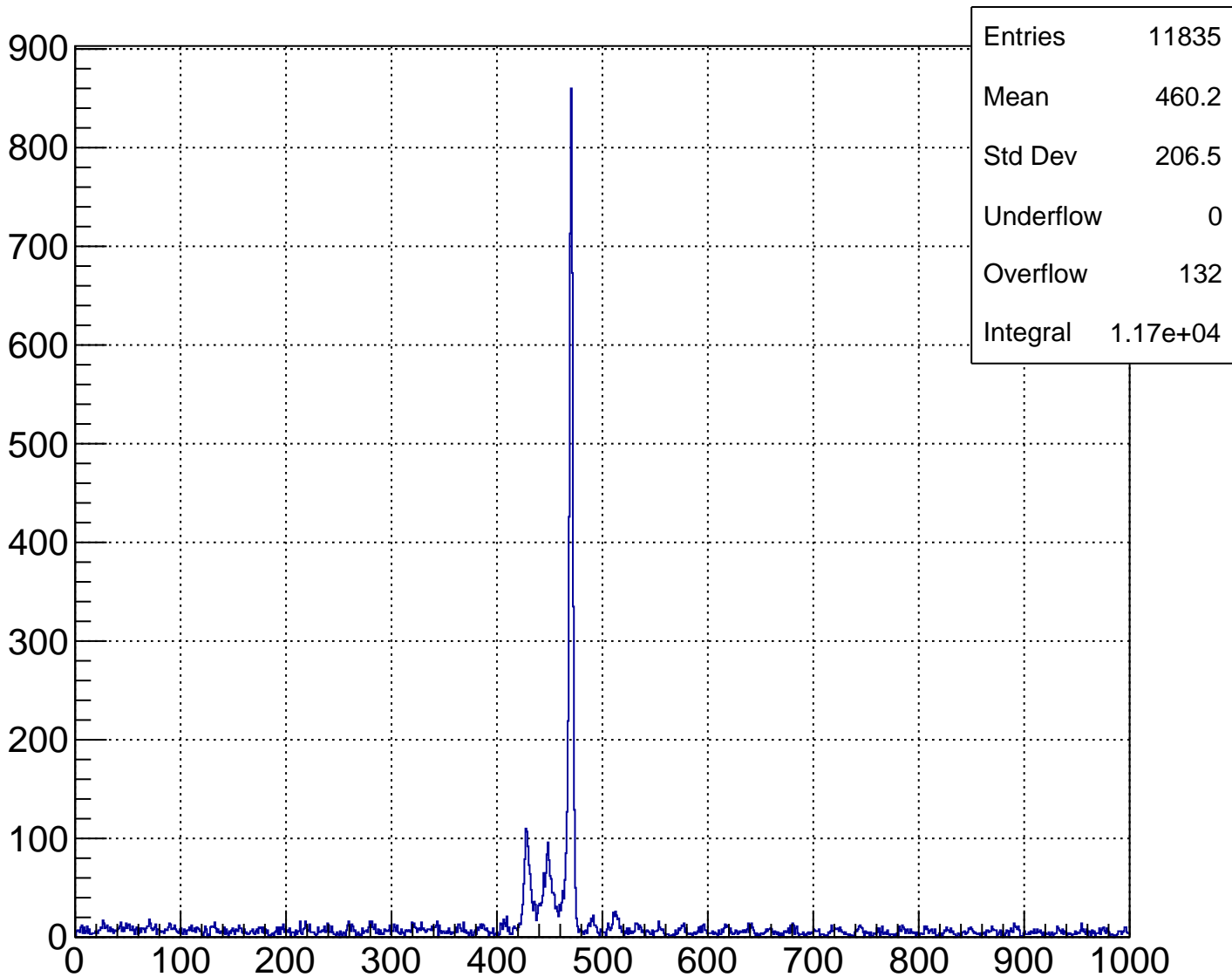




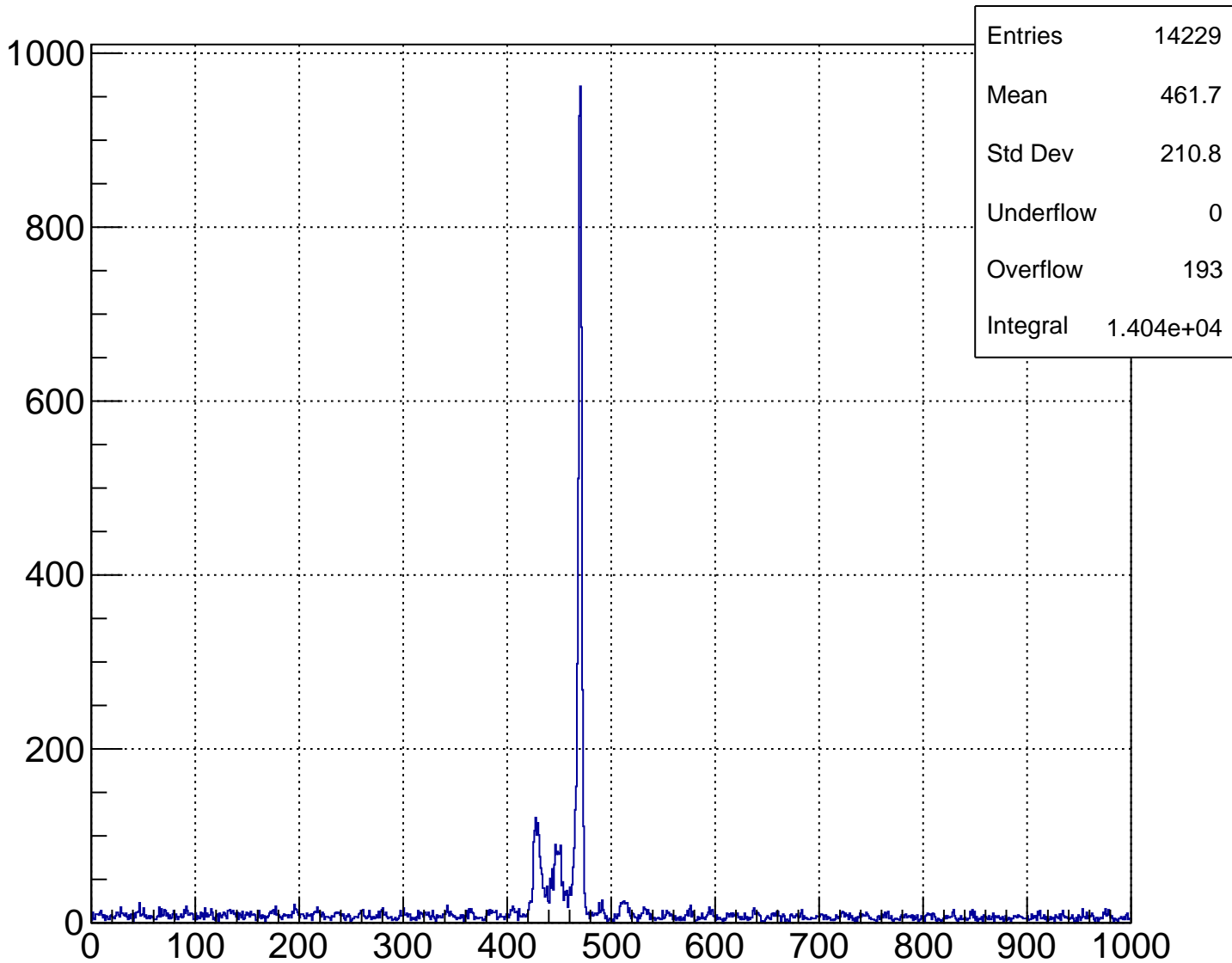
# SCH Tdc 48



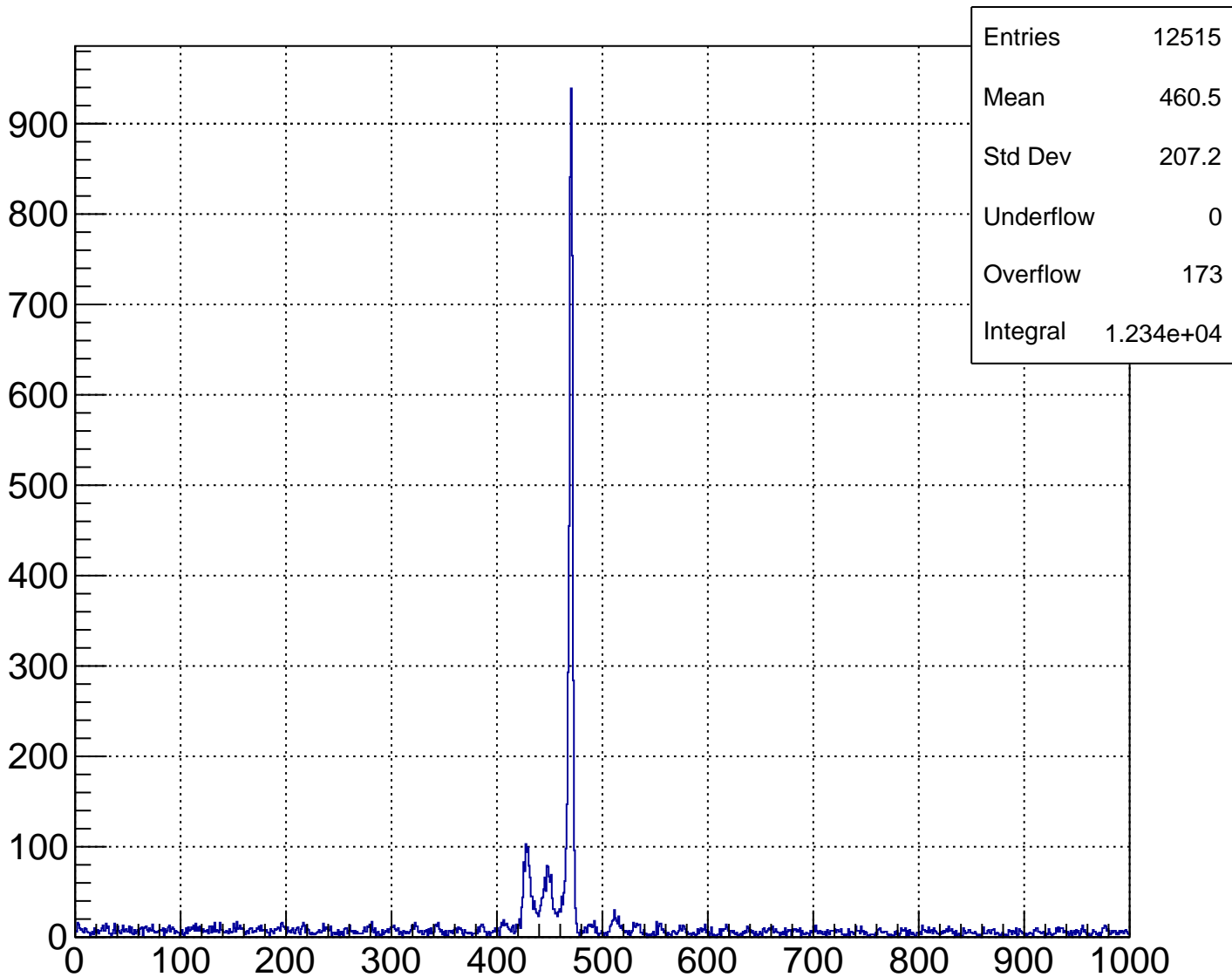
# SCH Tdc 49



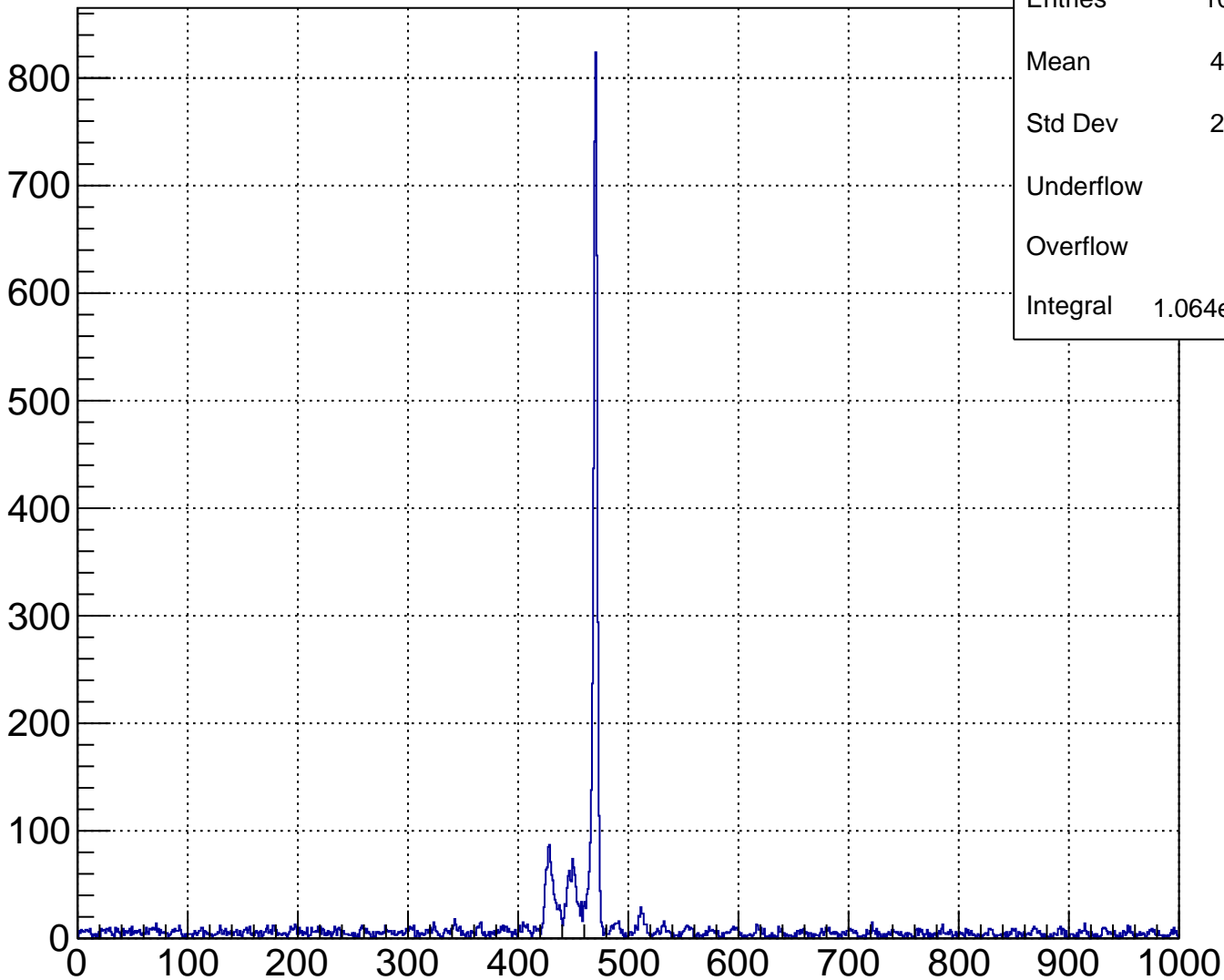
# SCH Tdc 50



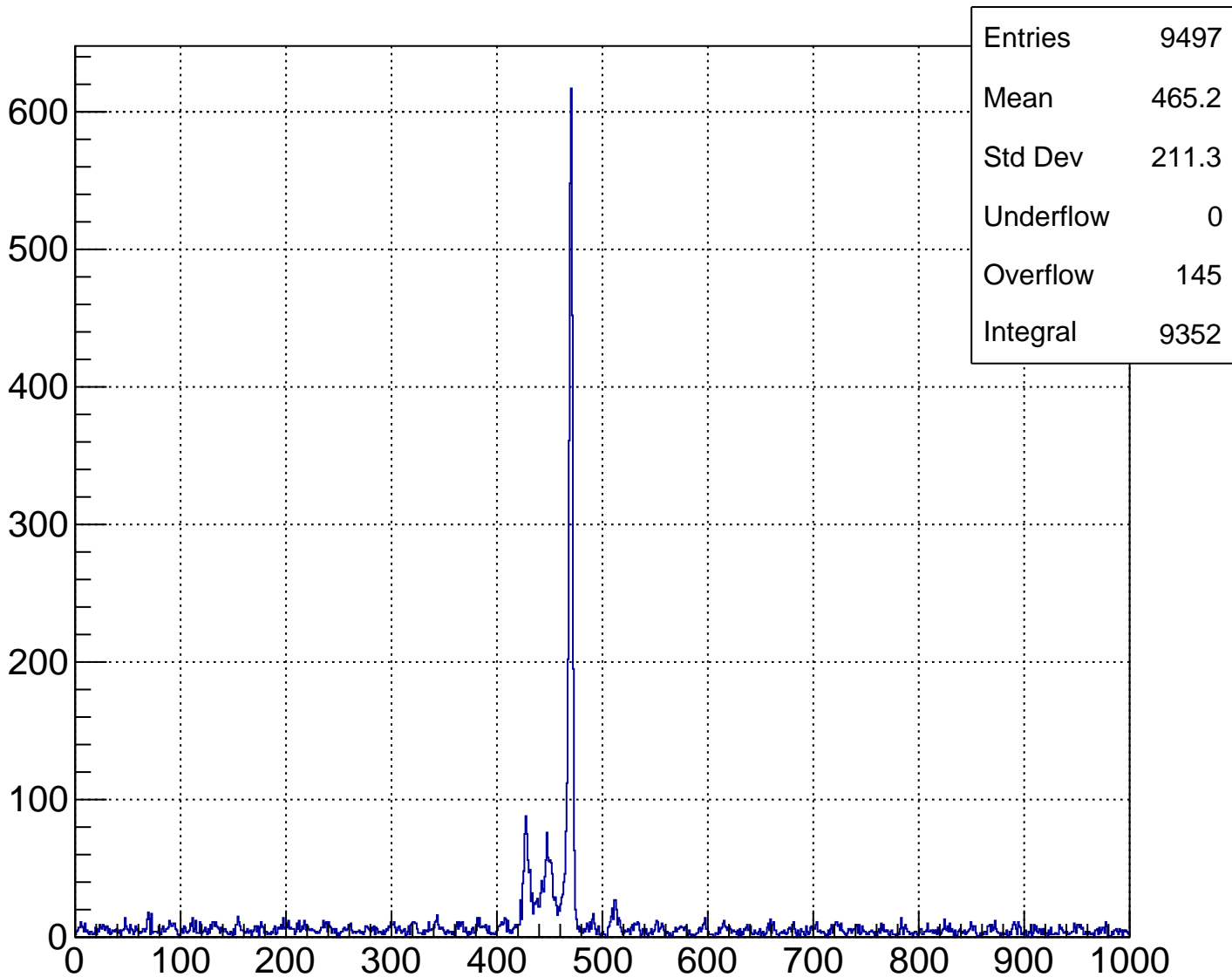
# SCH Tdc 51



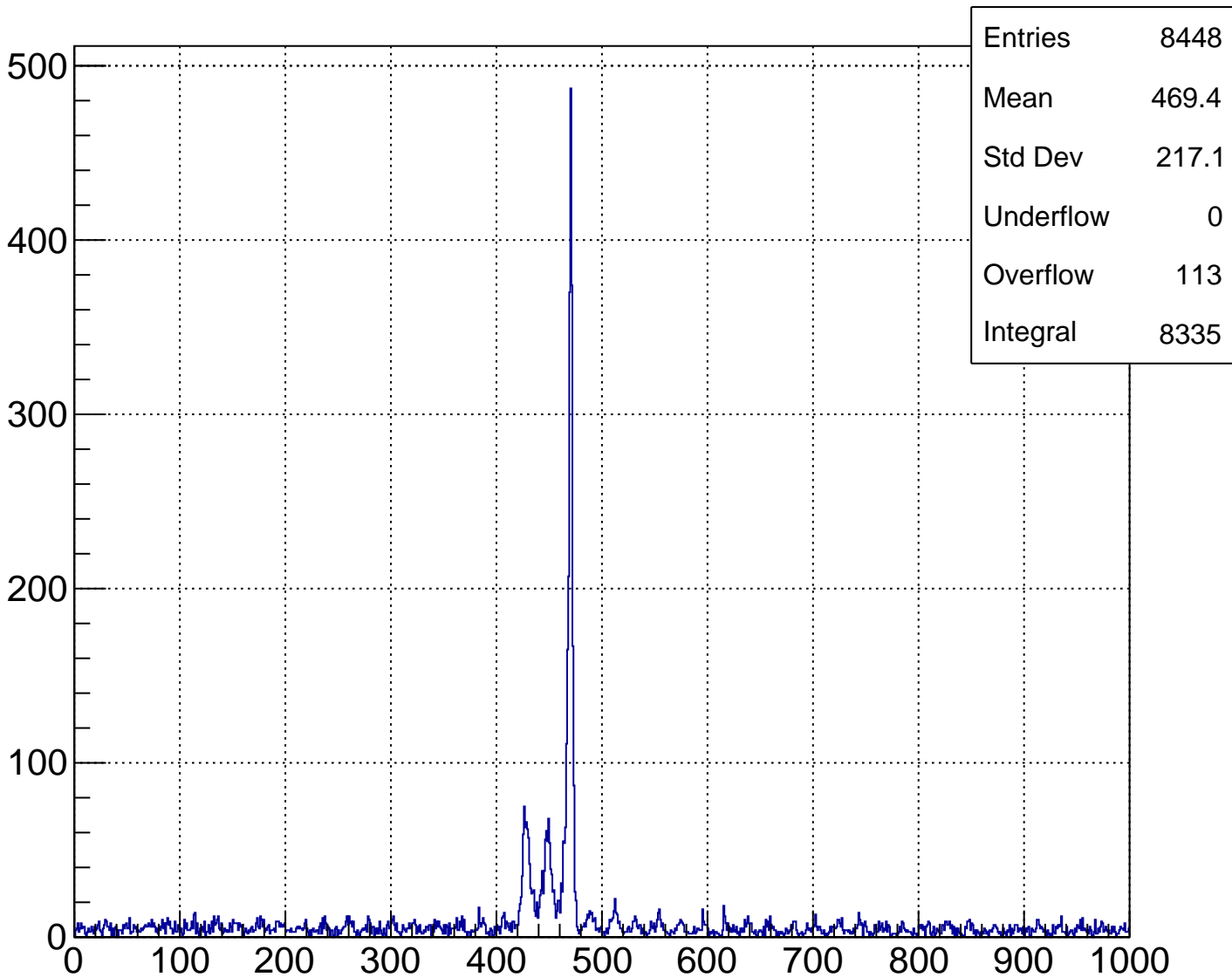
# SCH Tdc 52



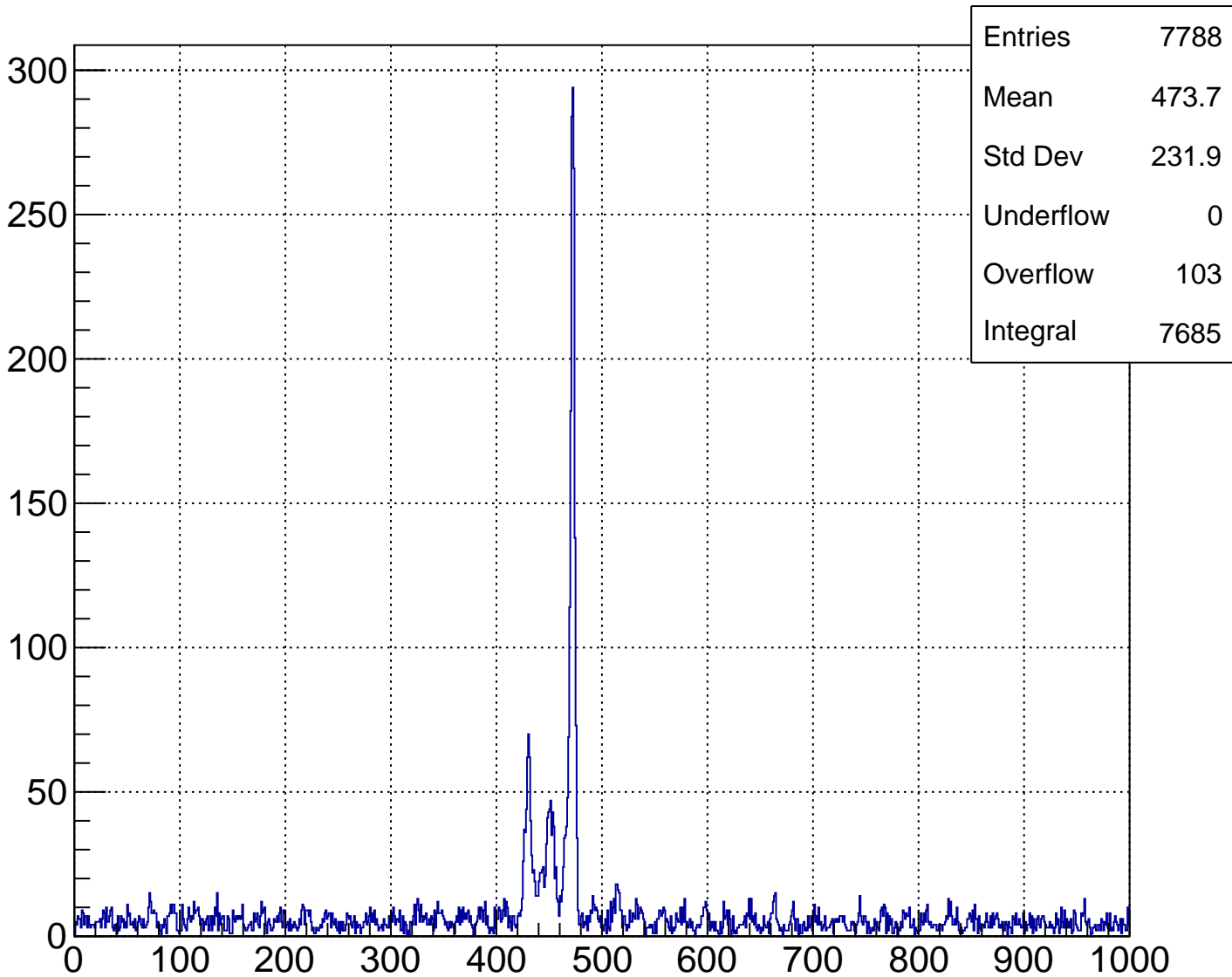
# SCH Tdc 53



# SCH Tdc 54

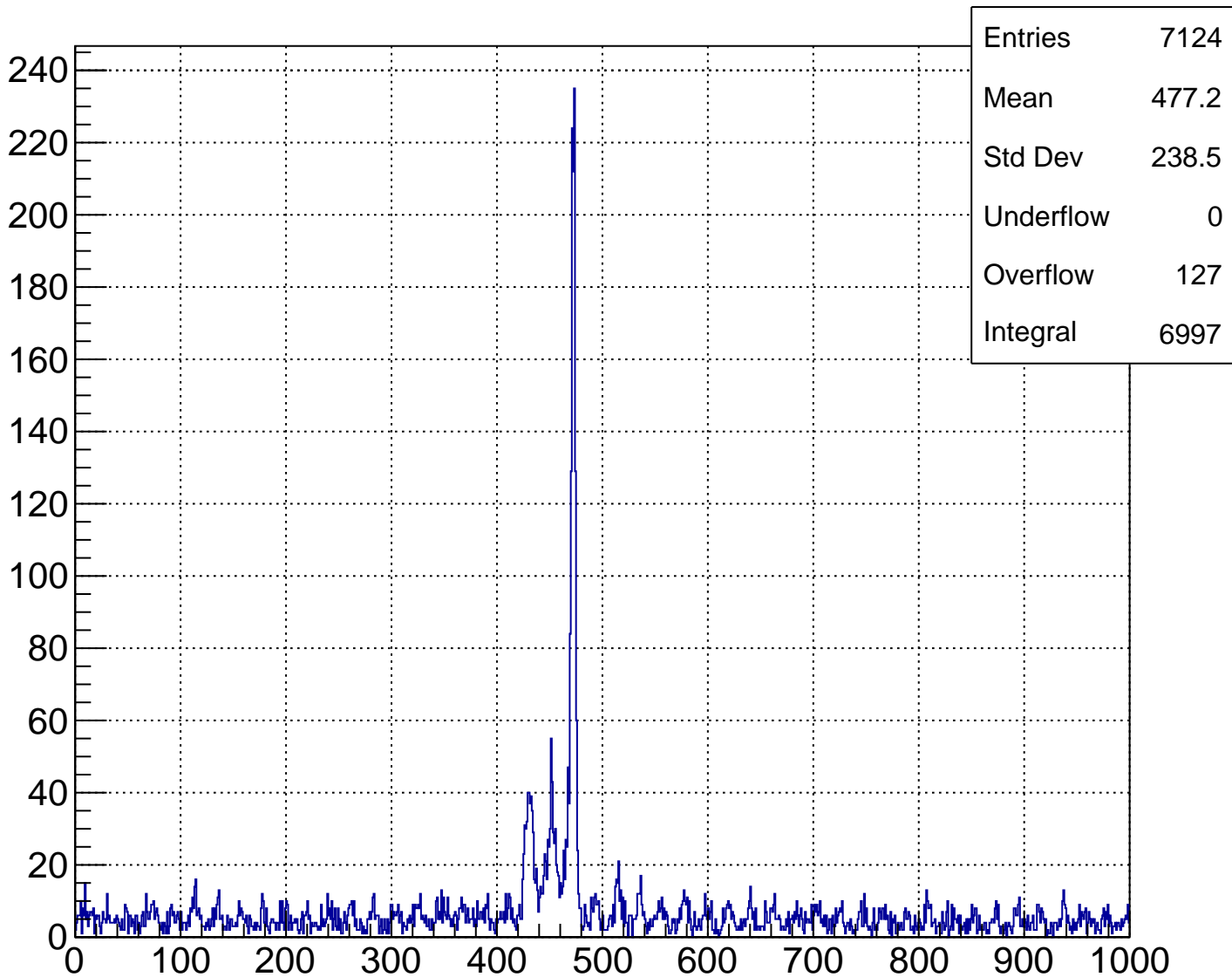


# SCH Tdc 55

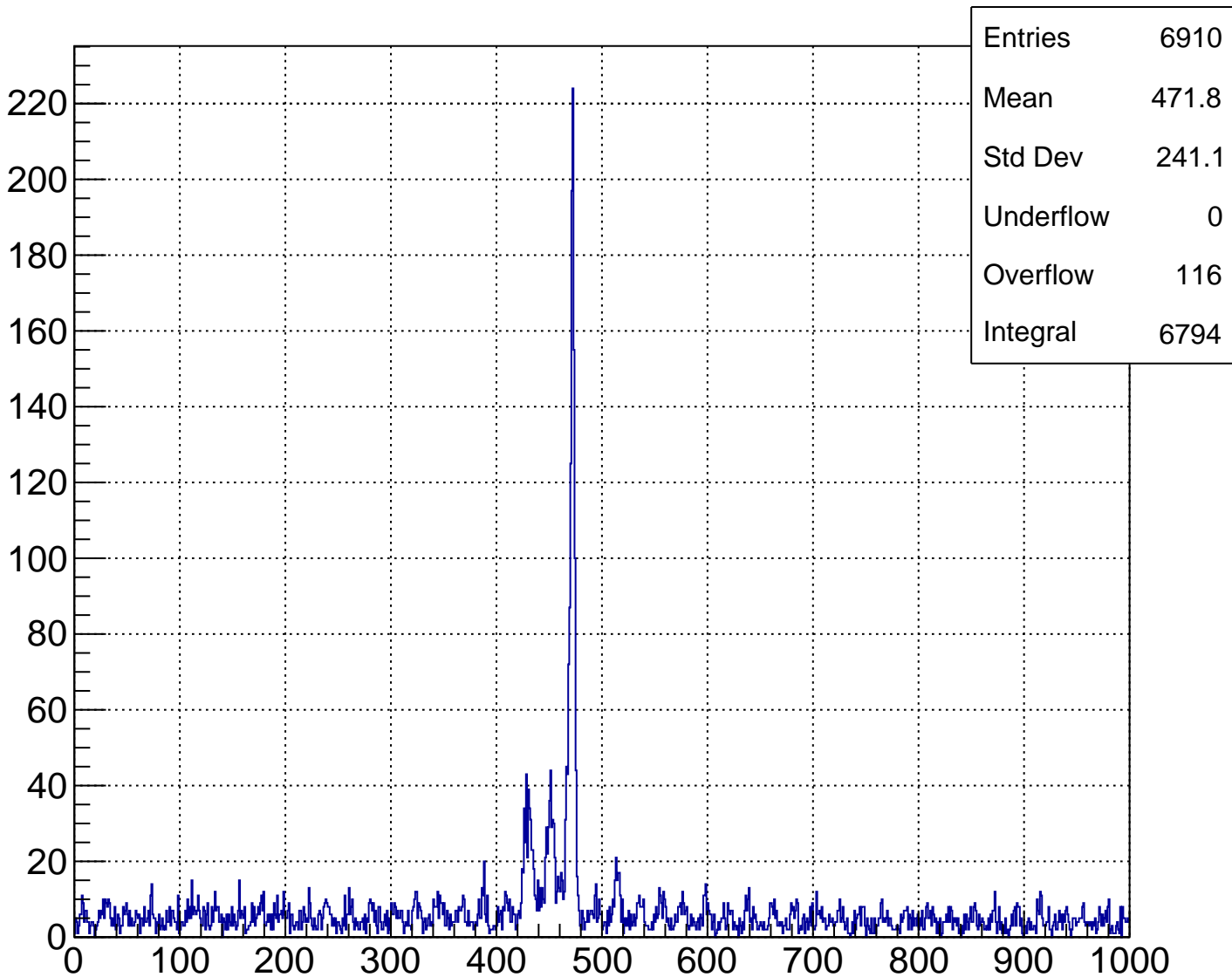




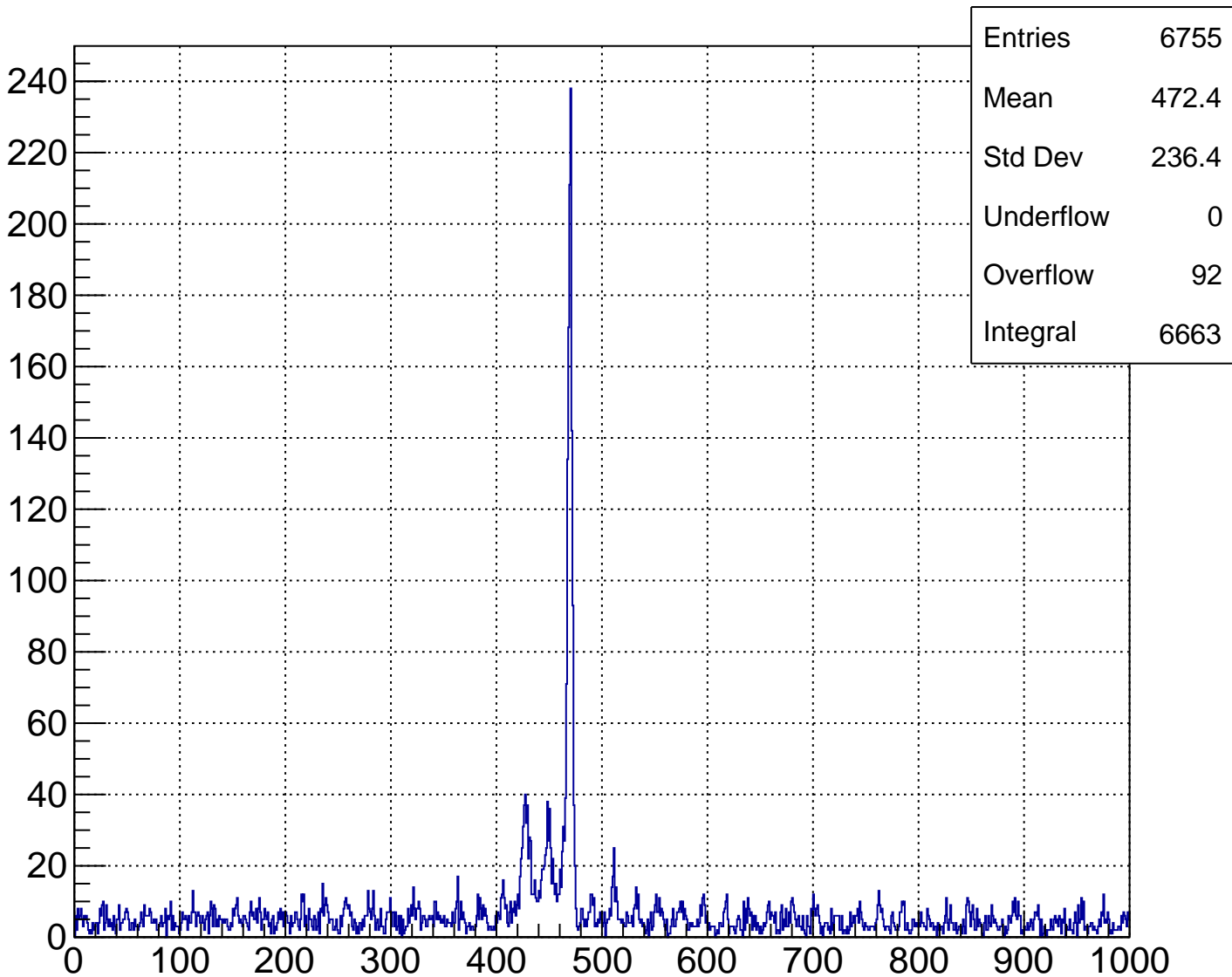
# SCH Tdc 56



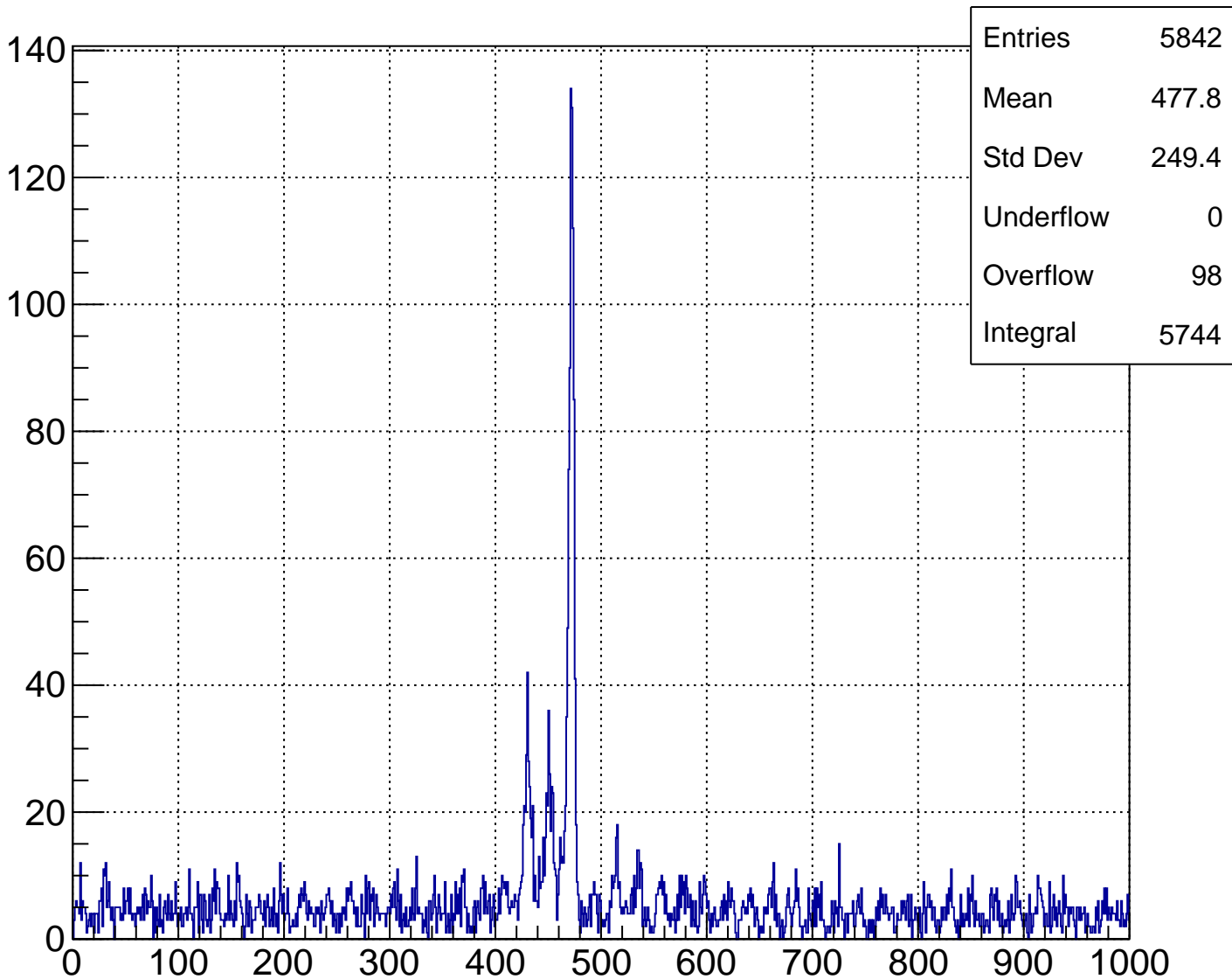
# SCH Tdc 57



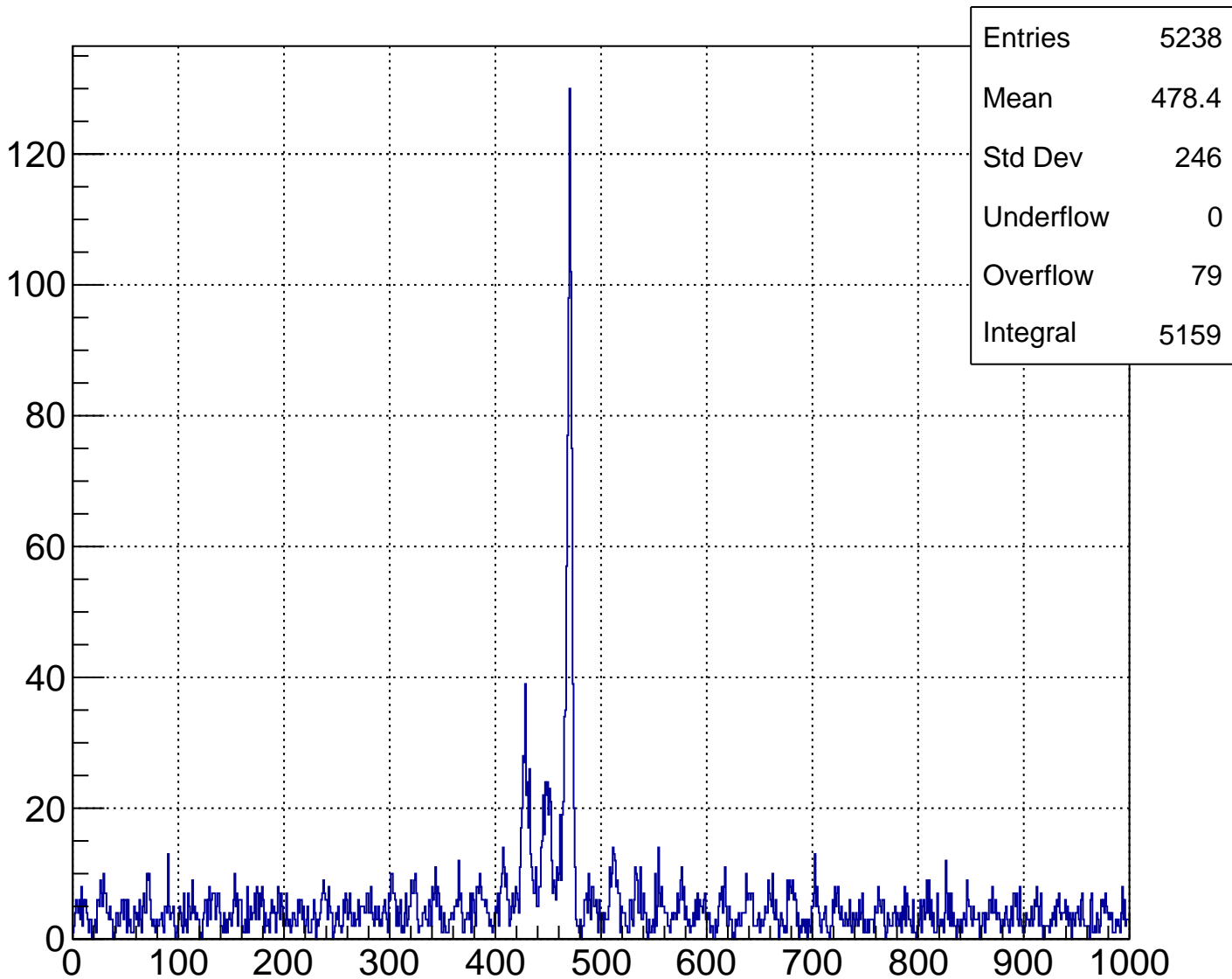
# SCH Tdc 58



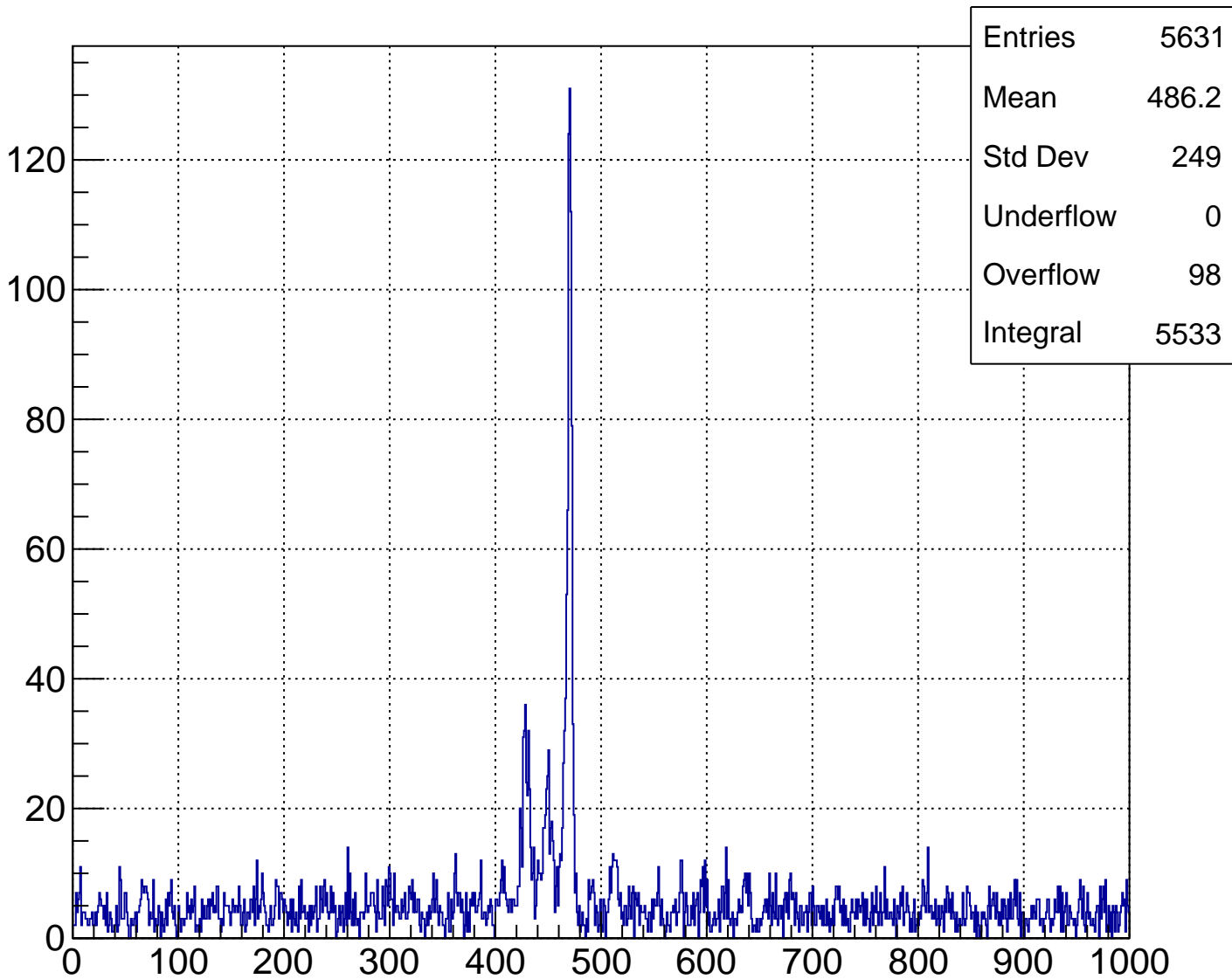
# SCH Tdc 59



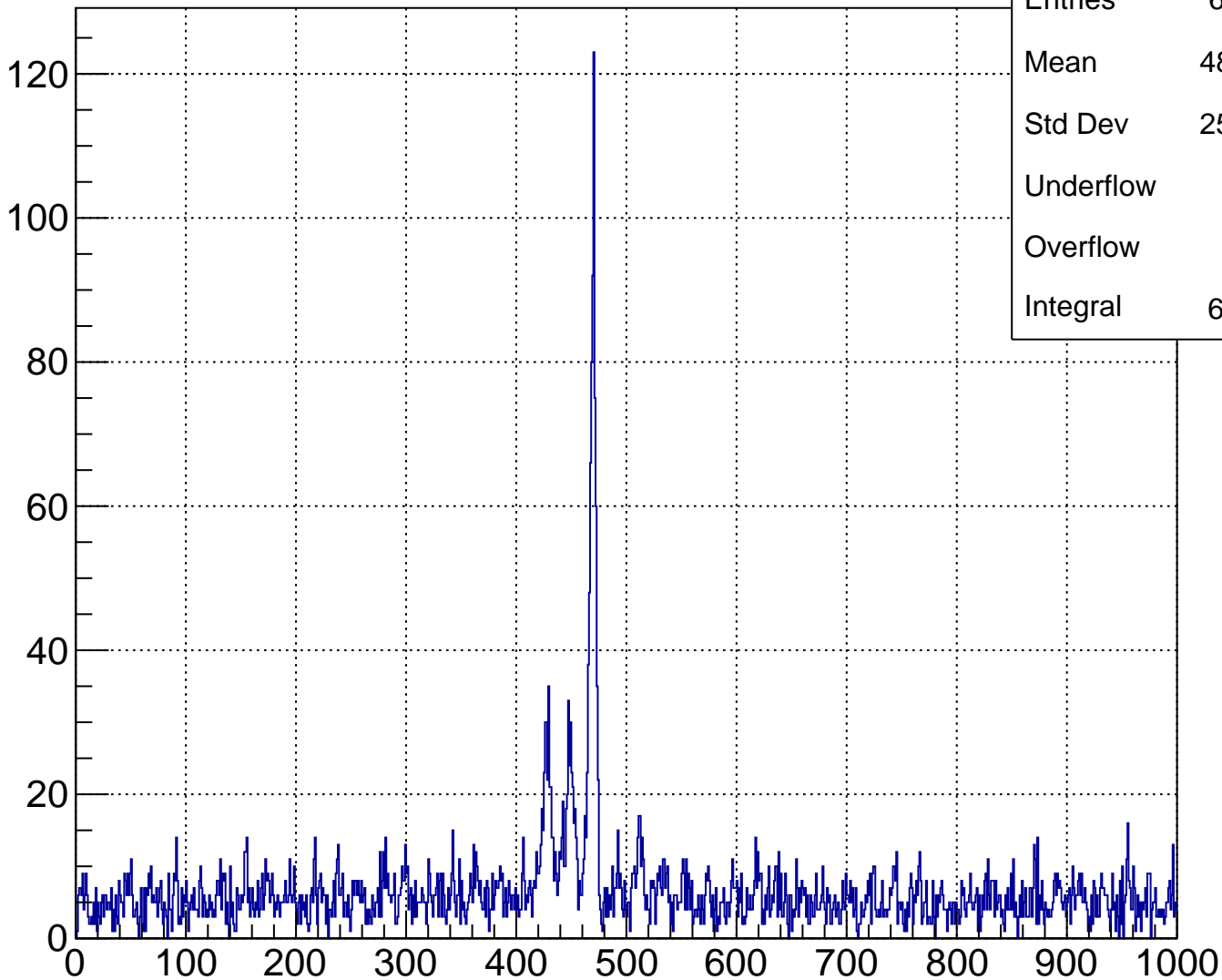
# SCH Tdc 60



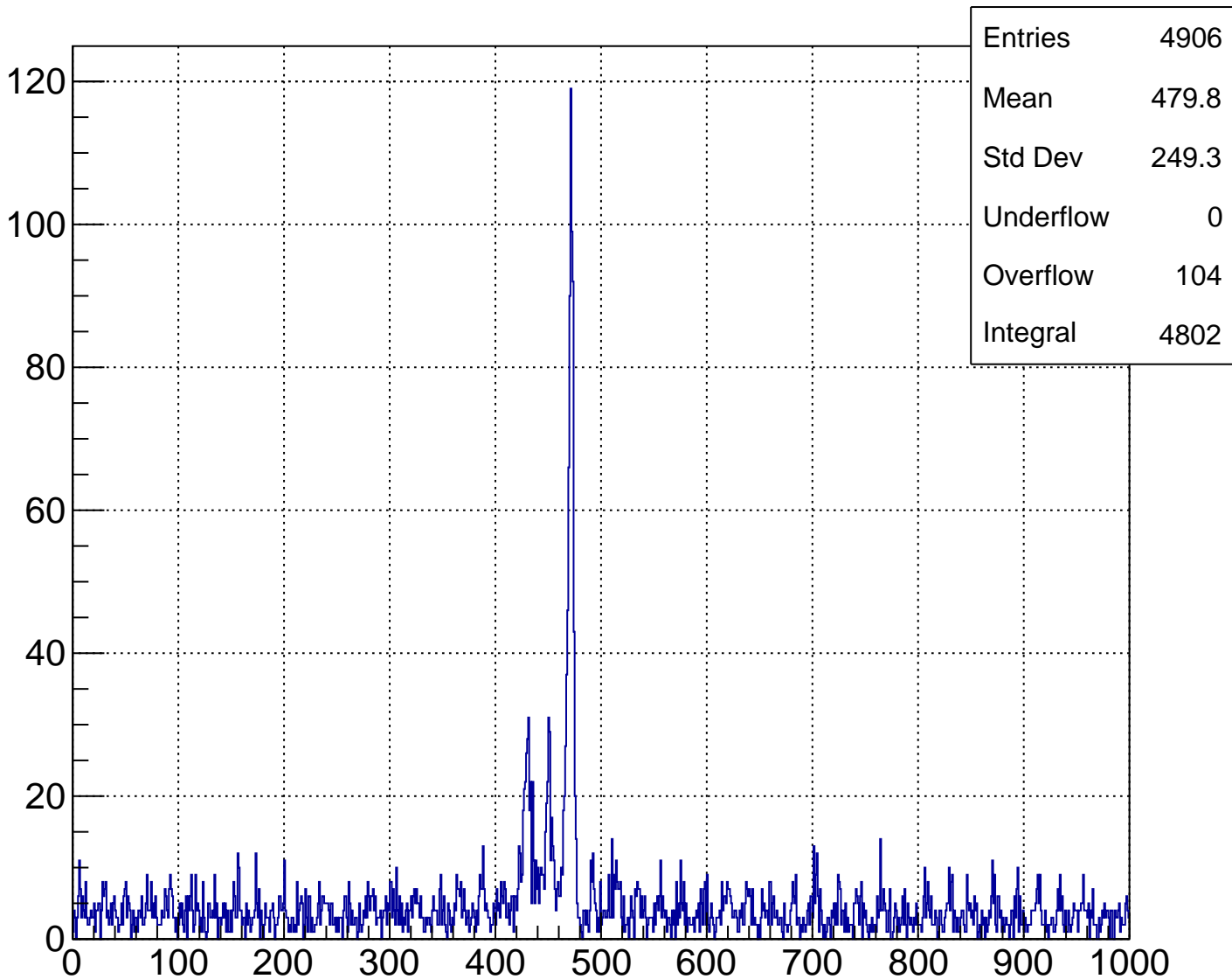
# SCH Tdc 61



# SCH Tdc 62



# SCH Tdc 63





# SCH Tdc 64

