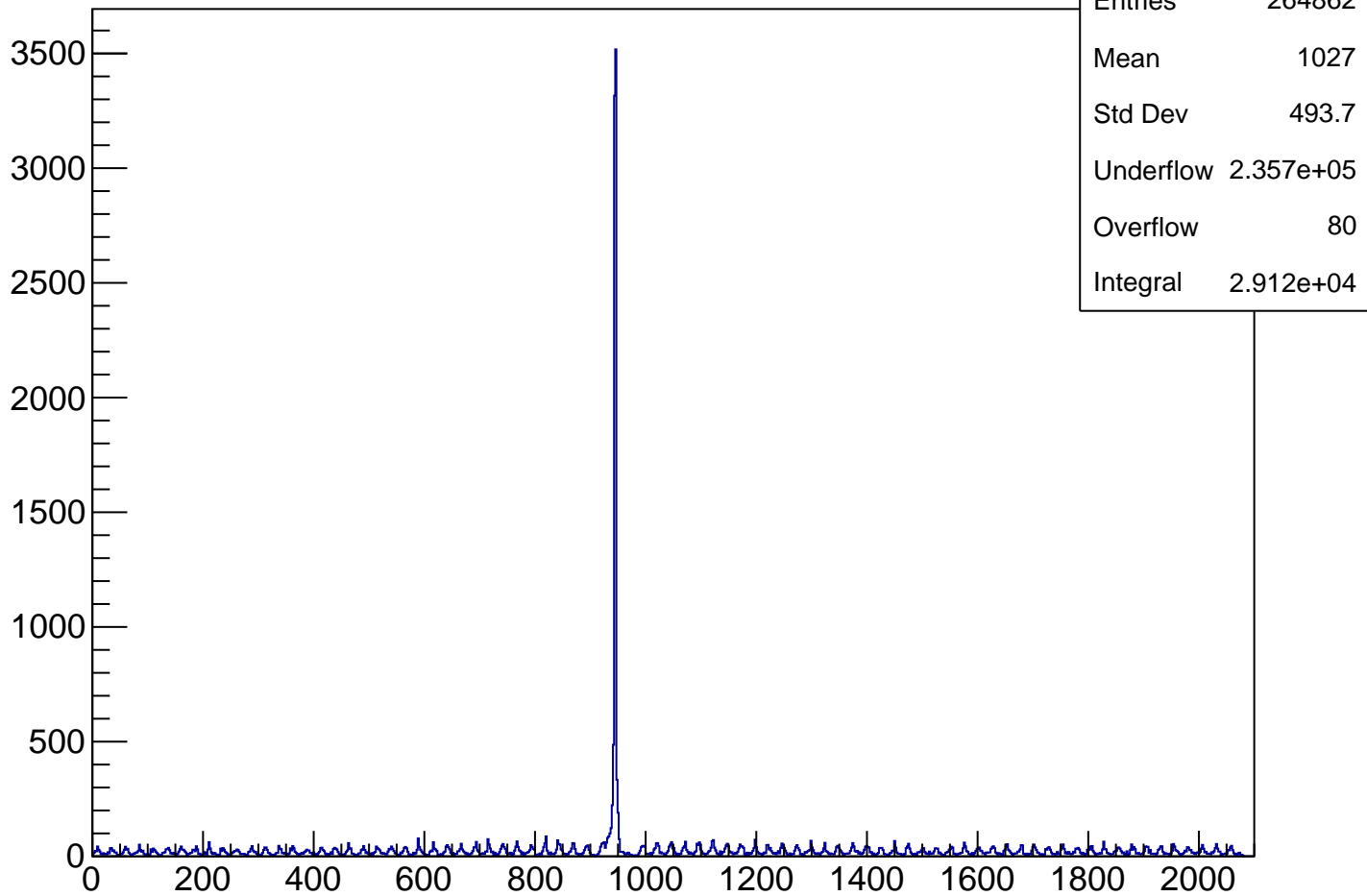
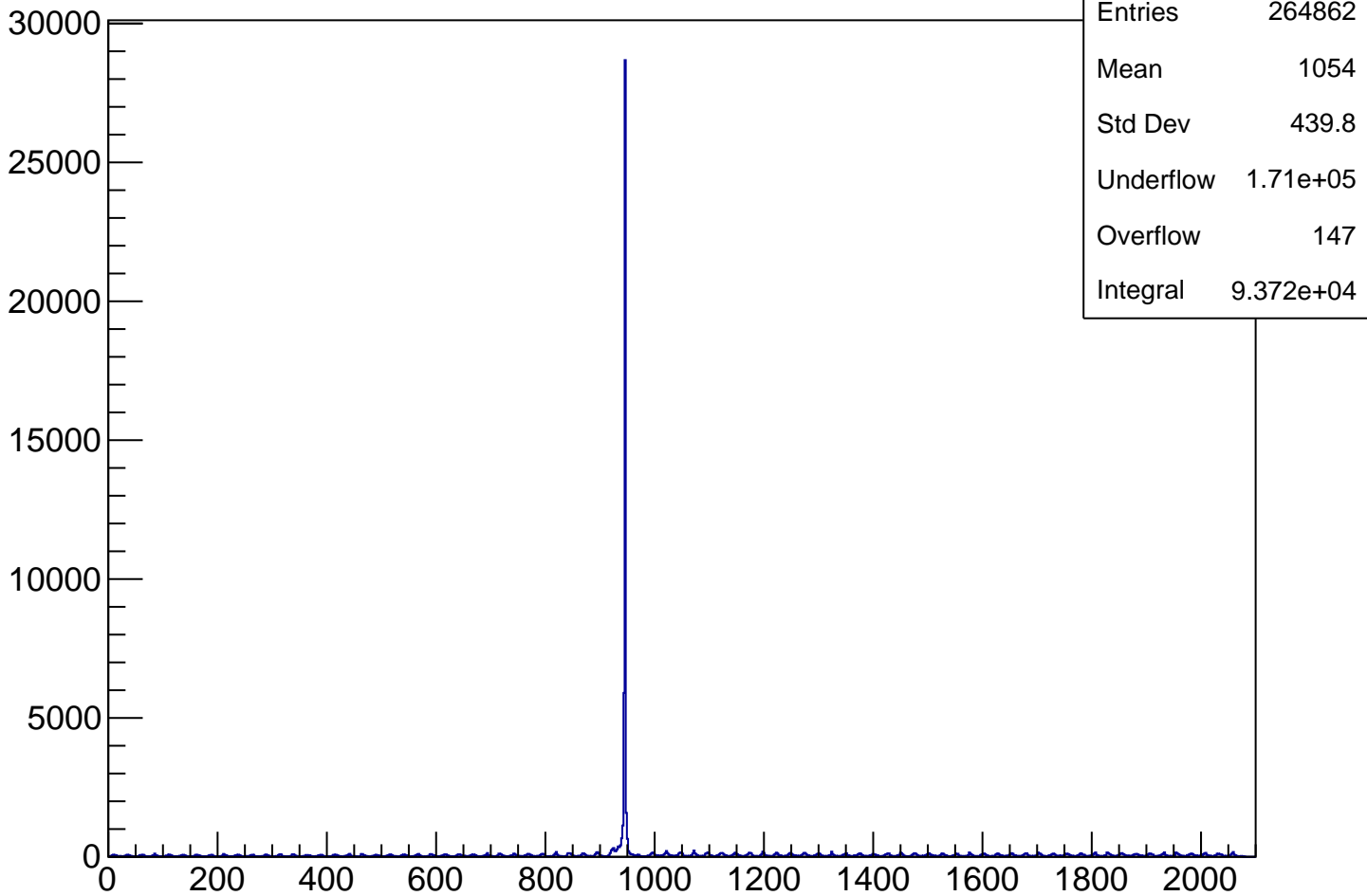


# TrigFlag Bh21K



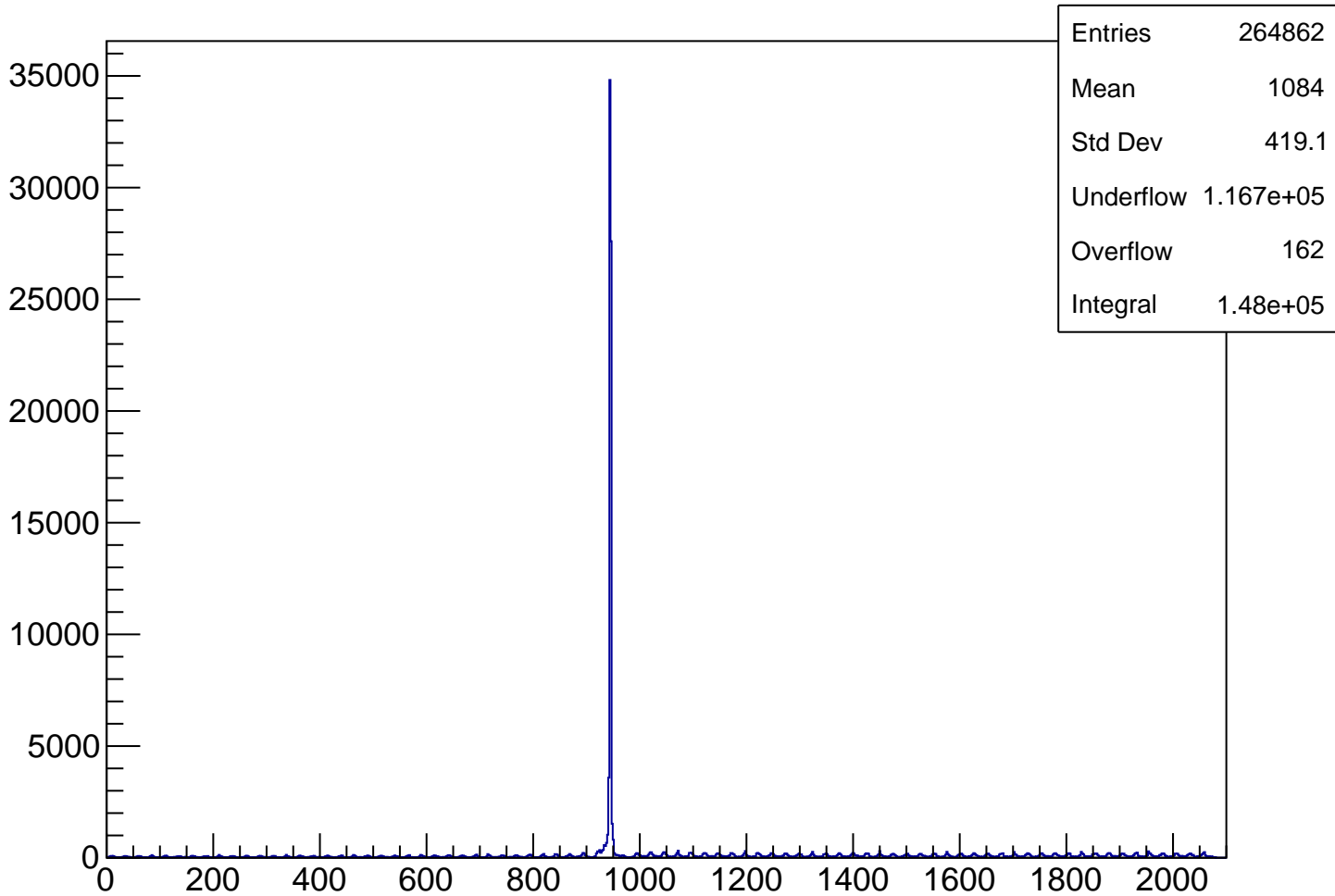
# TrigFlag Bh22K



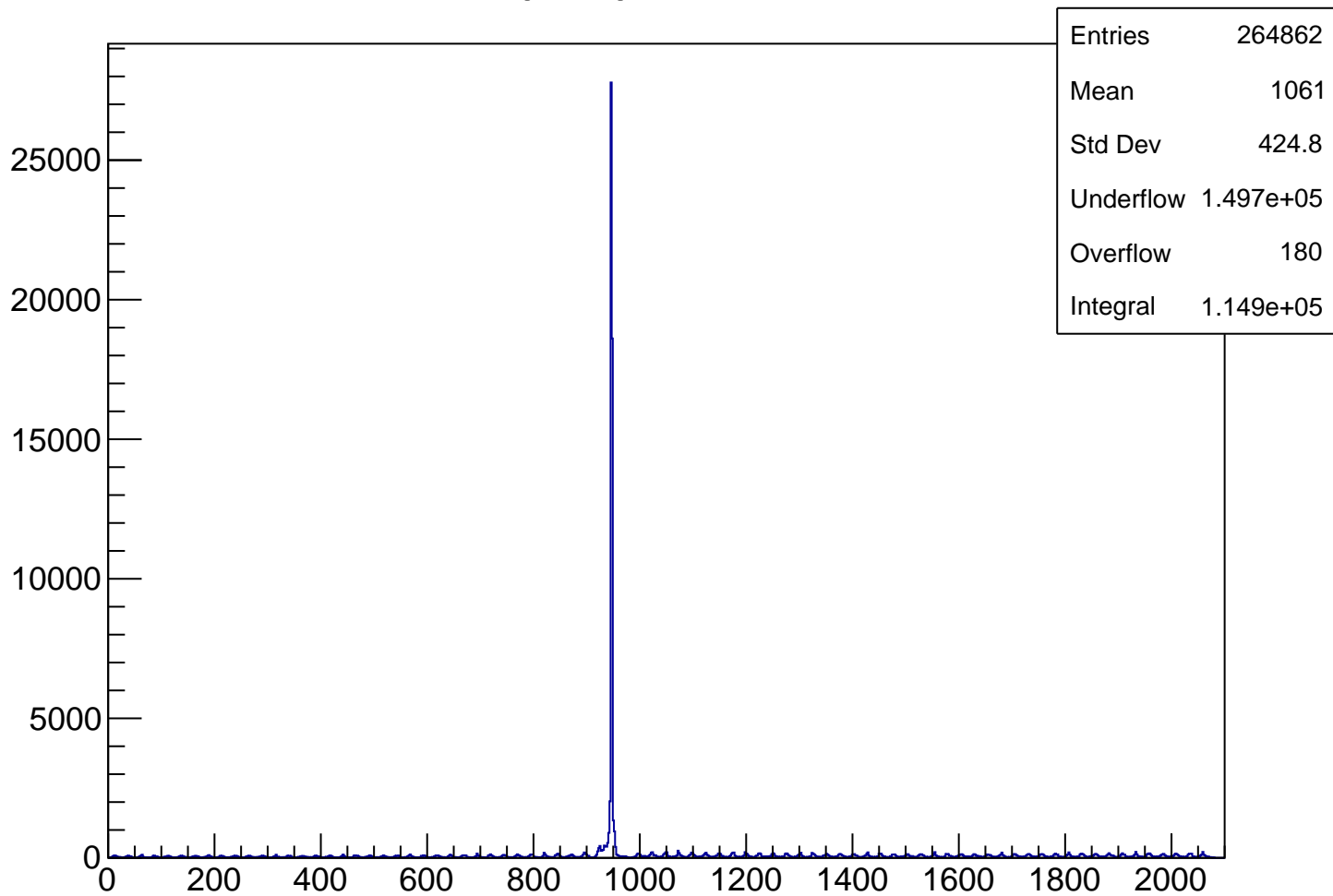
# TrigFlag Bh23K



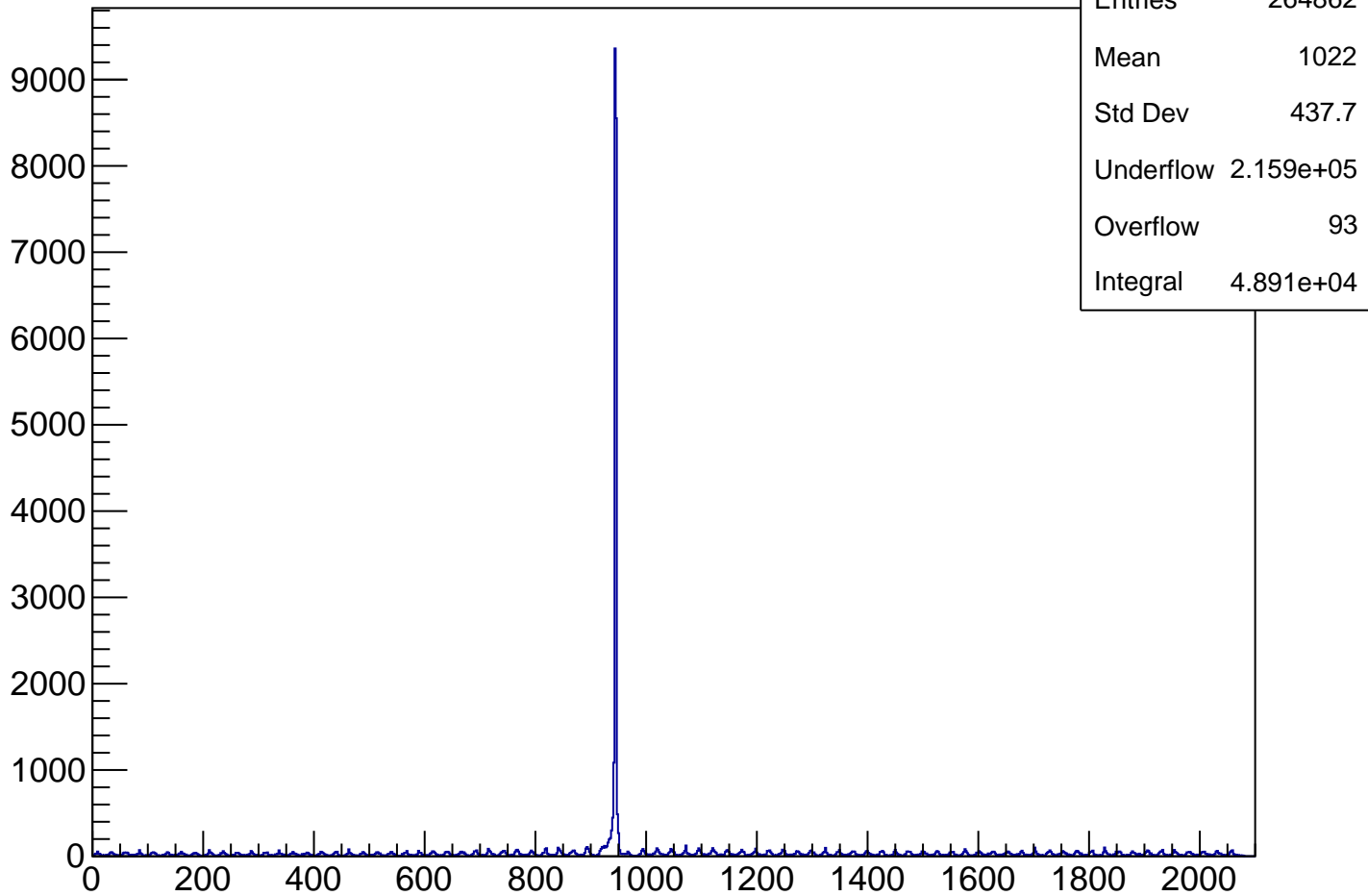
# TrigFlag Bh24K



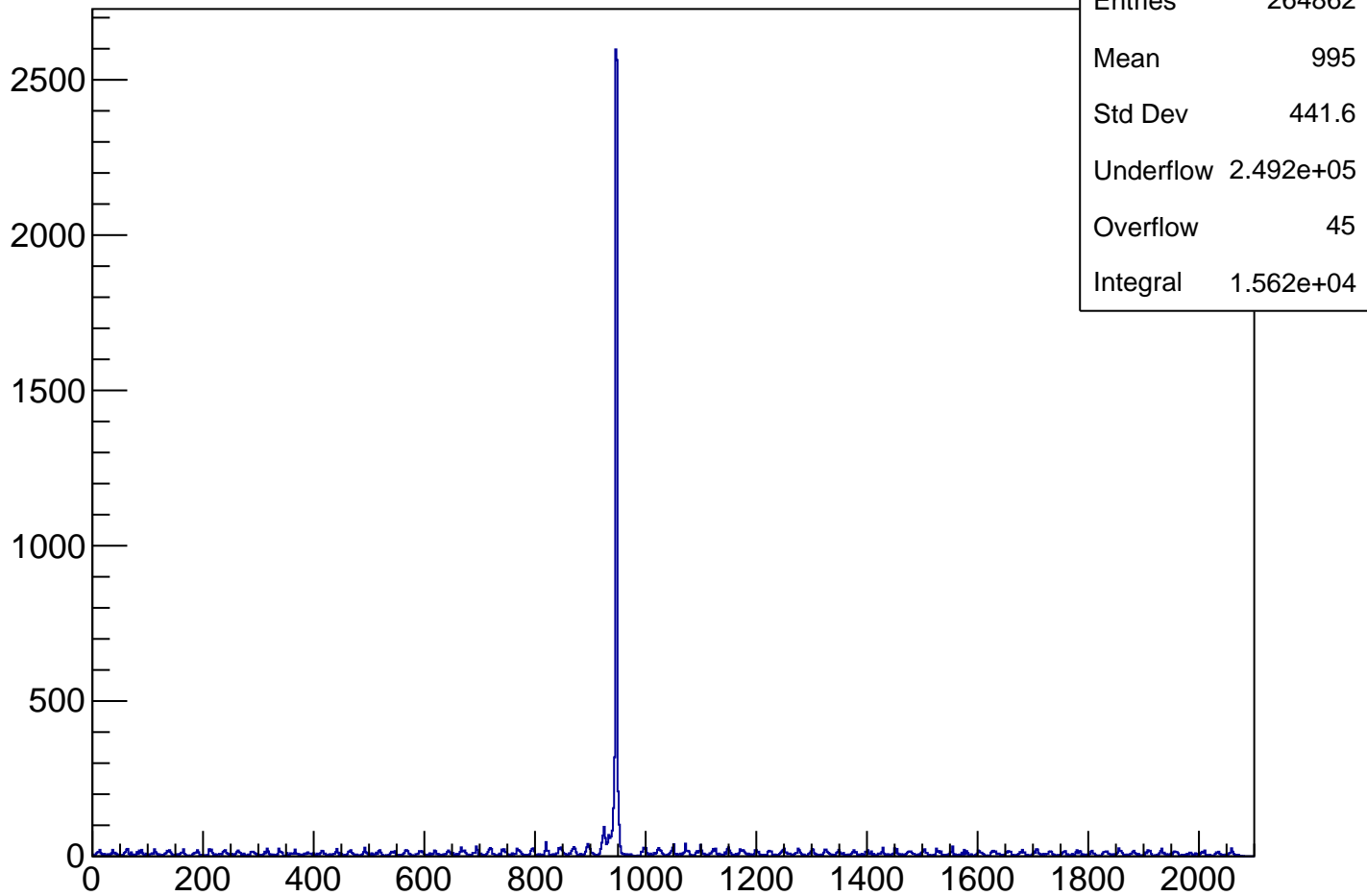
# TrigFlag Bh25K



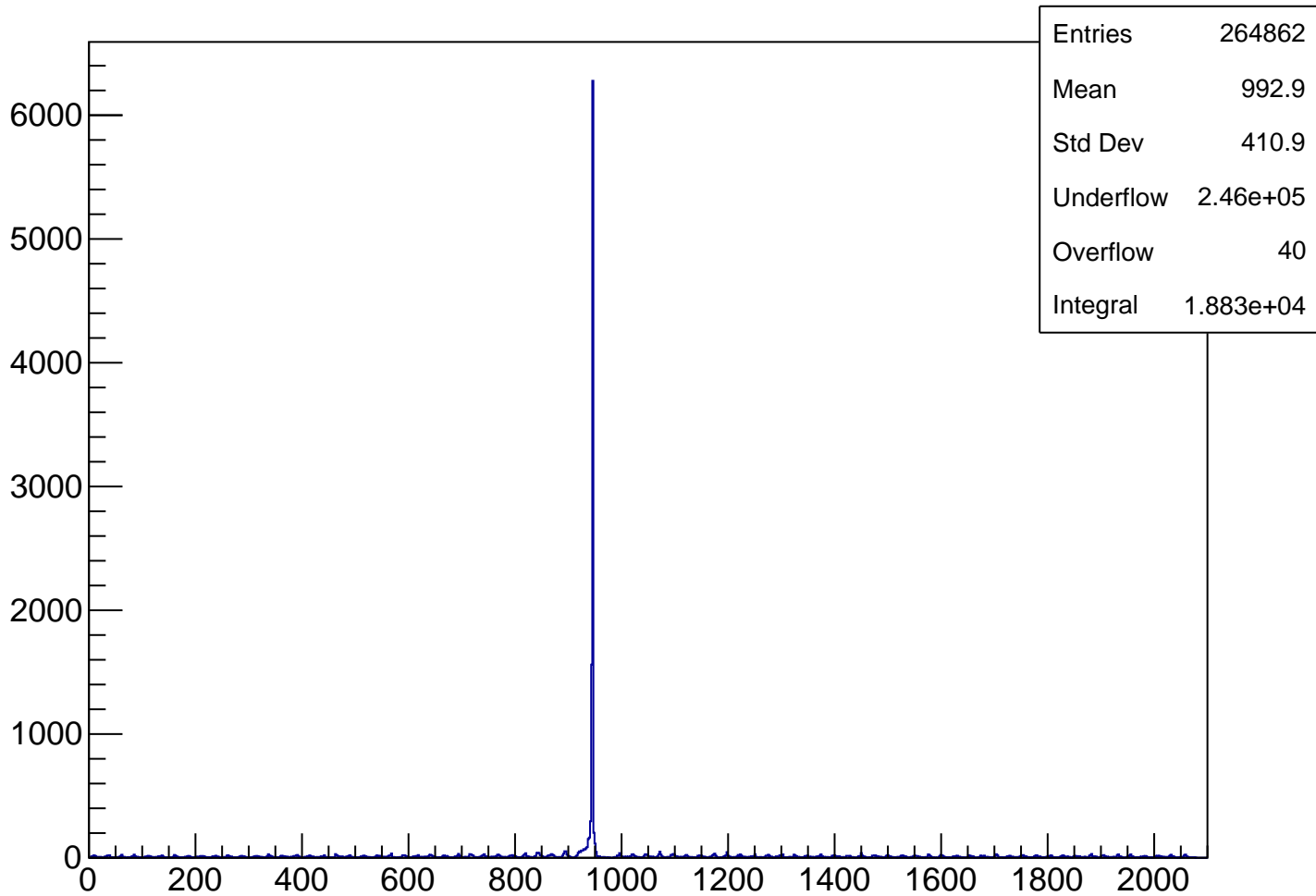
# TrigFlag Bh26K



# TrigFlag Bh27K

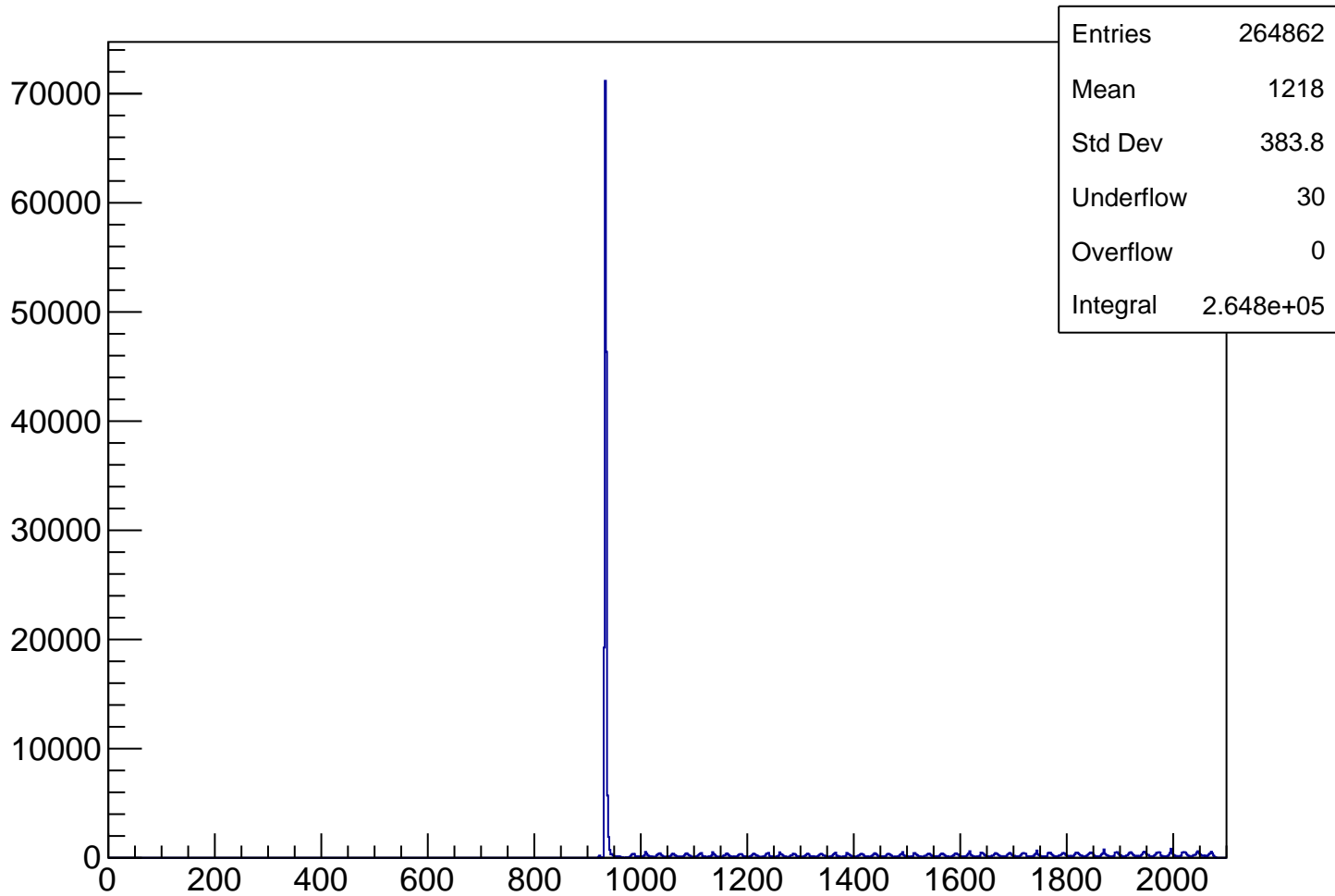


# TrigFlag Bh28K

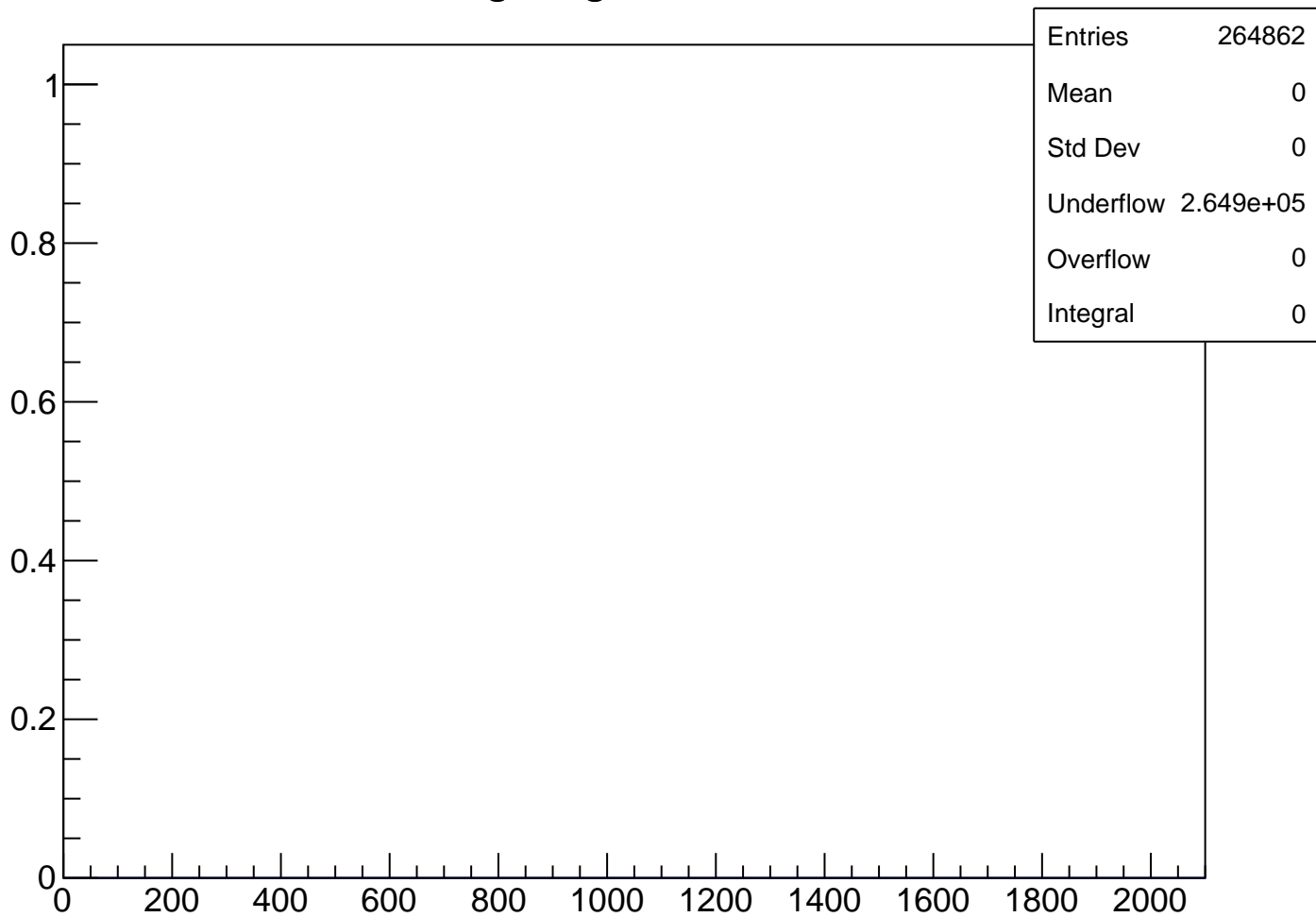




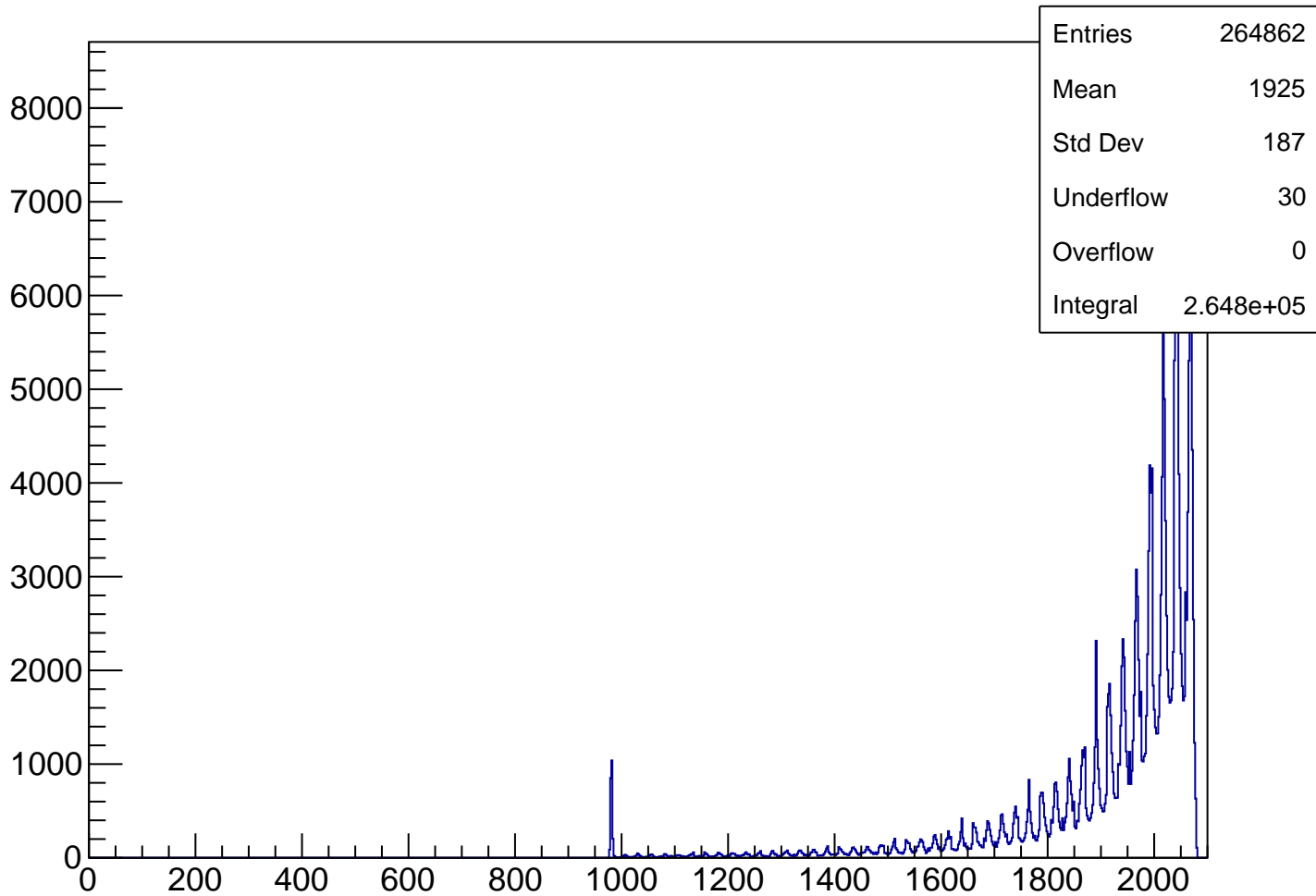
# TrigFlag Bh2K



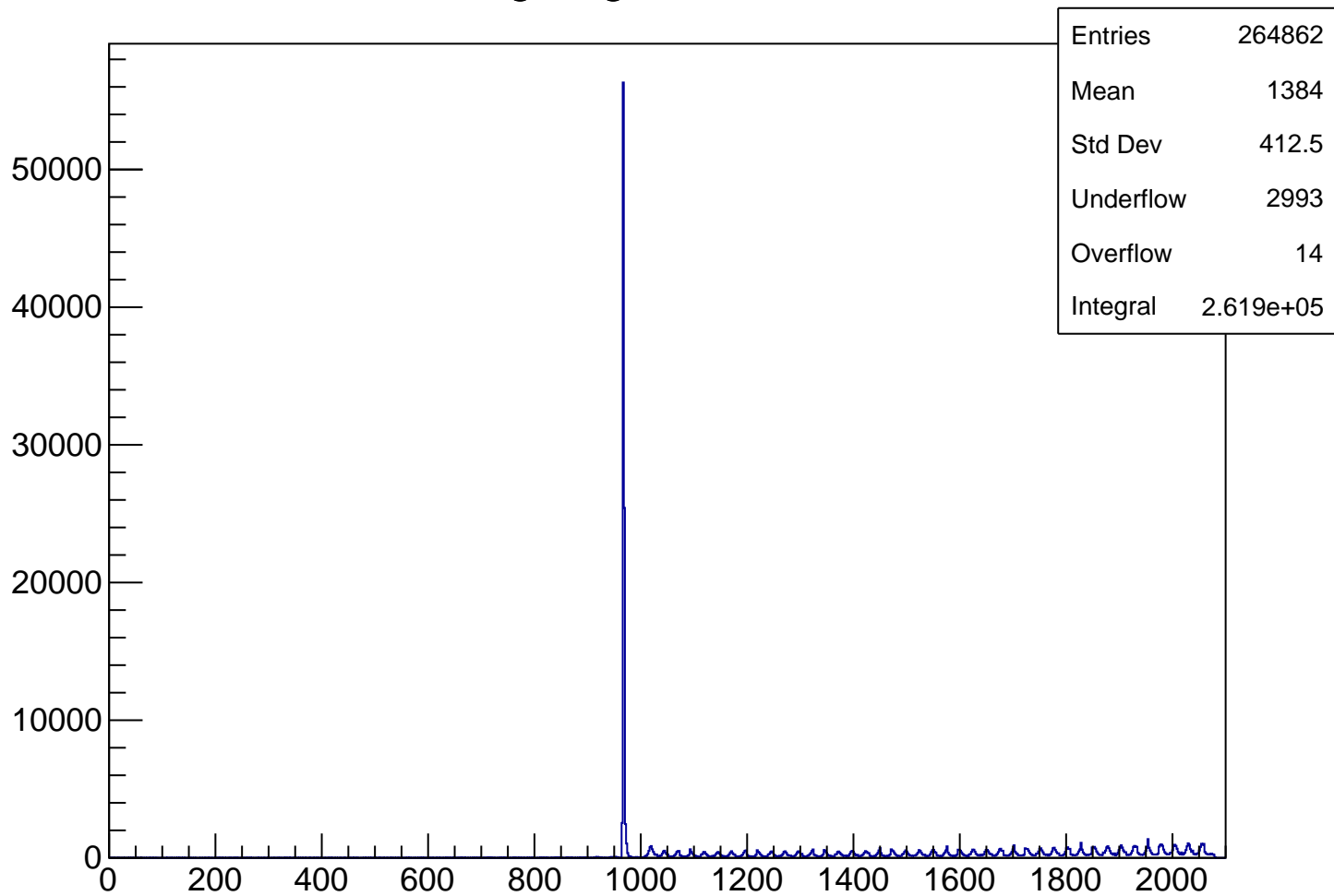
# TrigFlag ElseOr



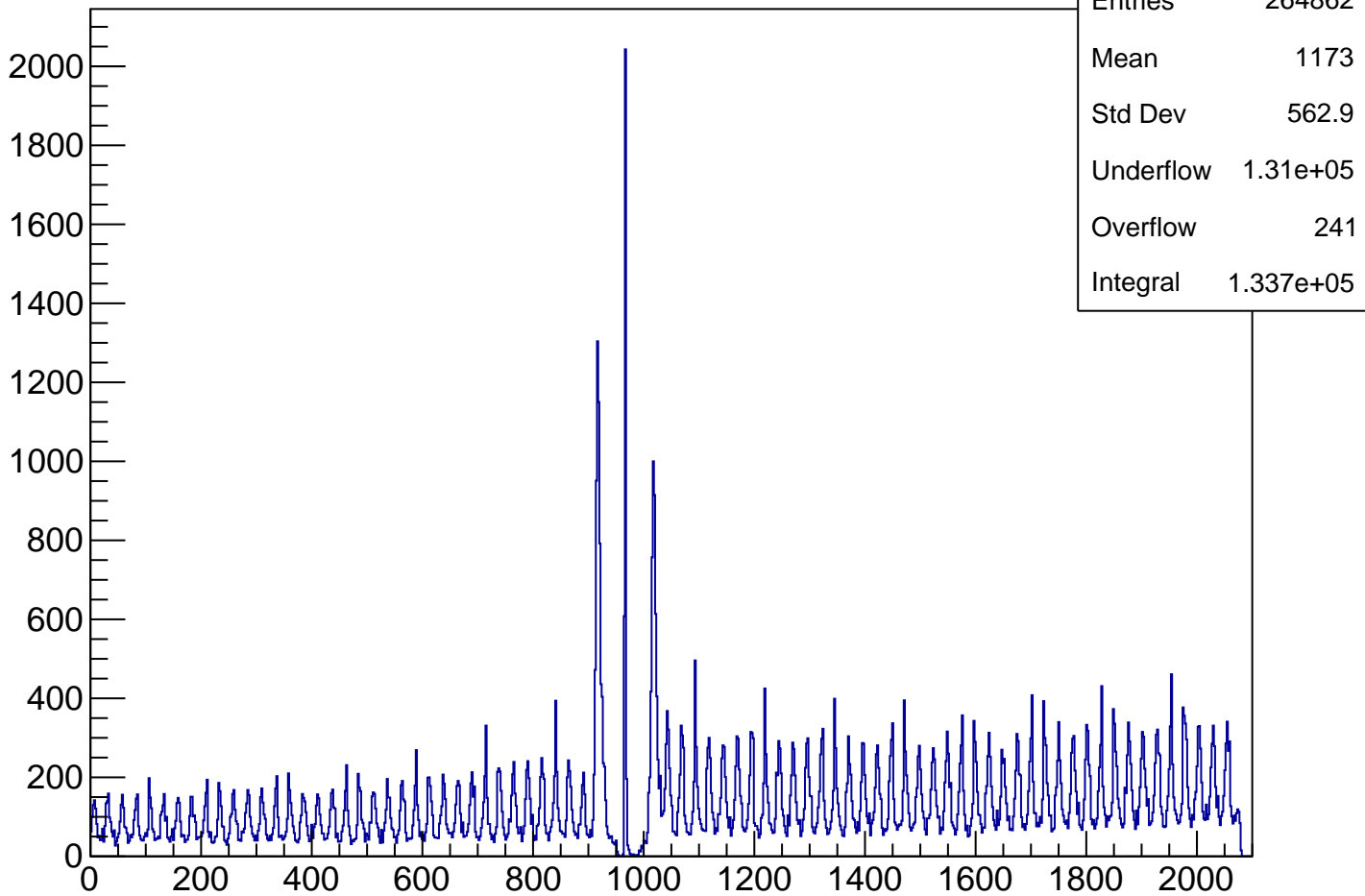
# TrigFlag Beam



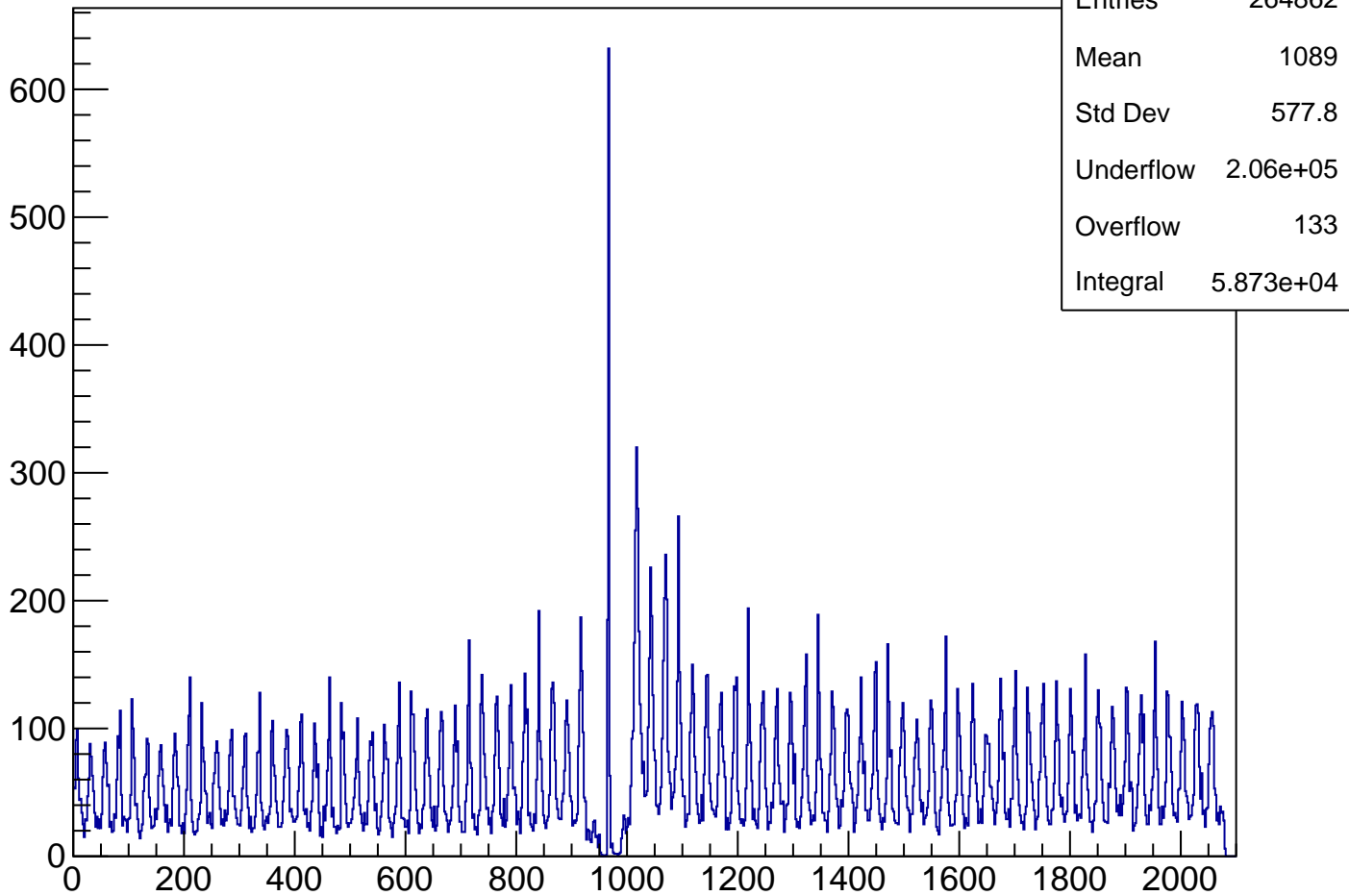
# TrigFlag BeamTof



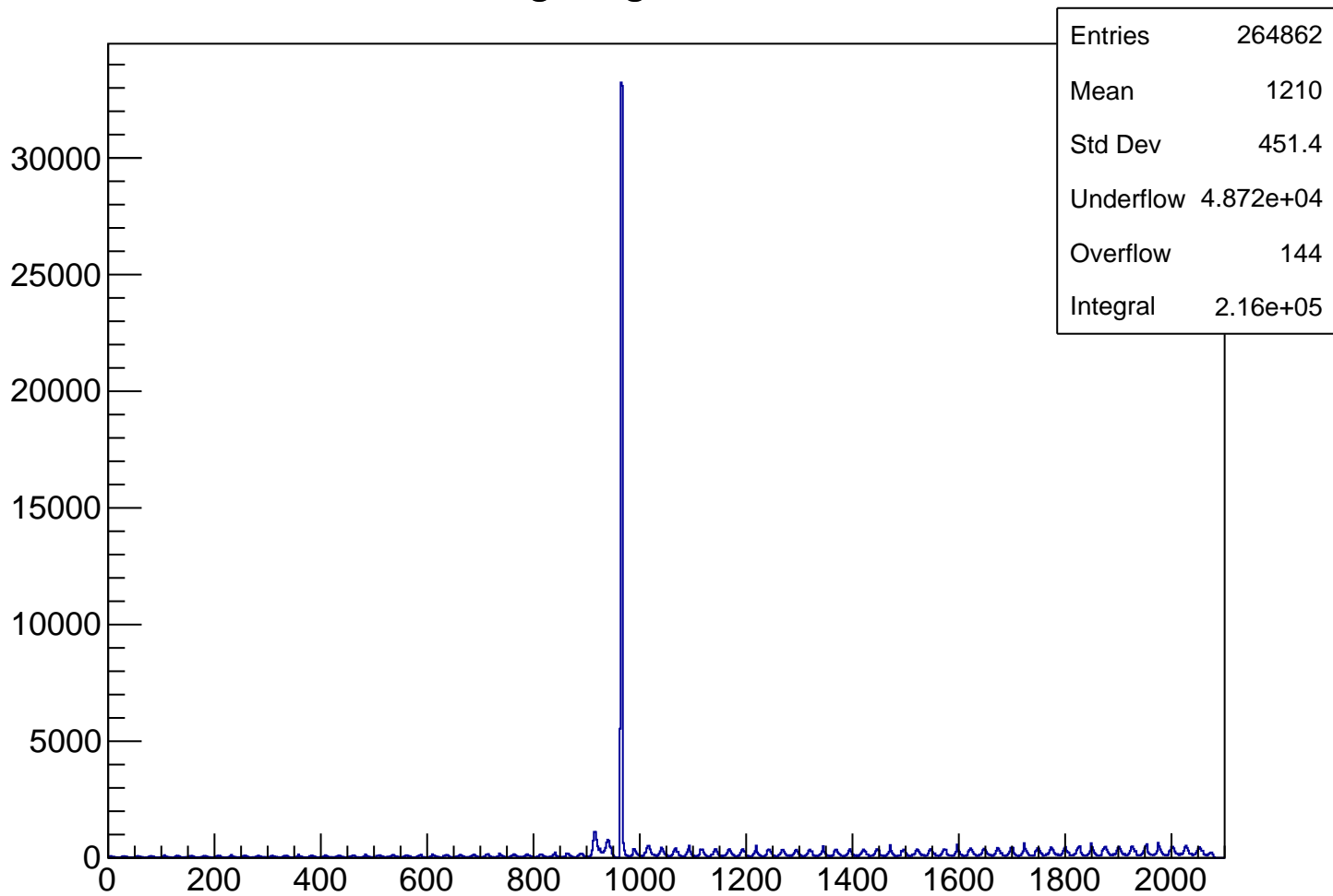
# TrigFlag BeamPi



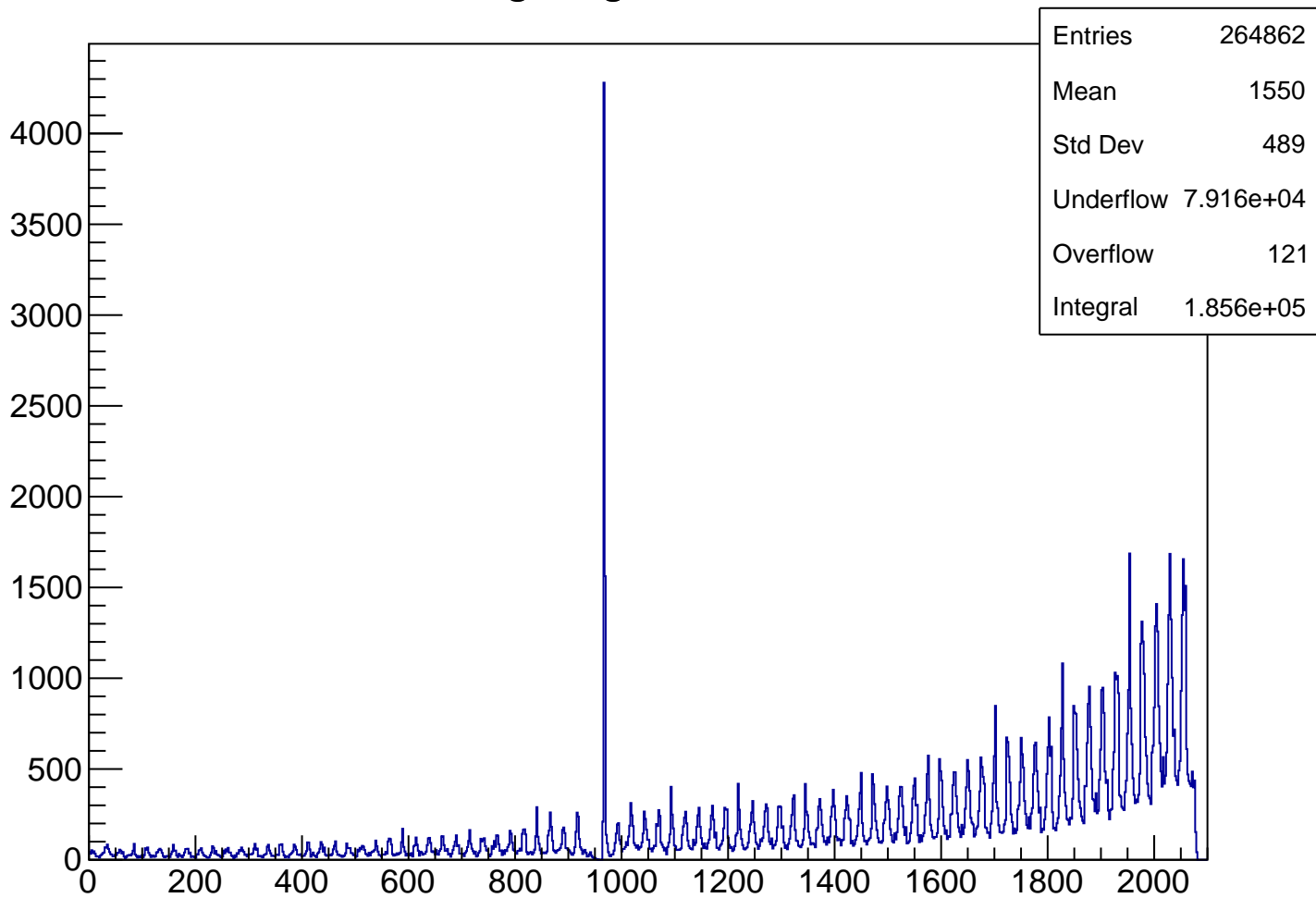
# TrigFlag BeamP



# TrigFlag Coin1

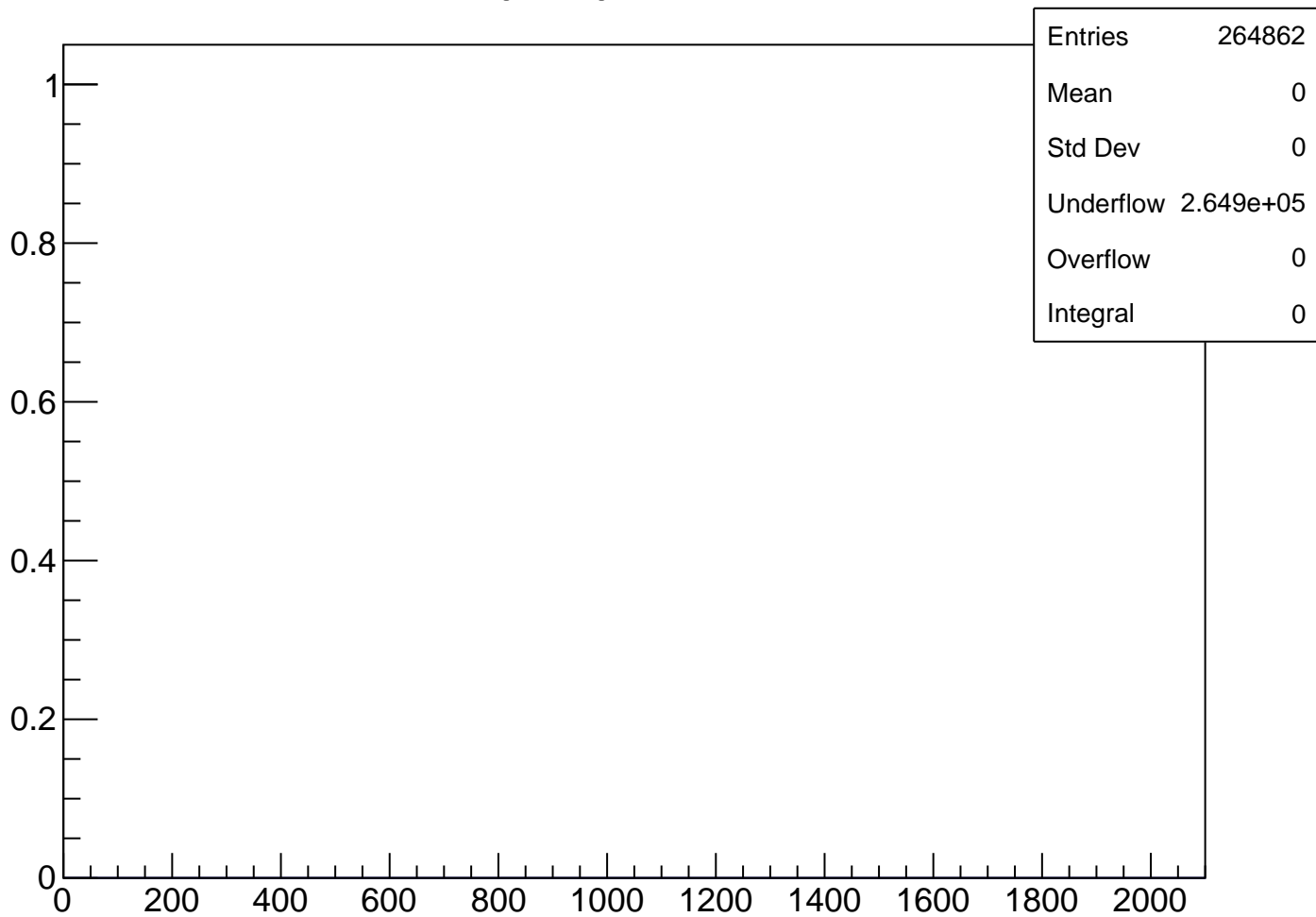


# TrigFlag Coin2

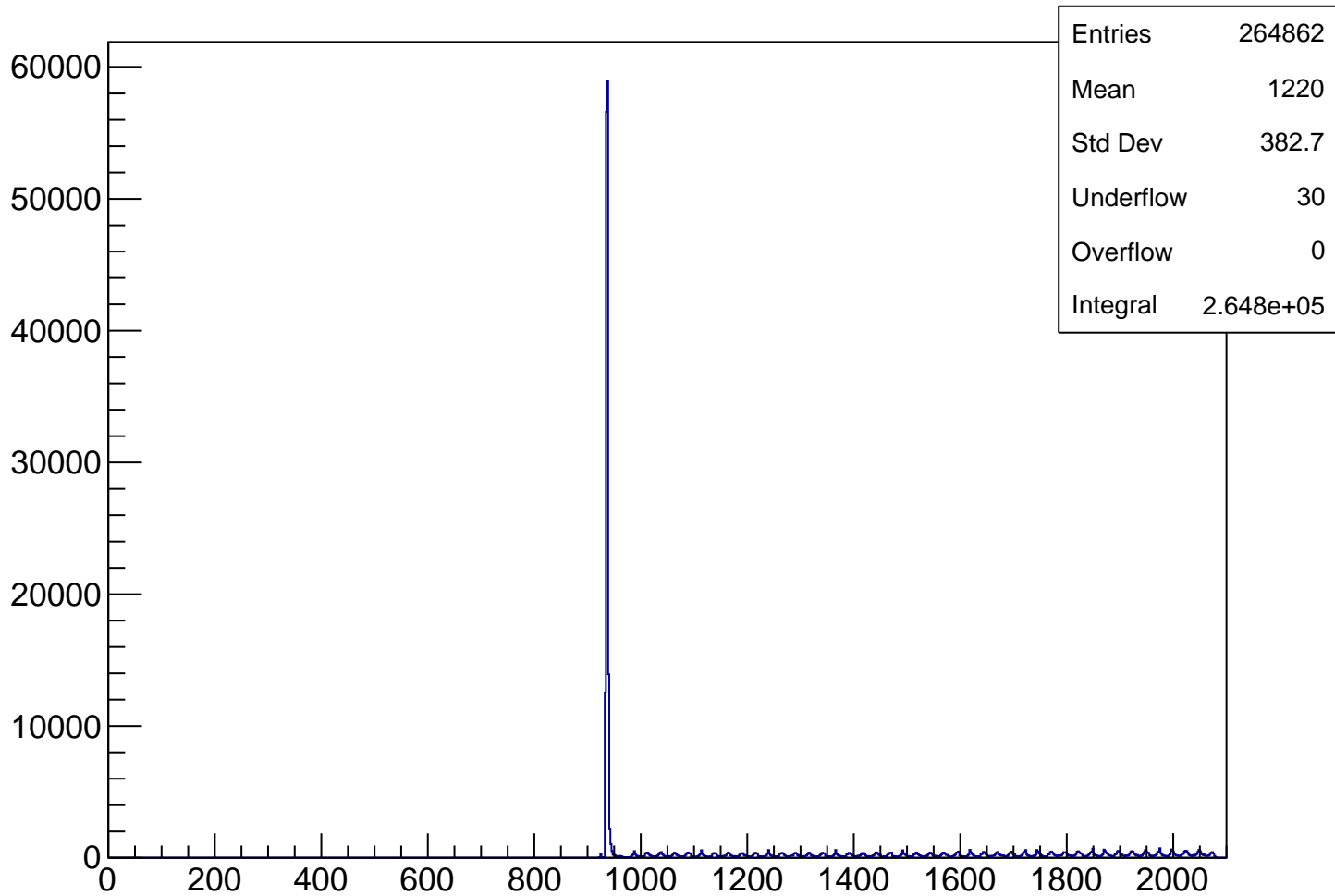




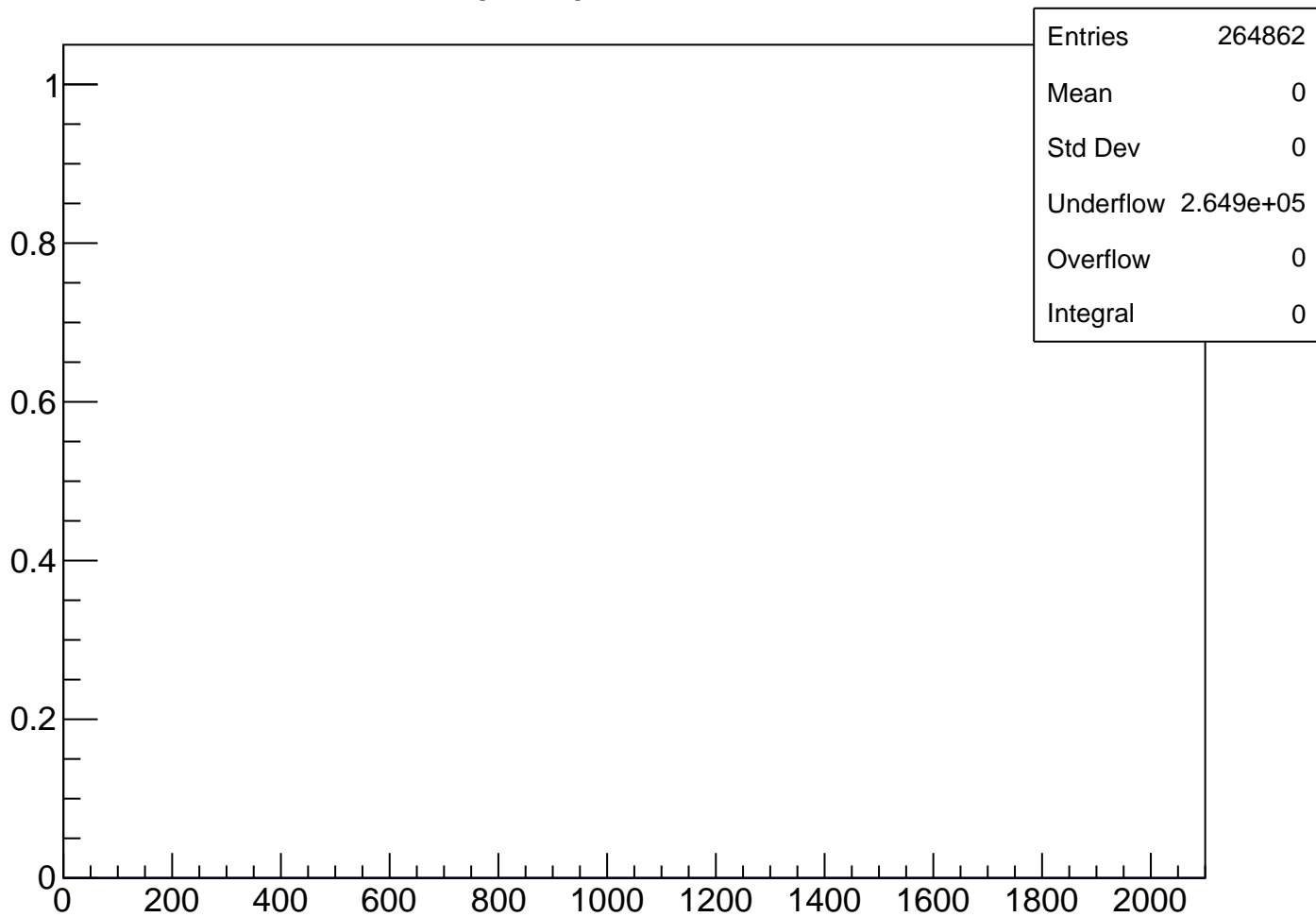
# TrigFlag E03



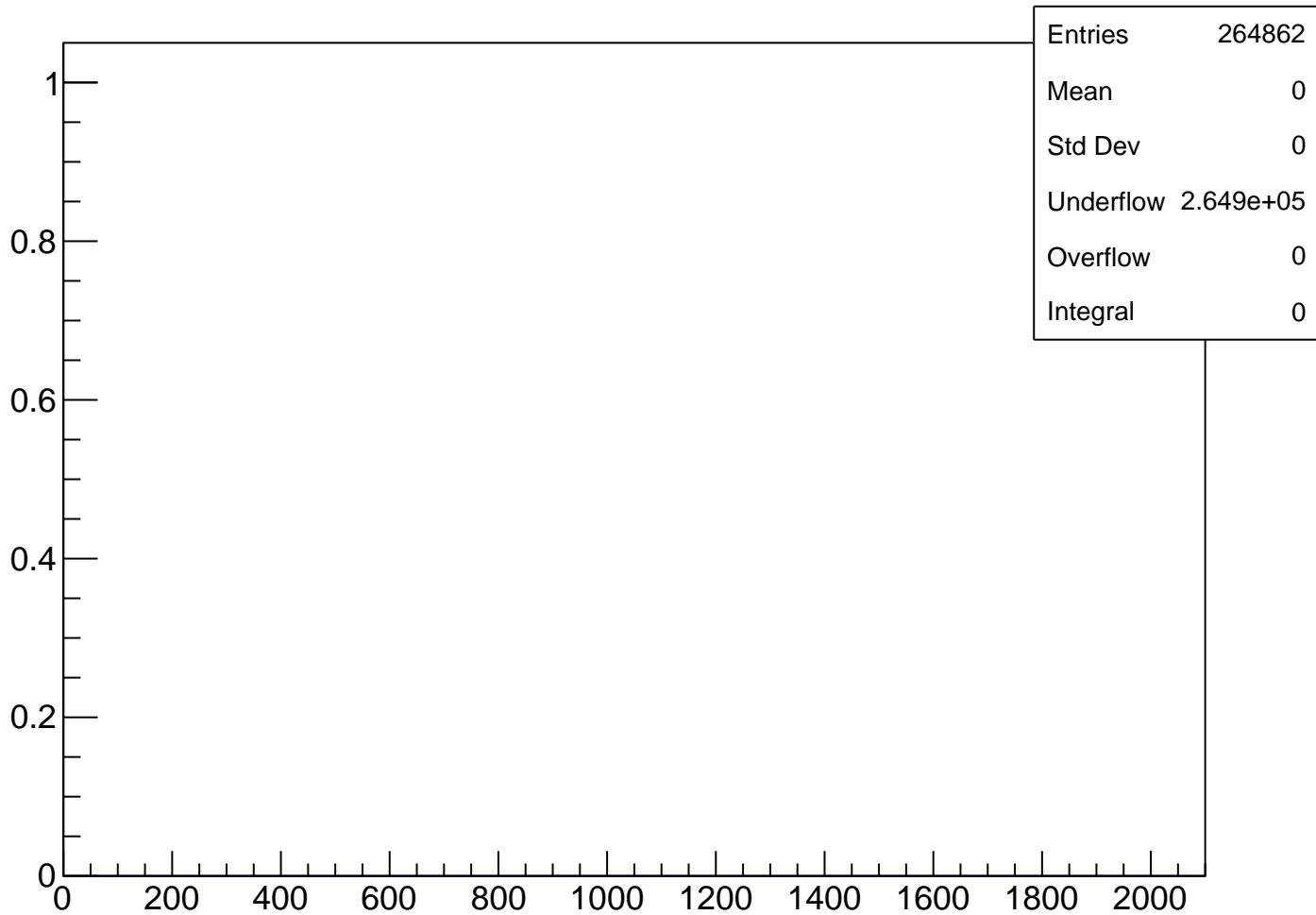
# TrigFlag Bh2KPs



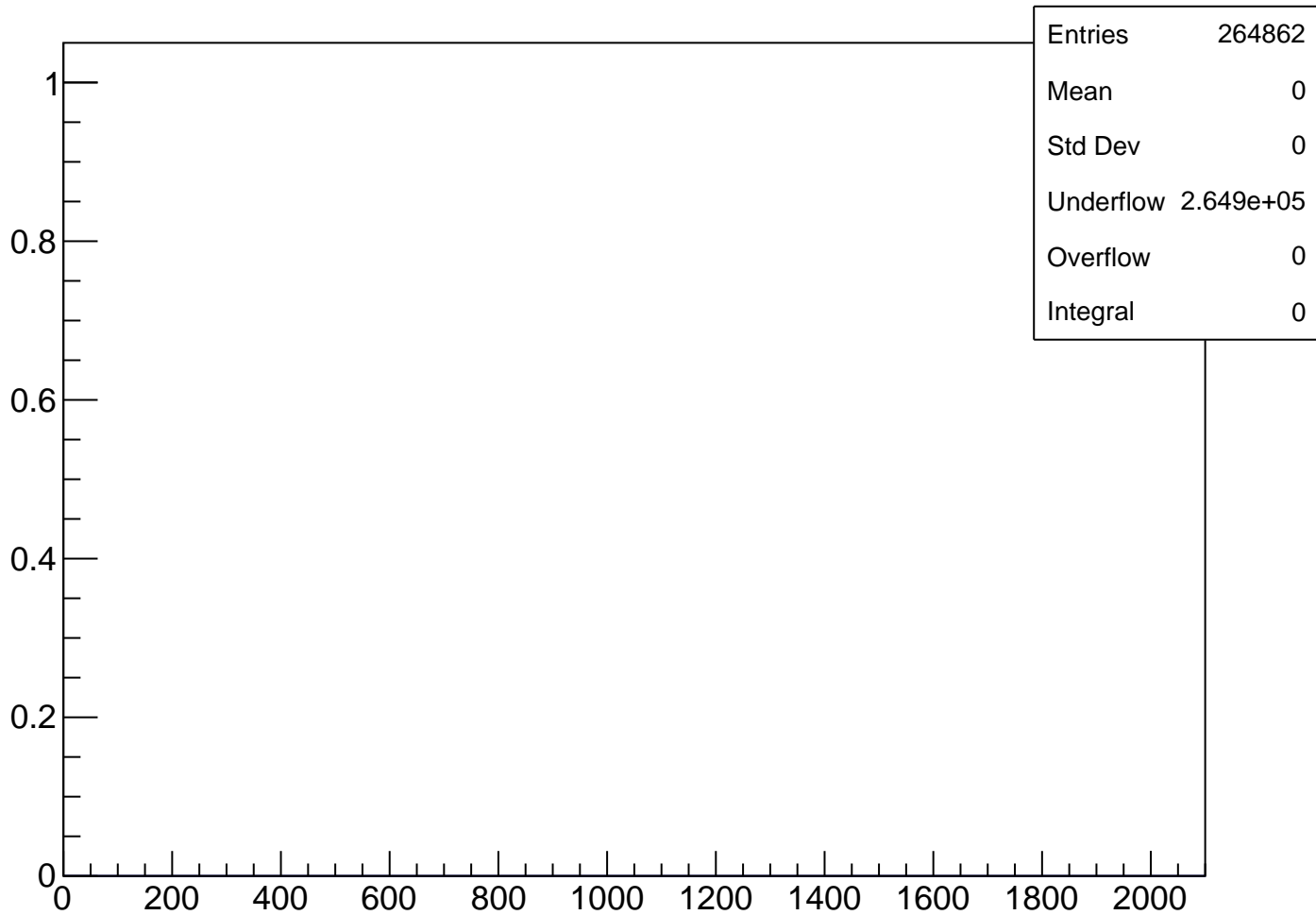
# TrigFlag BeamPs



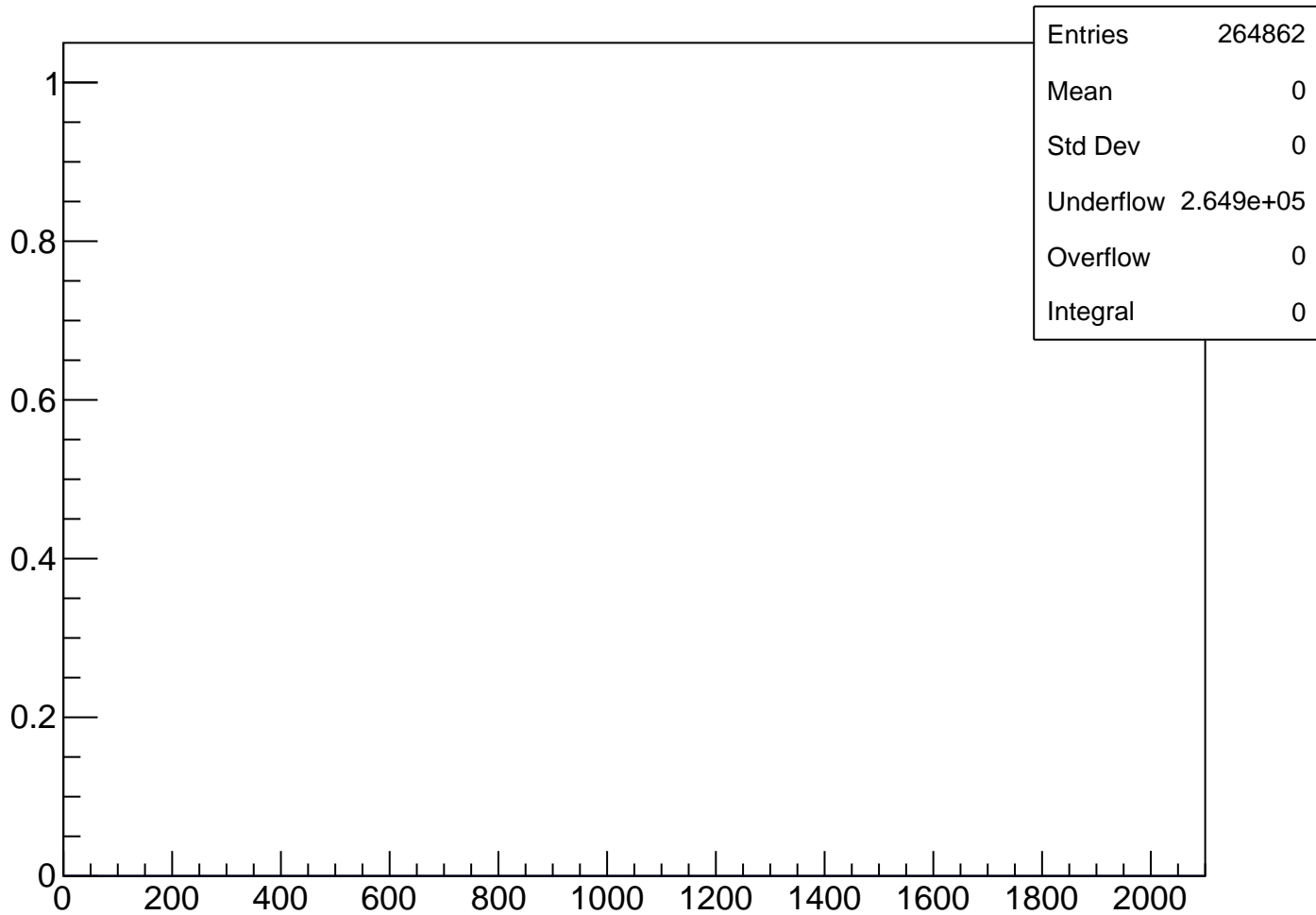
# TrigFlag BeamTofPs



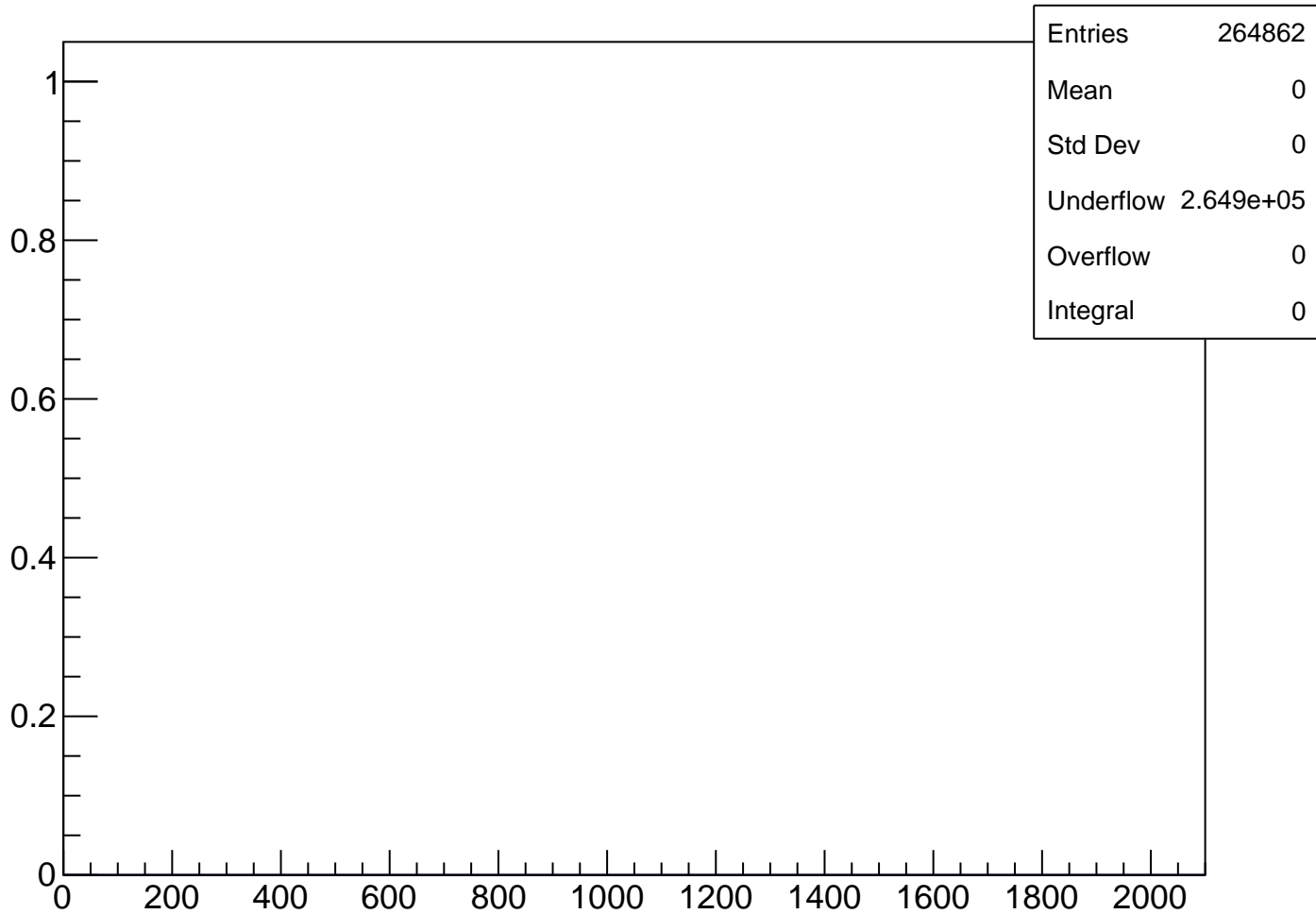
# TrigFlag BeamPiPs



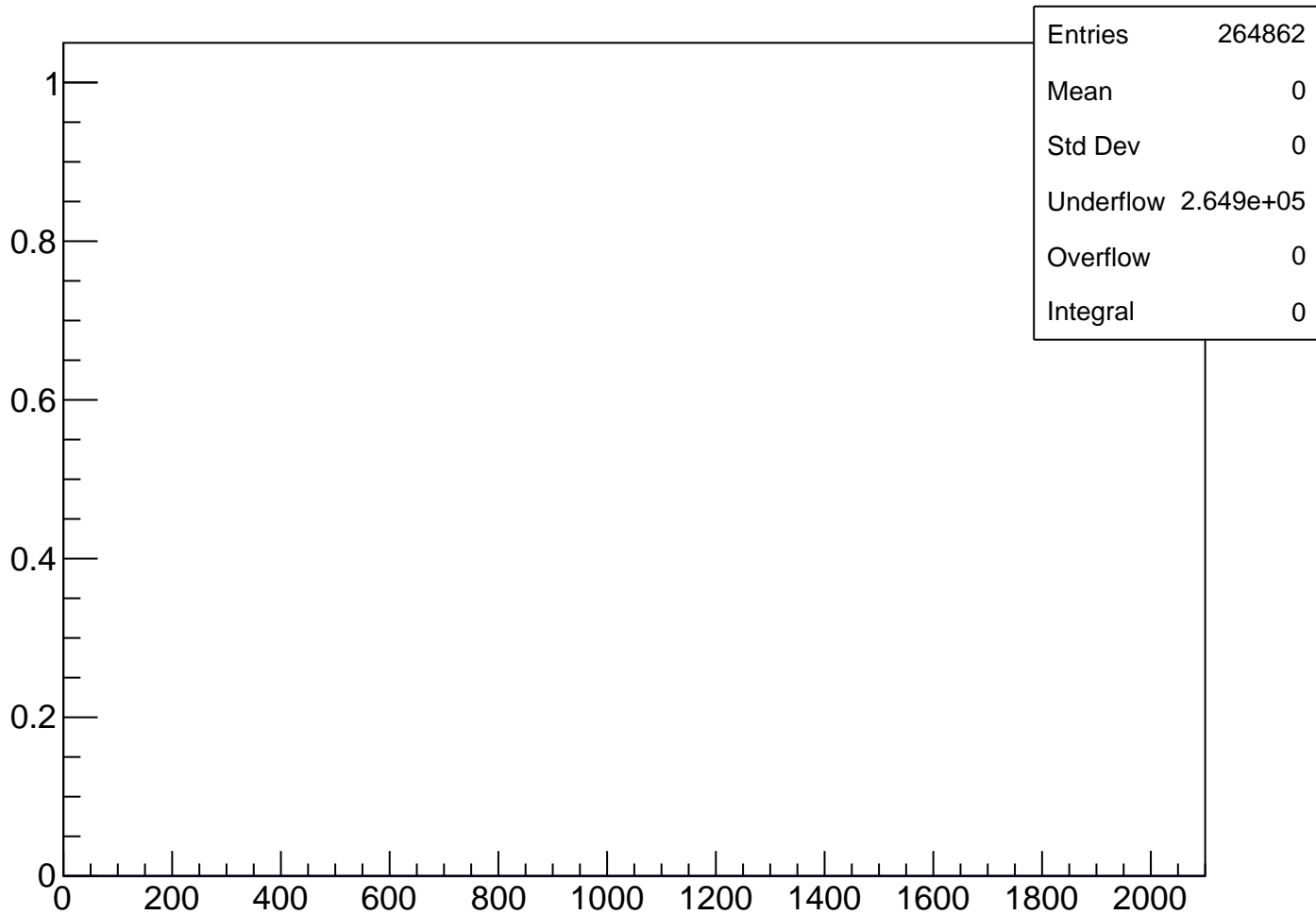
# TrigFlag BeamPPs



# TrigFlag Coin1Ps

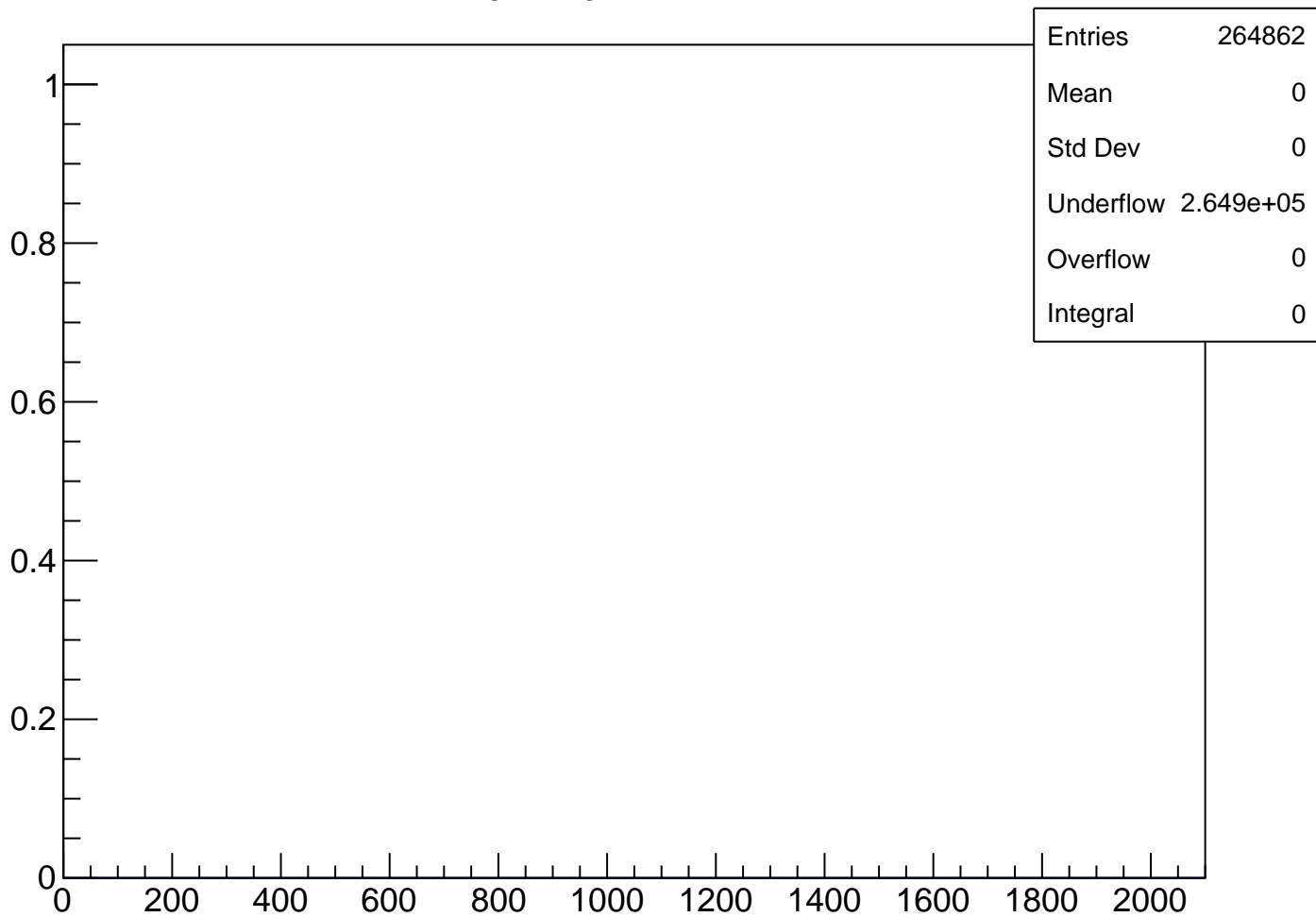


# TrigFlag Coin2Ps

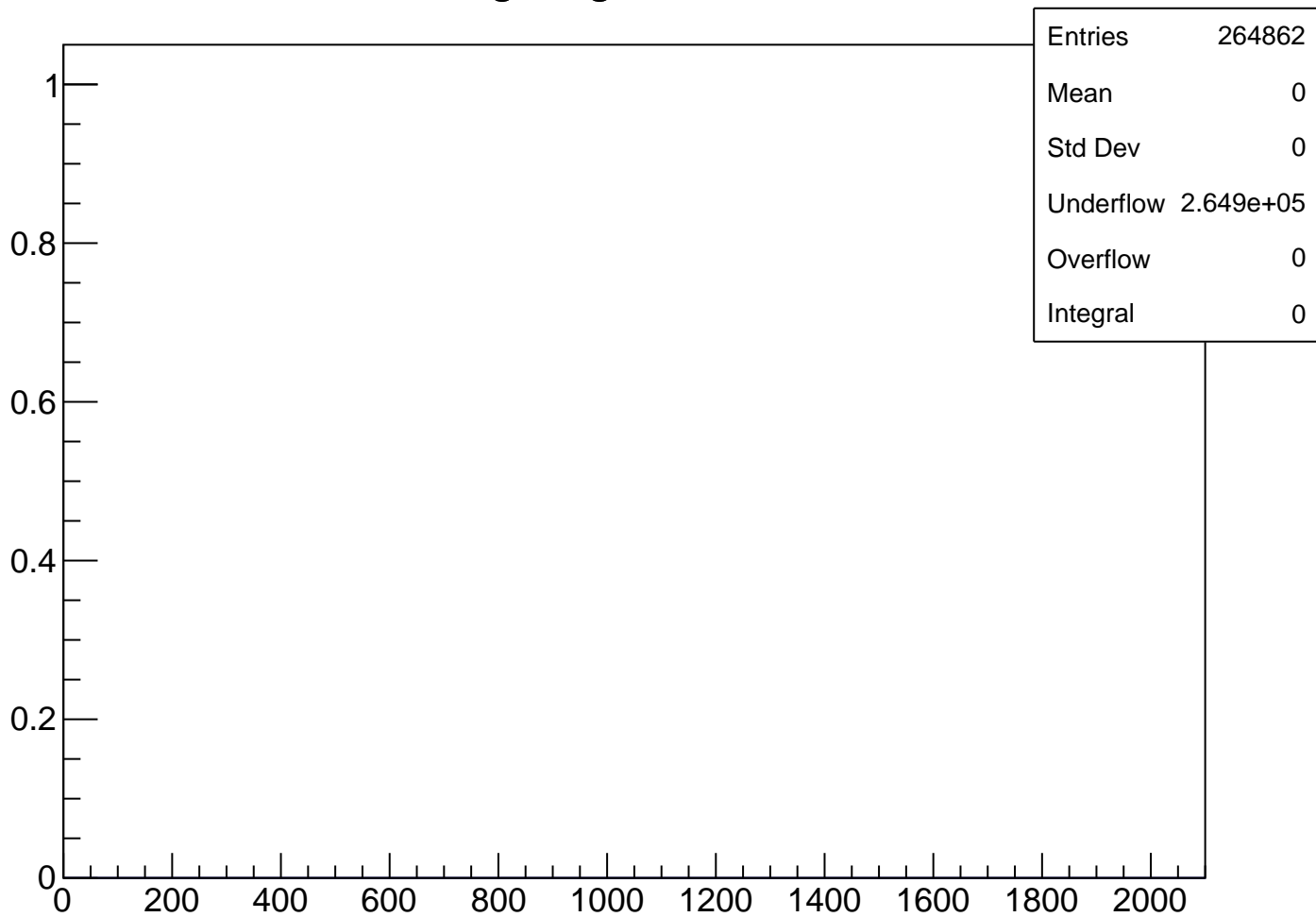




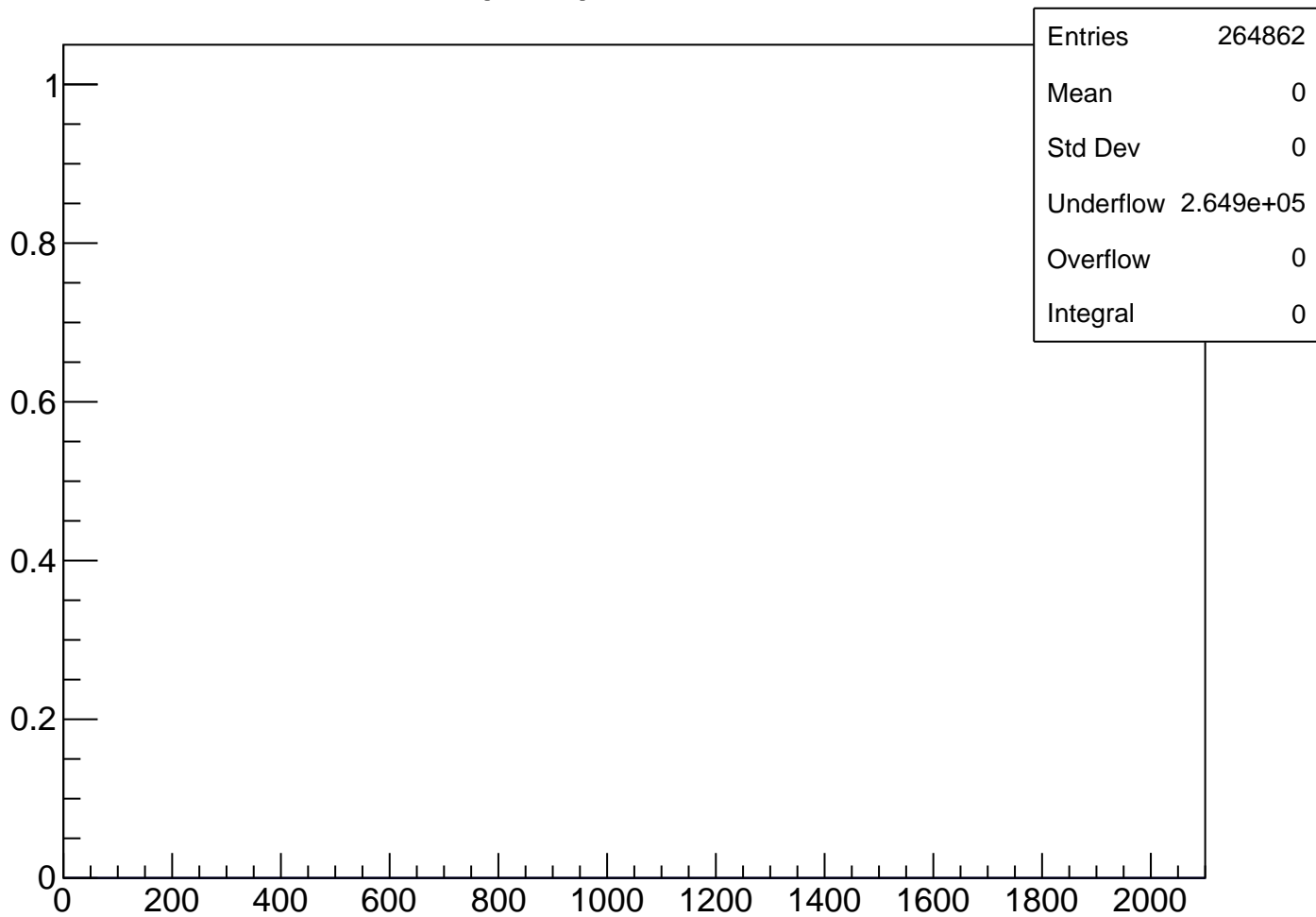
# TrigFlag E03Ps



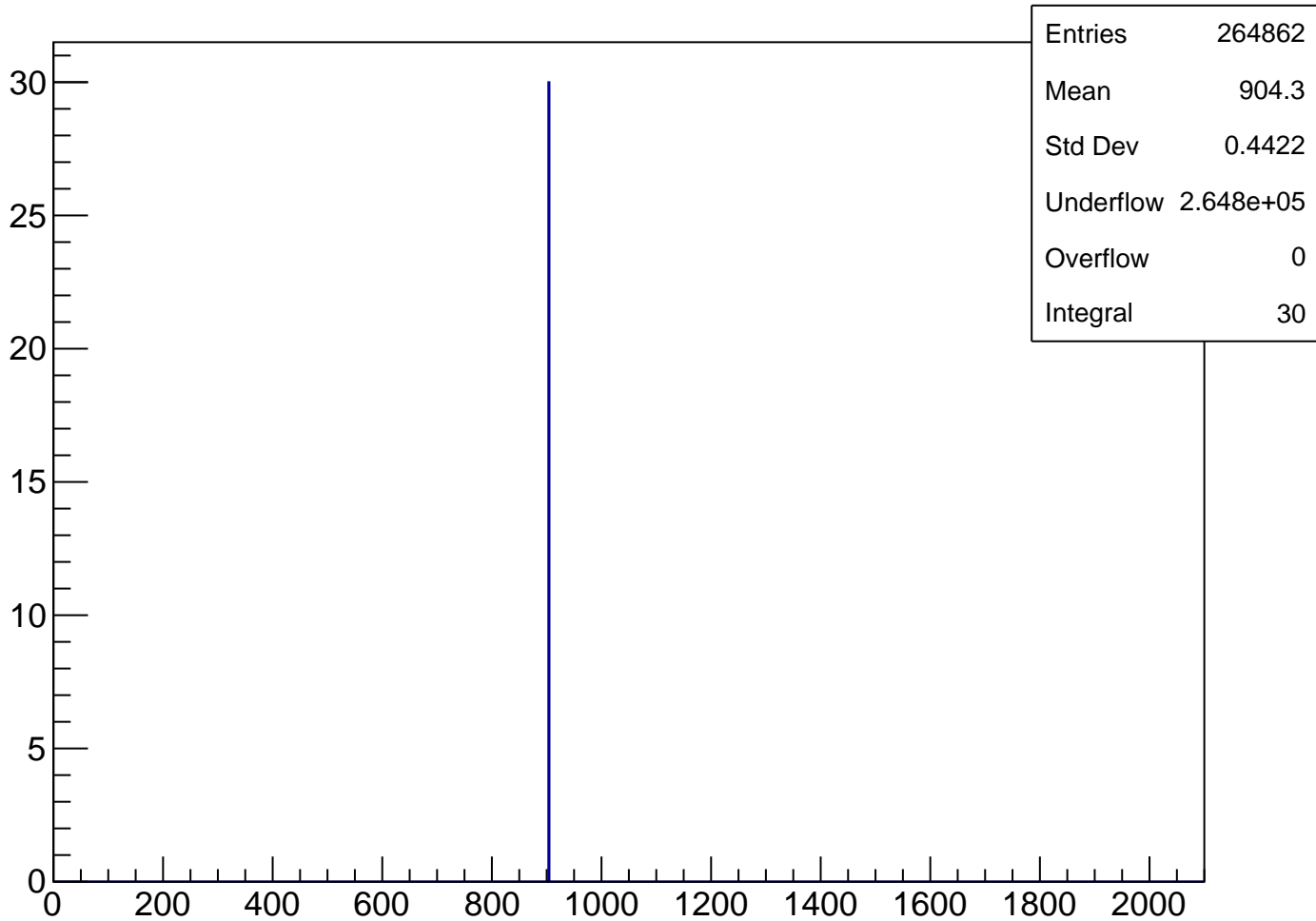
# TrigFlag Clock



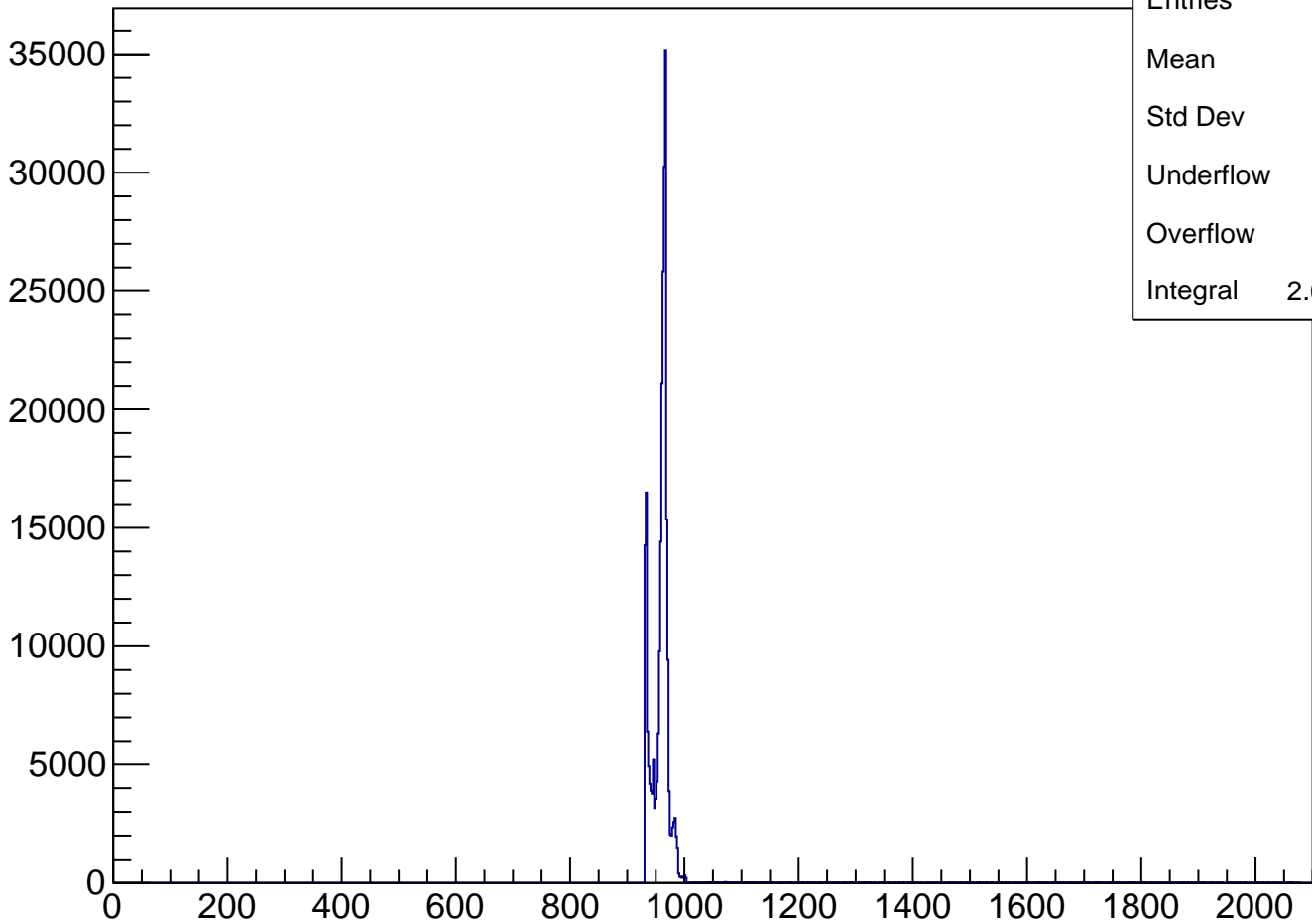
# TrigFlag Reserve2



# TrigFlag SpillEnd

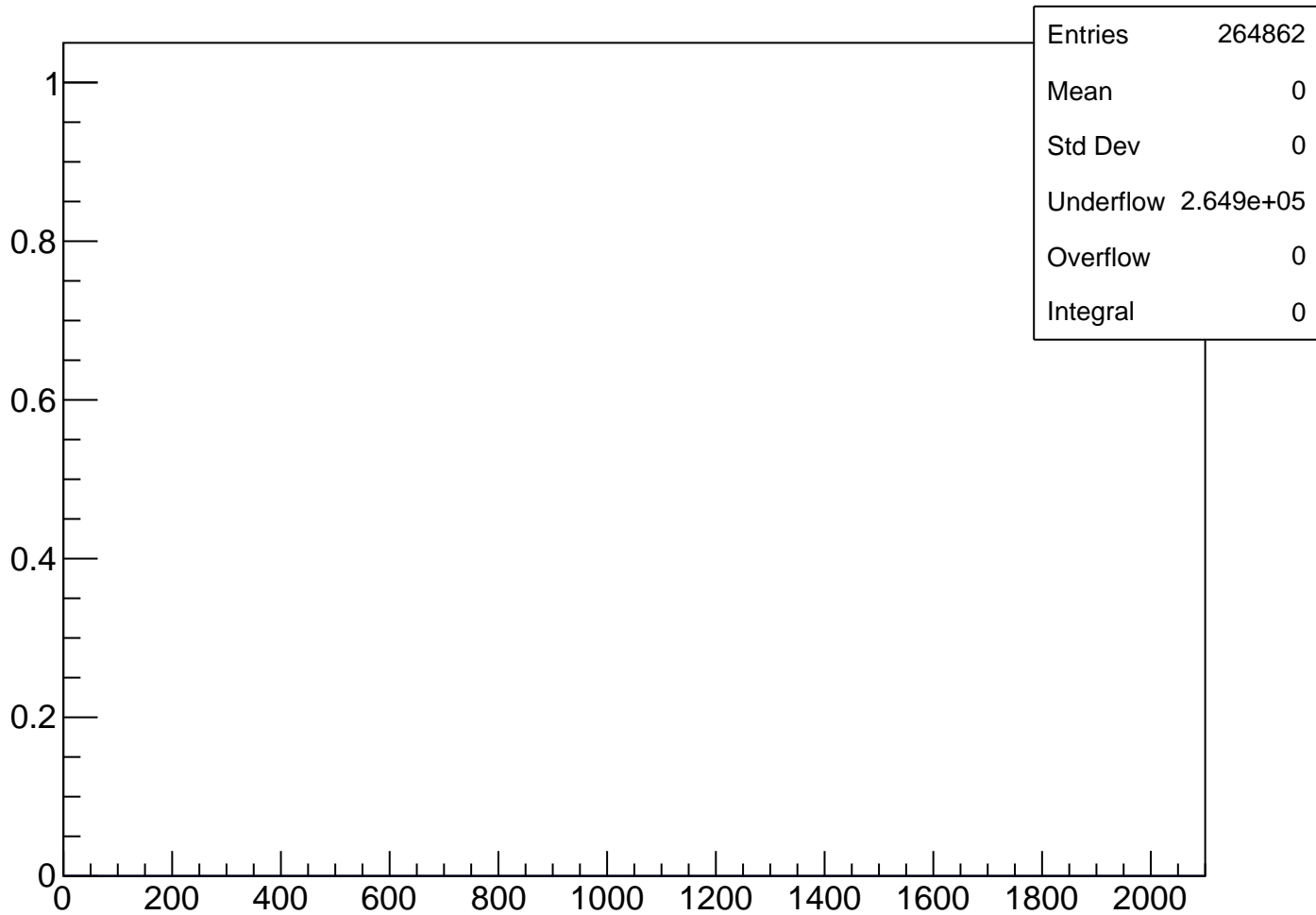


# TrigFlag Matrix

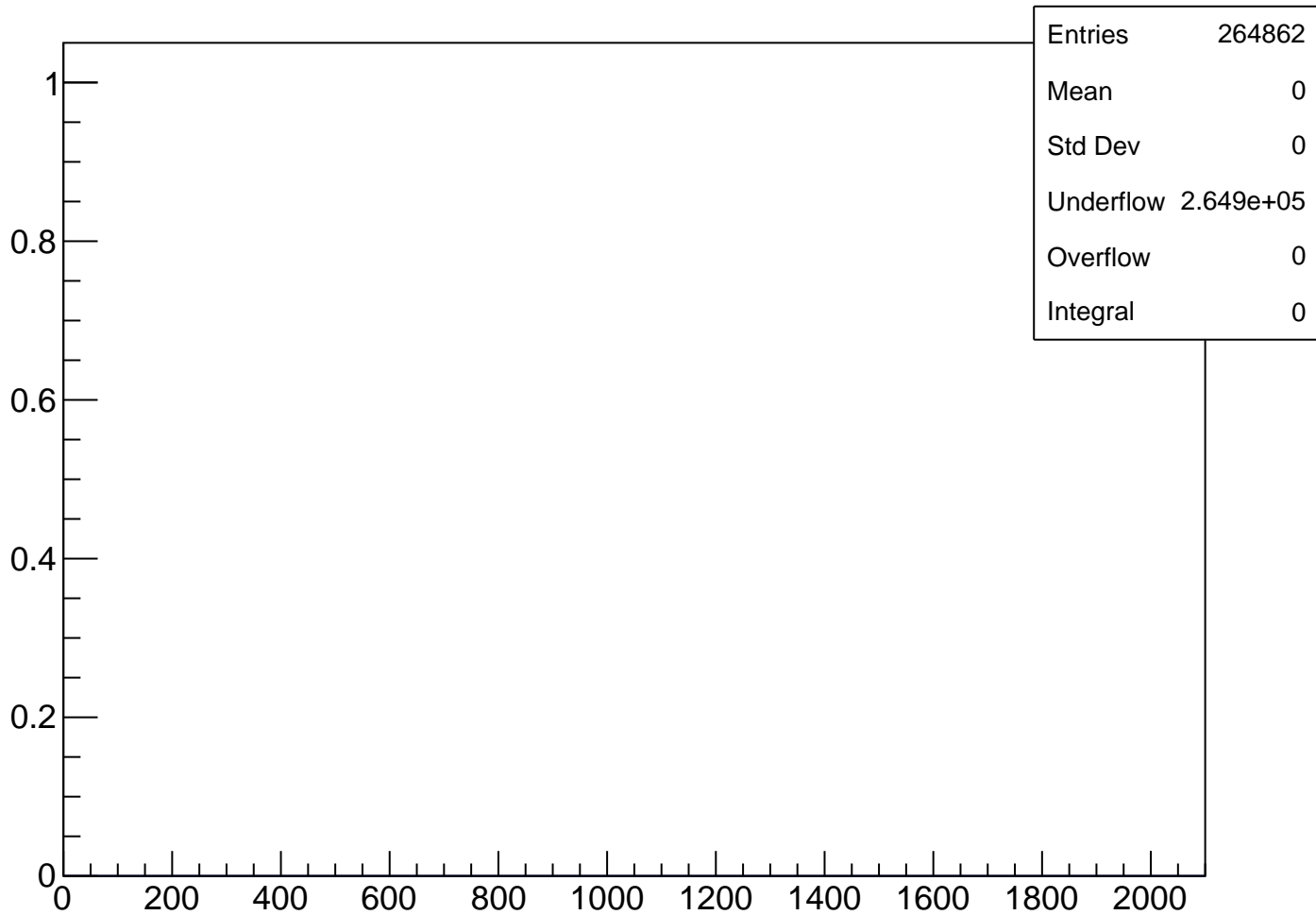


Entries	264862
Mean	970.9
Std Dev	95.88
Underflow	30
Overflow	0
Integral	2.648e+05

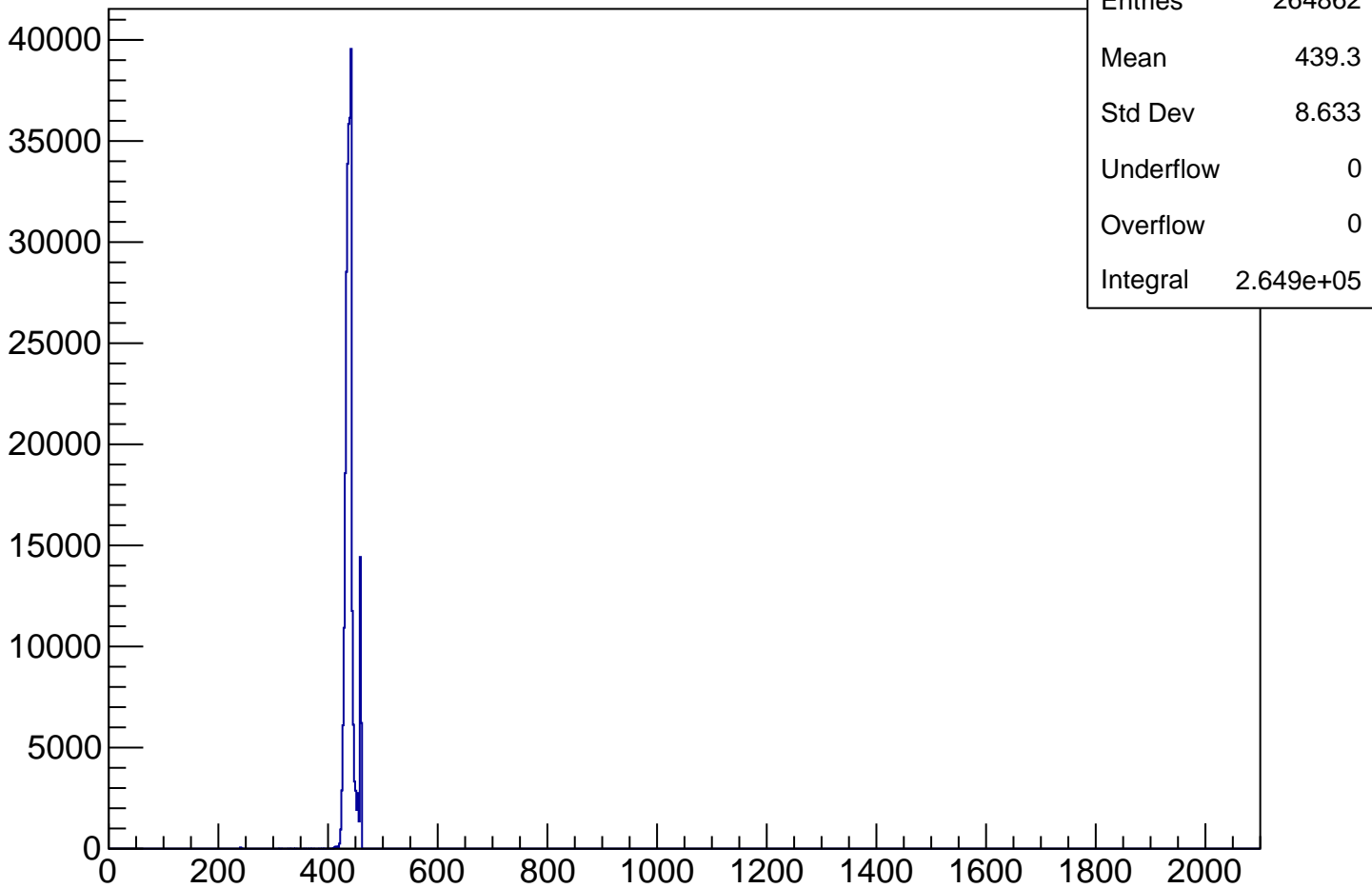
# TrigFlag MstAccept



# TrigFlag MstClear

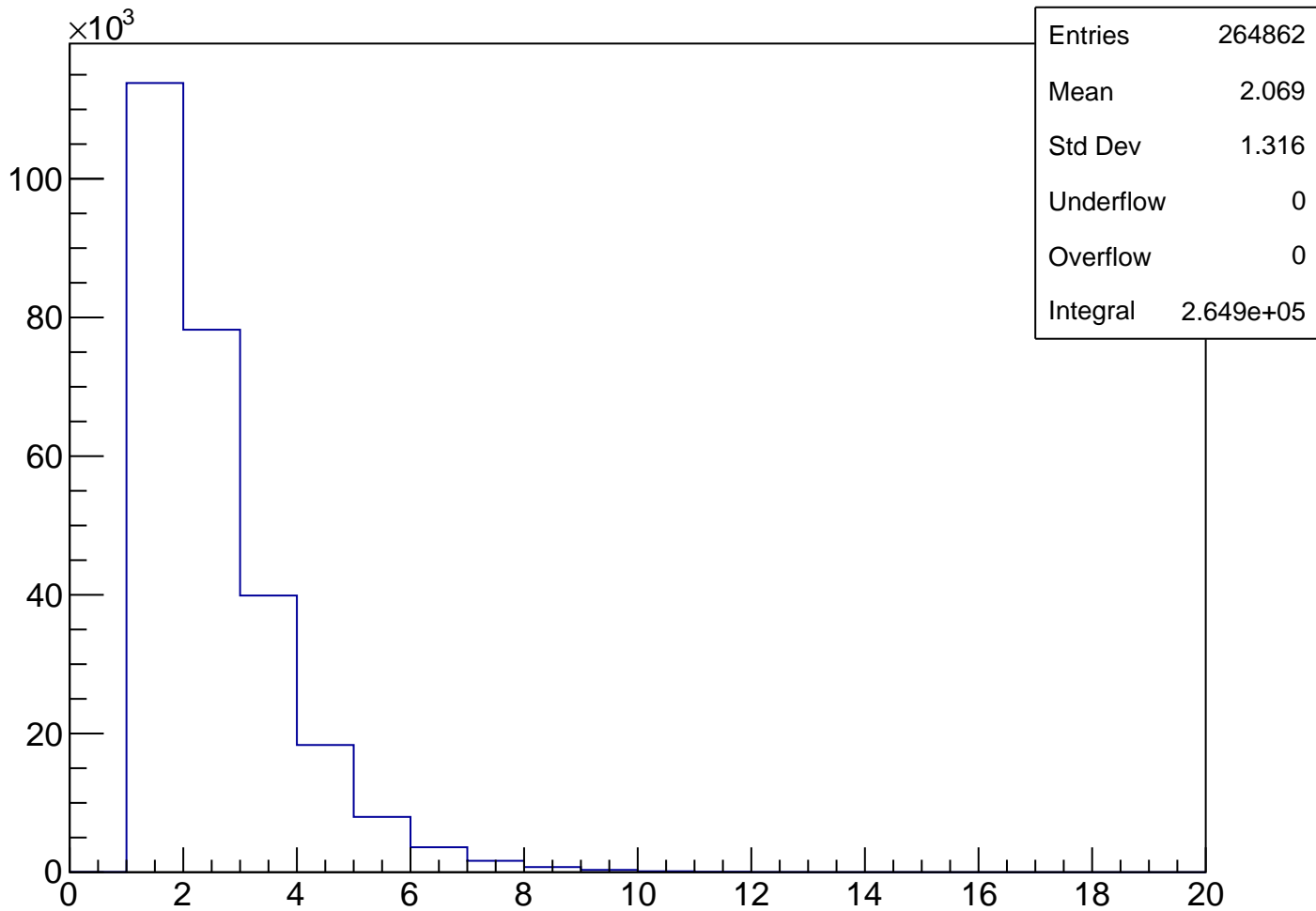


# TrigFlag TofTiming

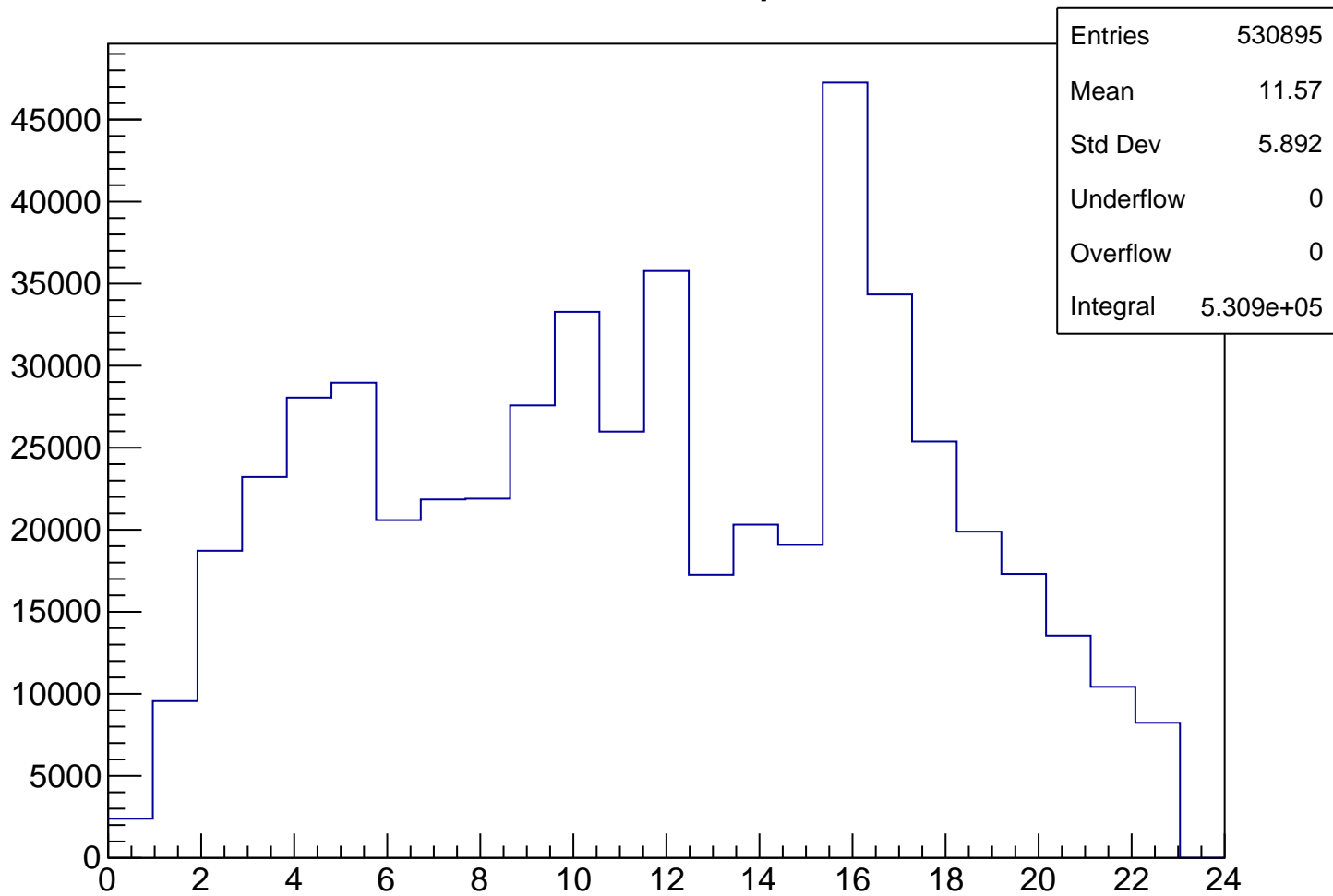




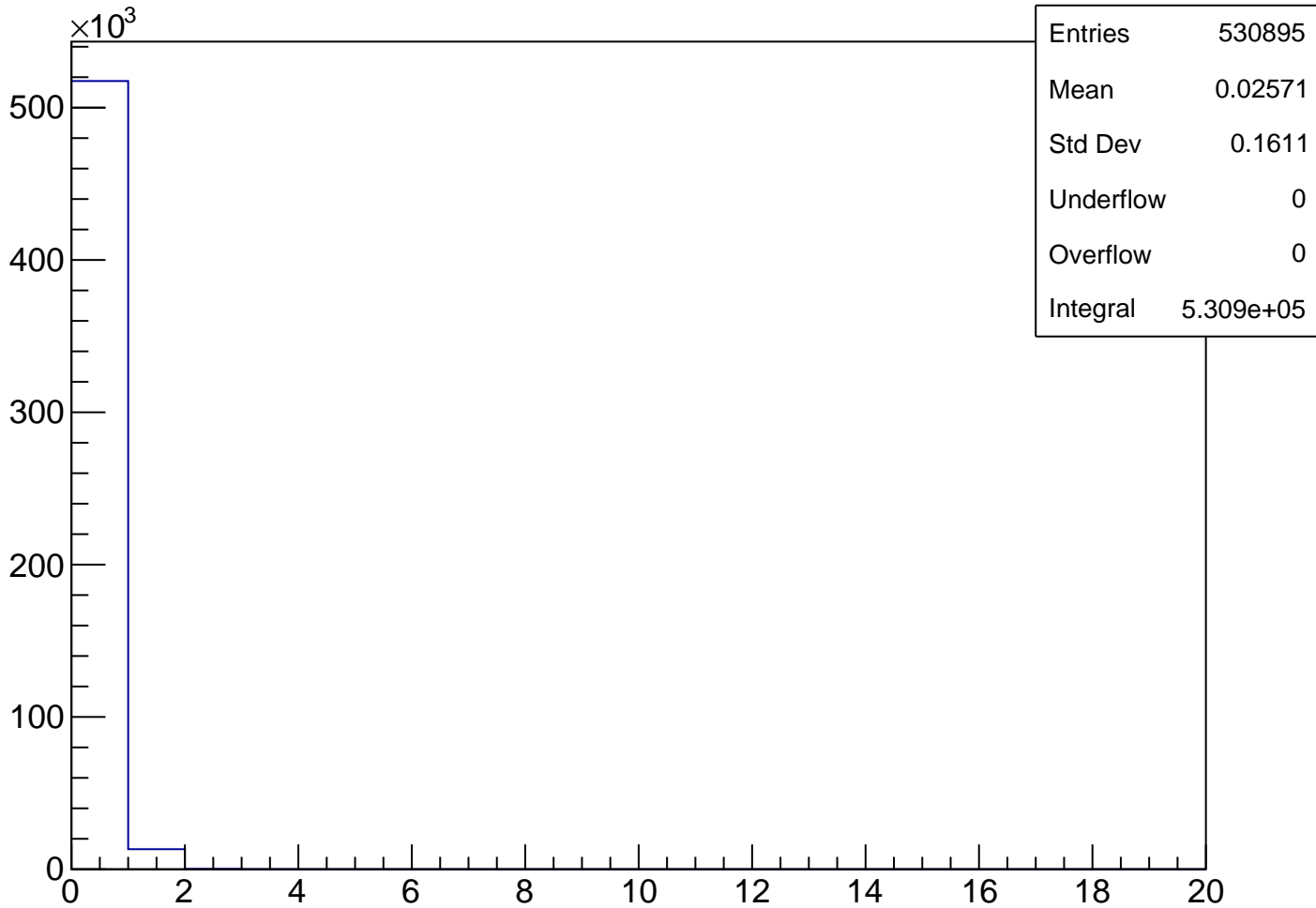
# TofMtOr Nhits



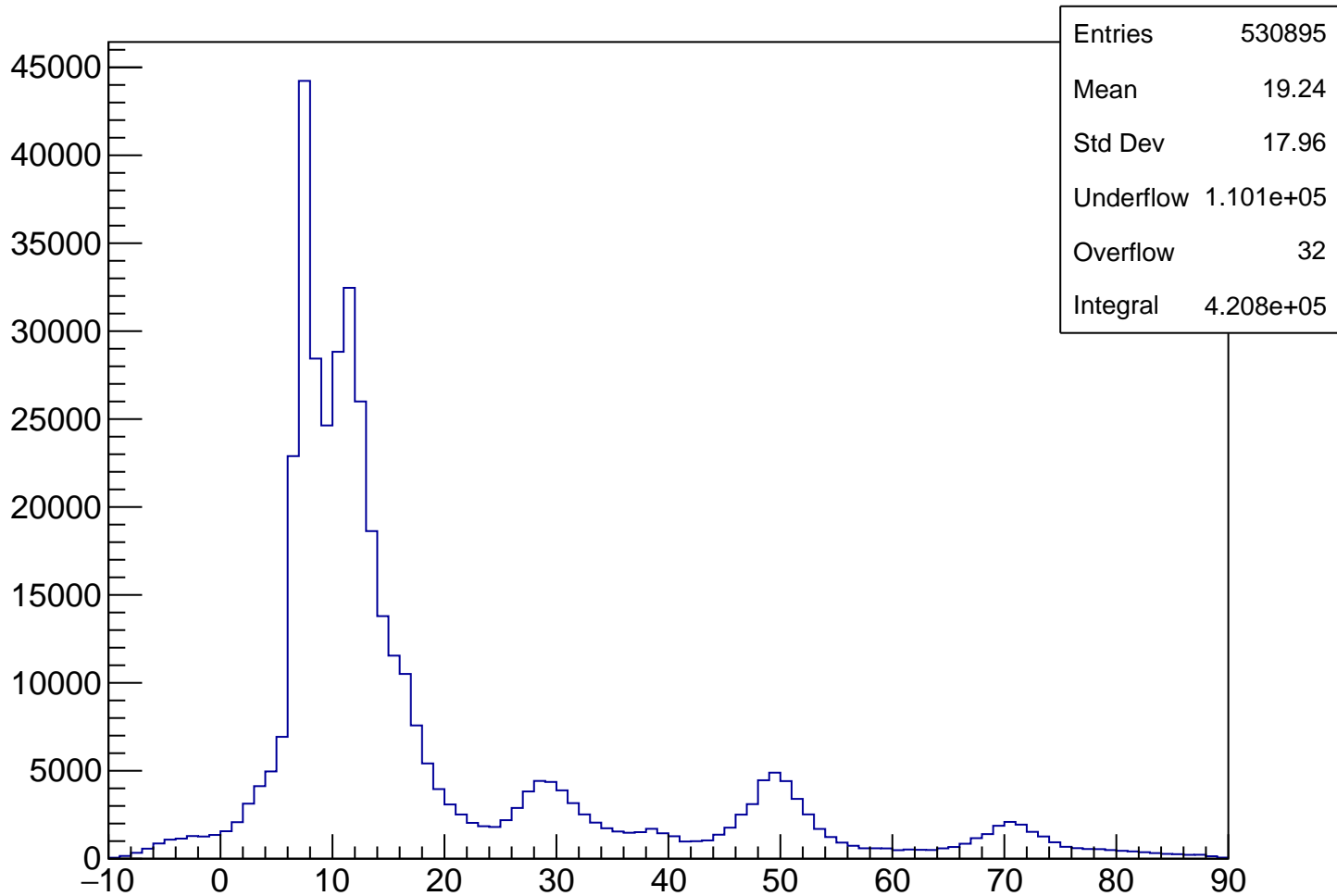
# TofMtOr Hitpat



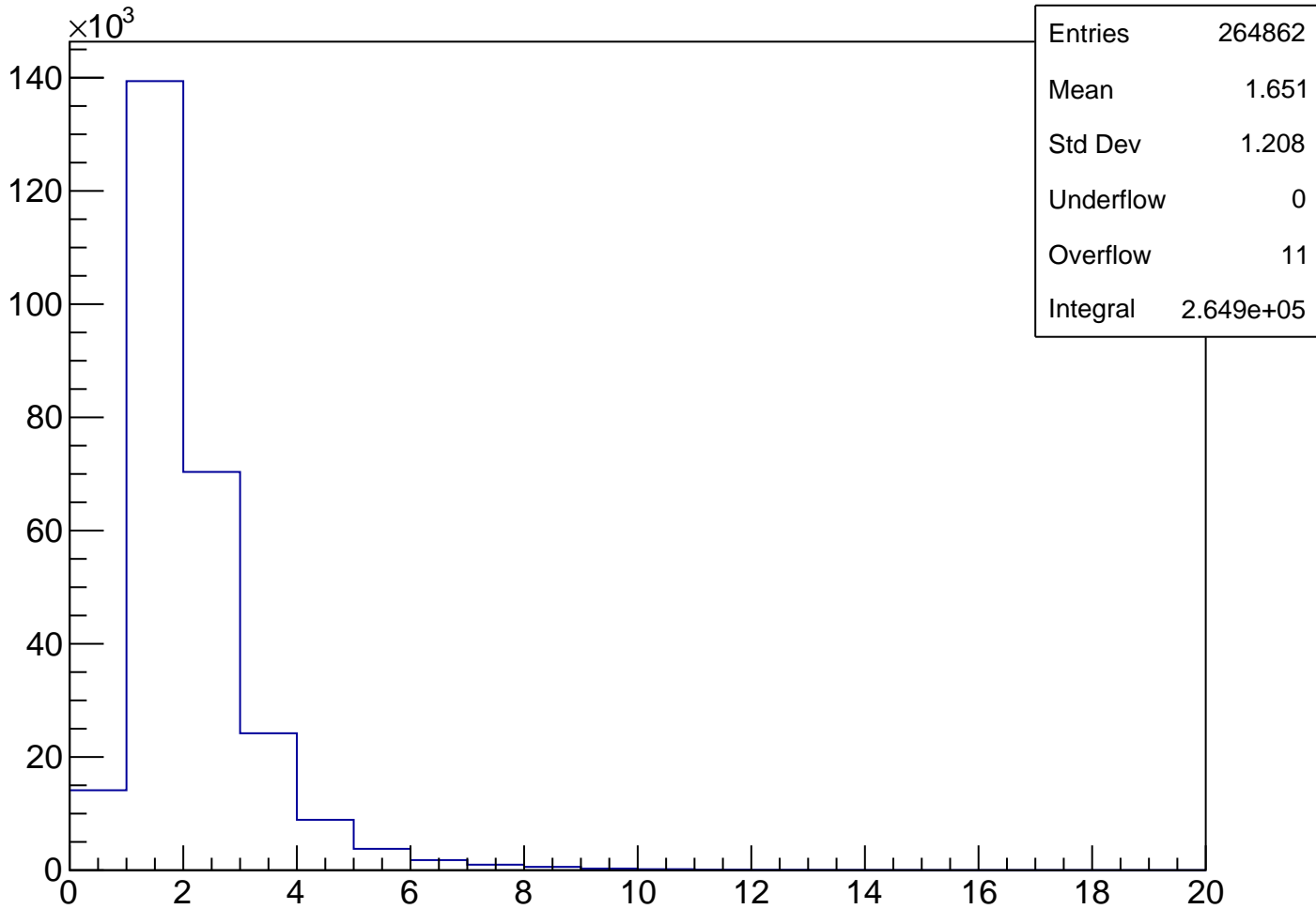
# TofMtOrDepthPat



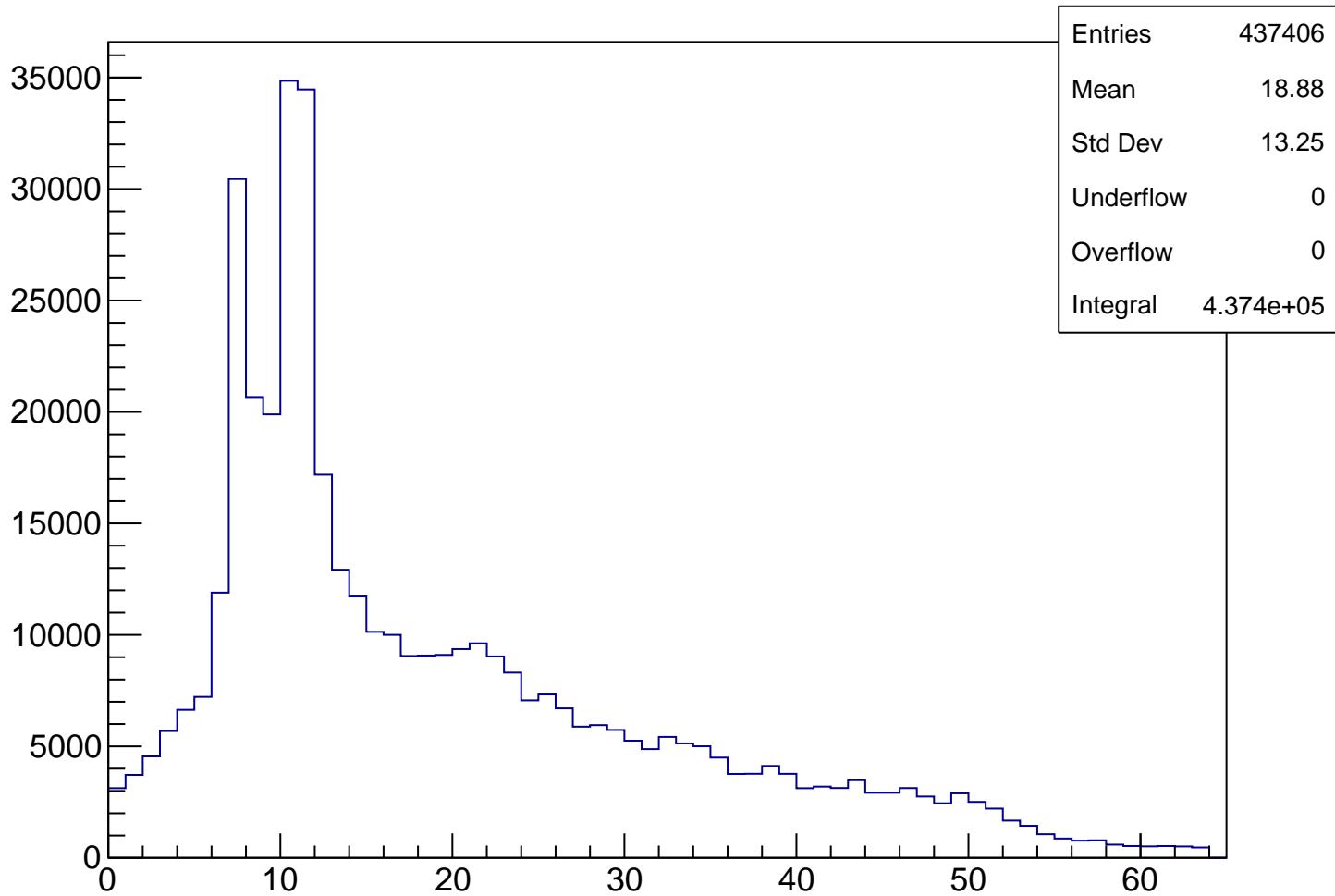
# TofMtOr



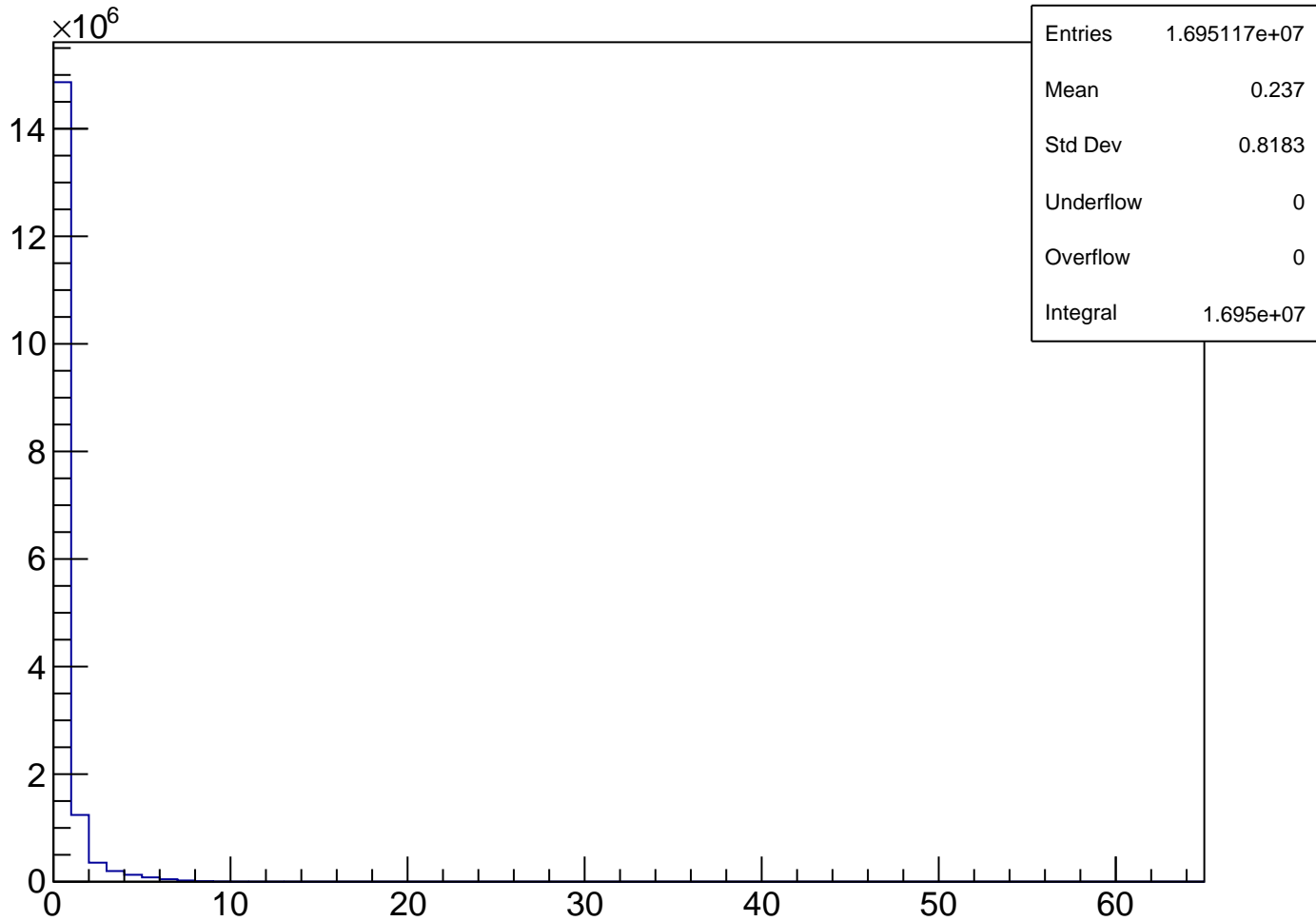
# Sch Nhits



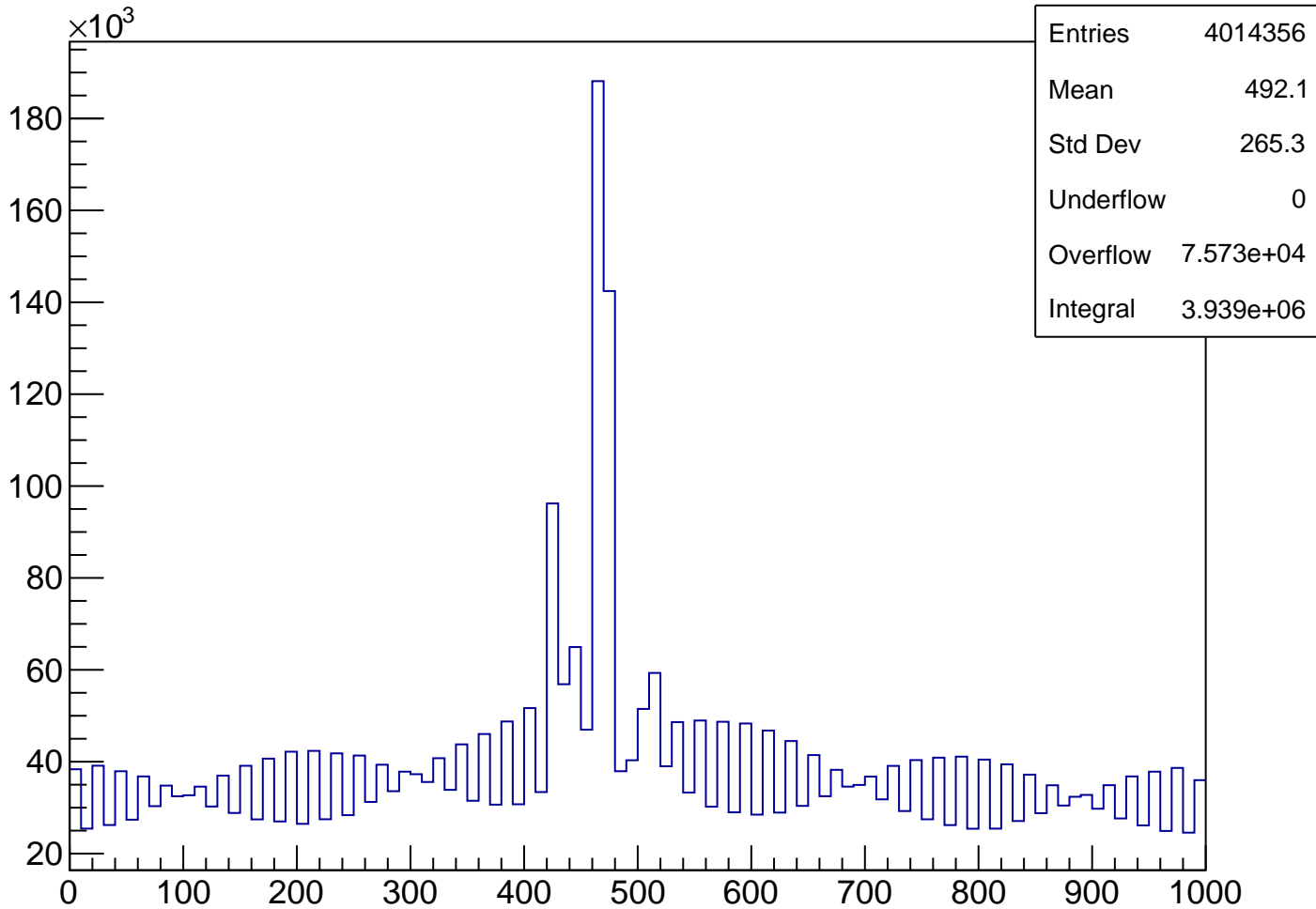
# Sch Hitpat



# Sch Depthpat

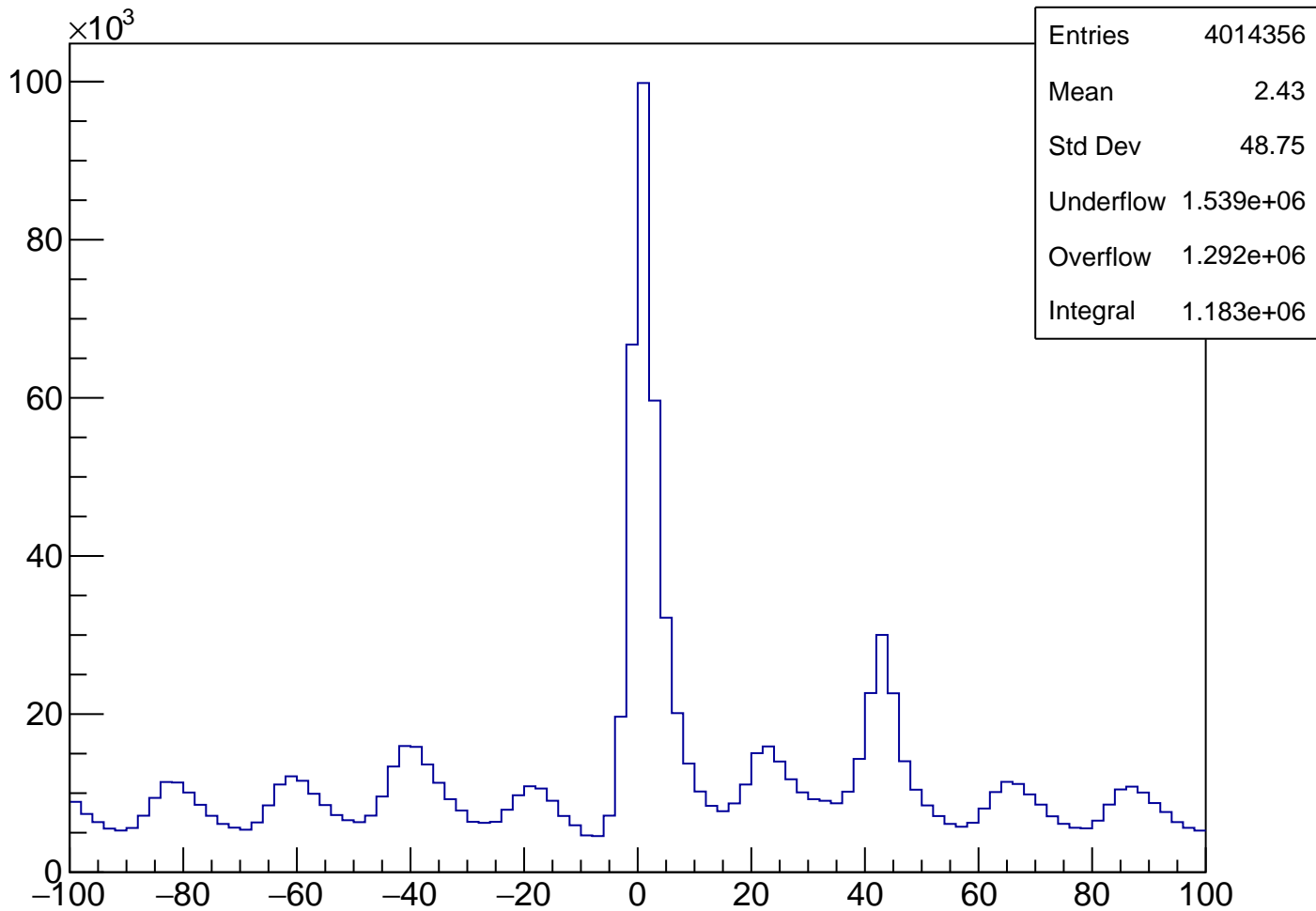


# Sch Tdc

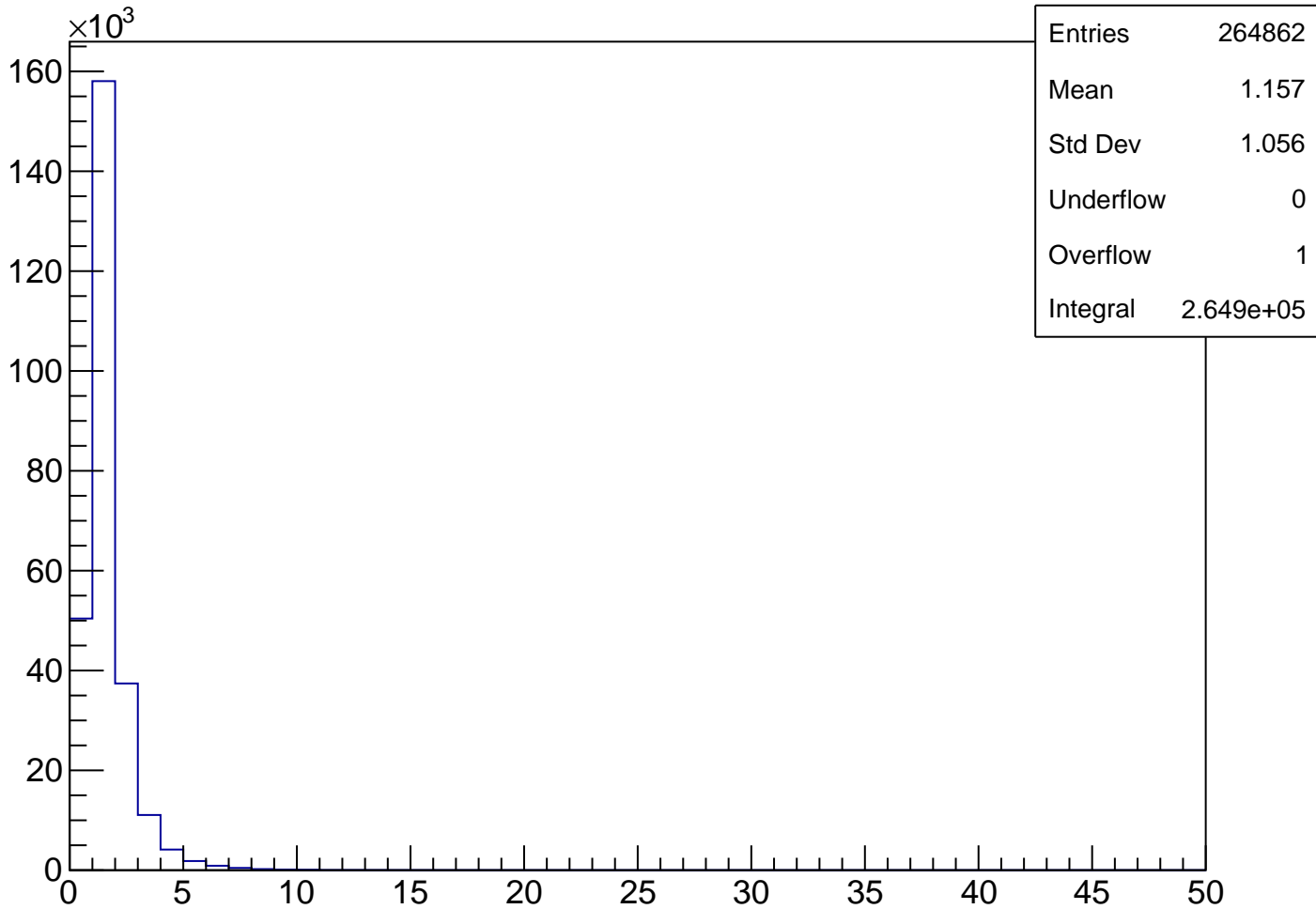




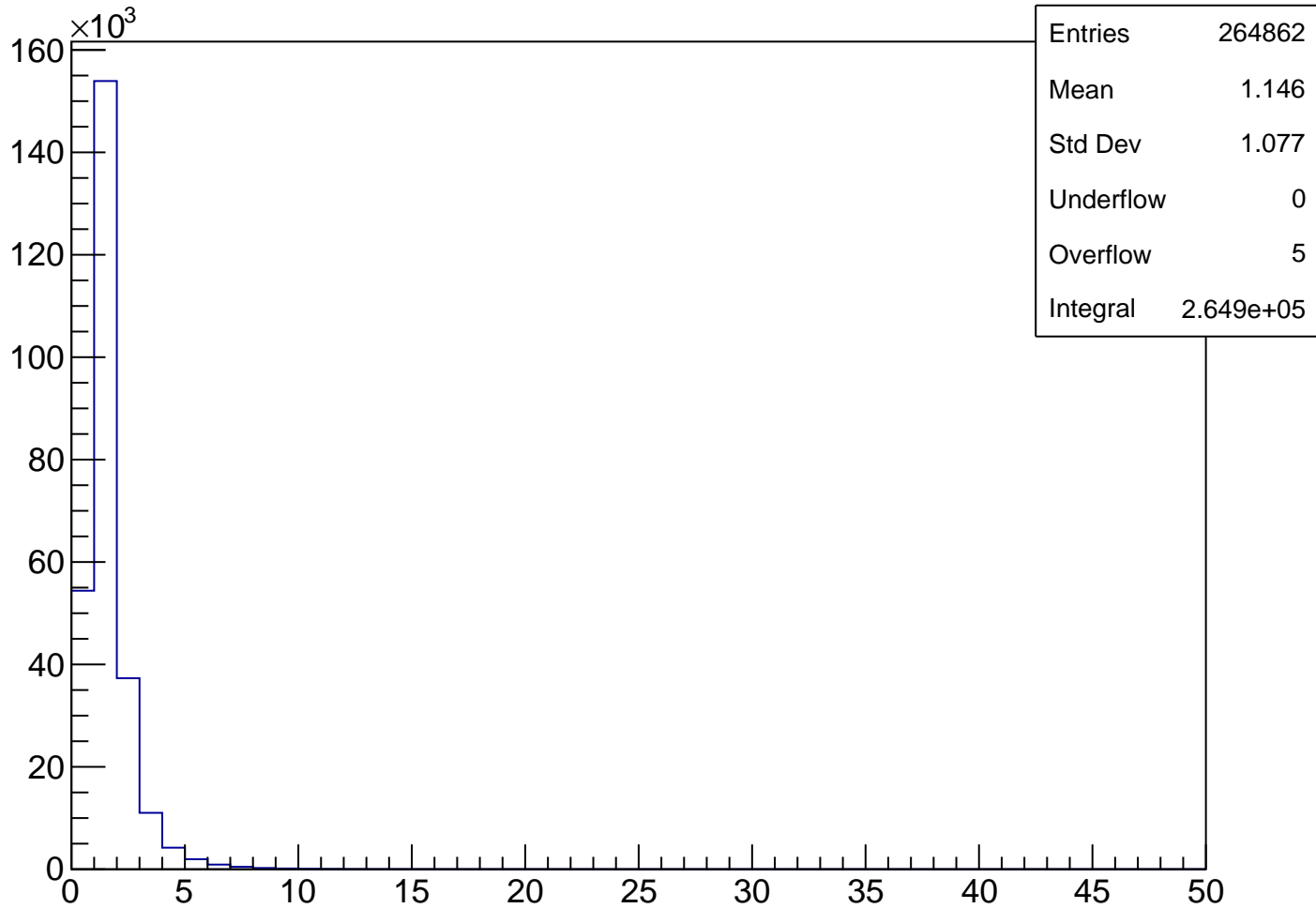
# Sch Time



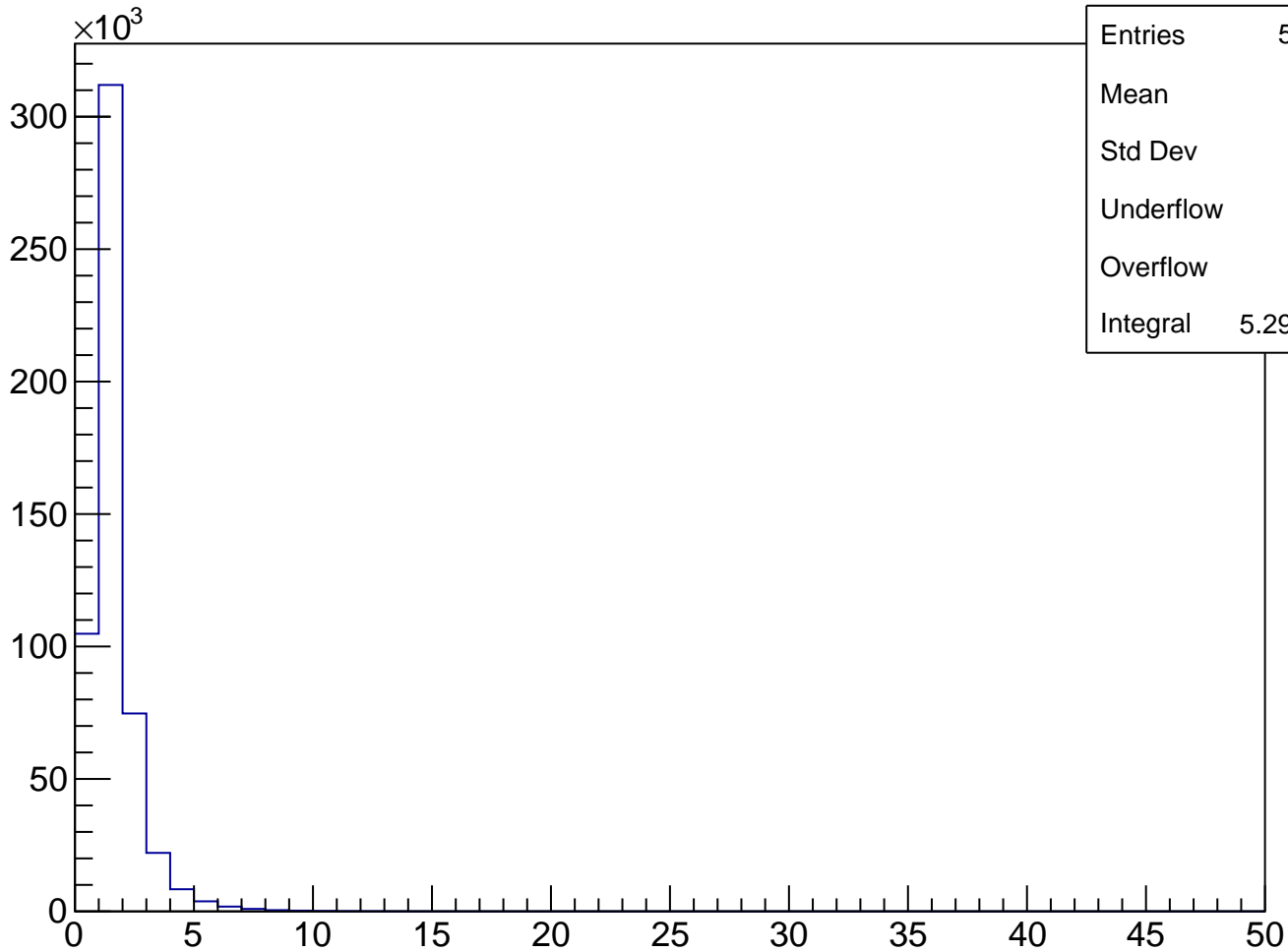
# SftX U Nhits



# SftX D Nhits

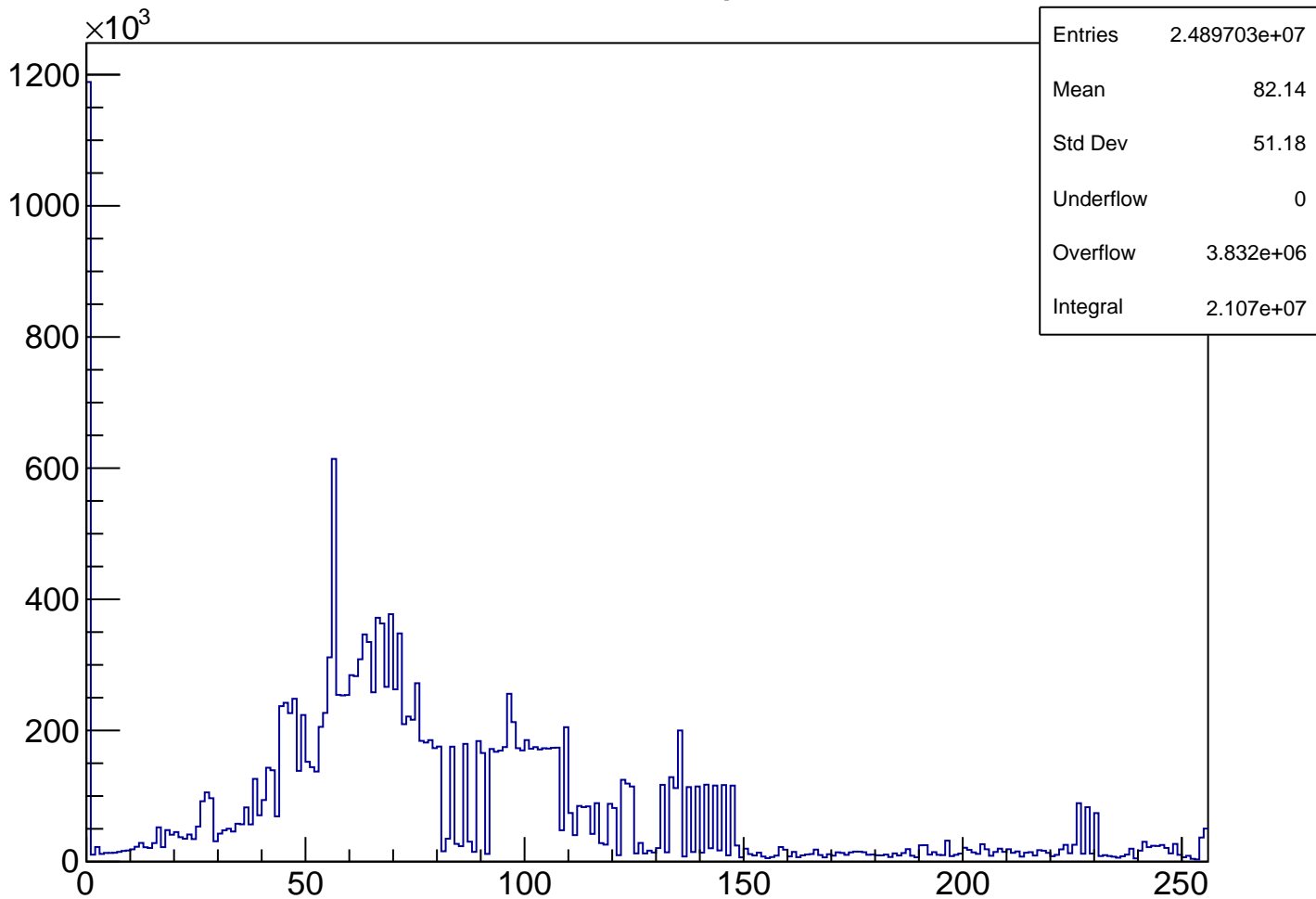


# SftXNhits

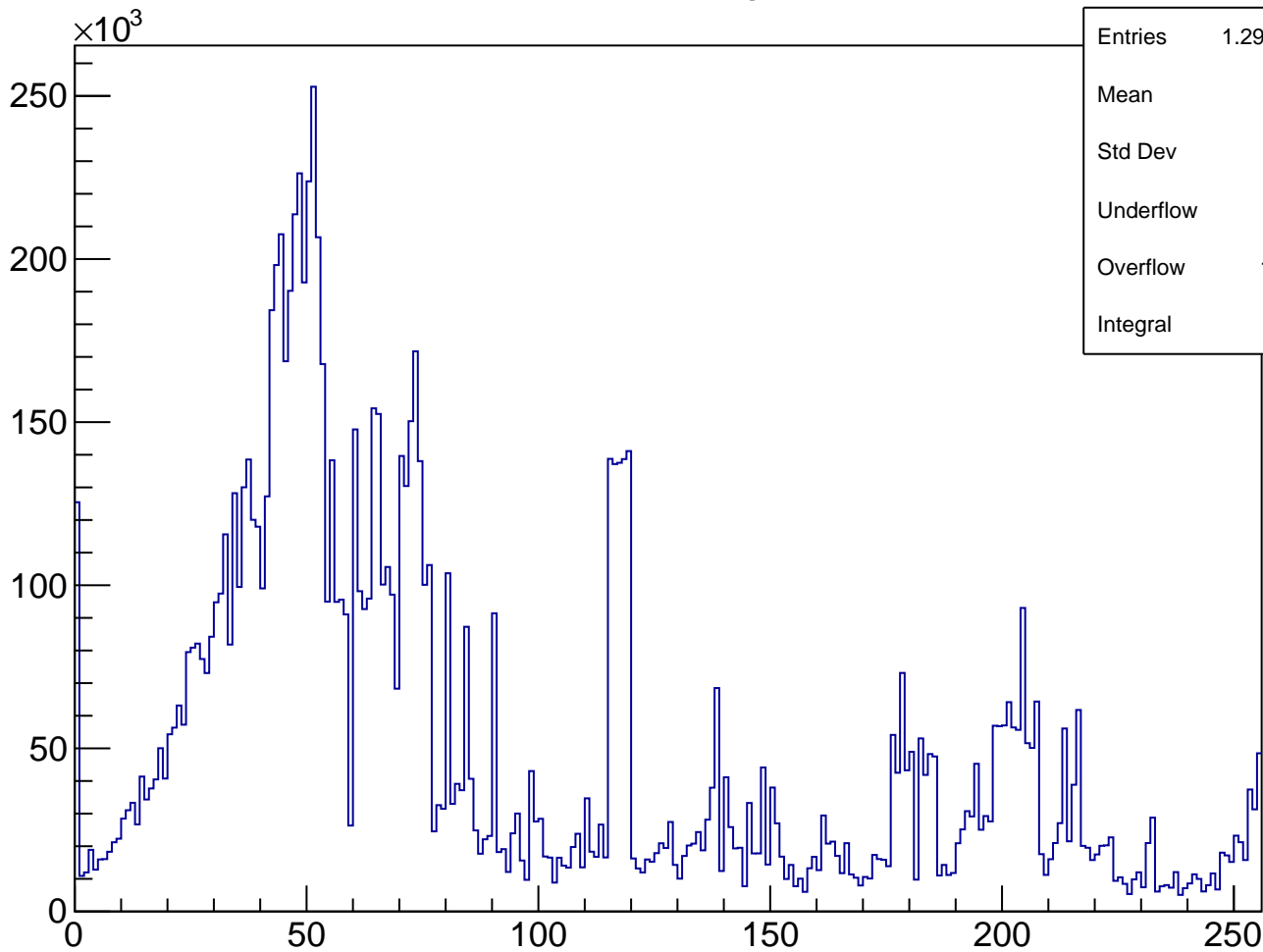


Entries	529724
Mean	1.152
Std Dev	1.066
Underflow	0
Overflow	6
Integral	5.297e+05

# SftX U Hitpat

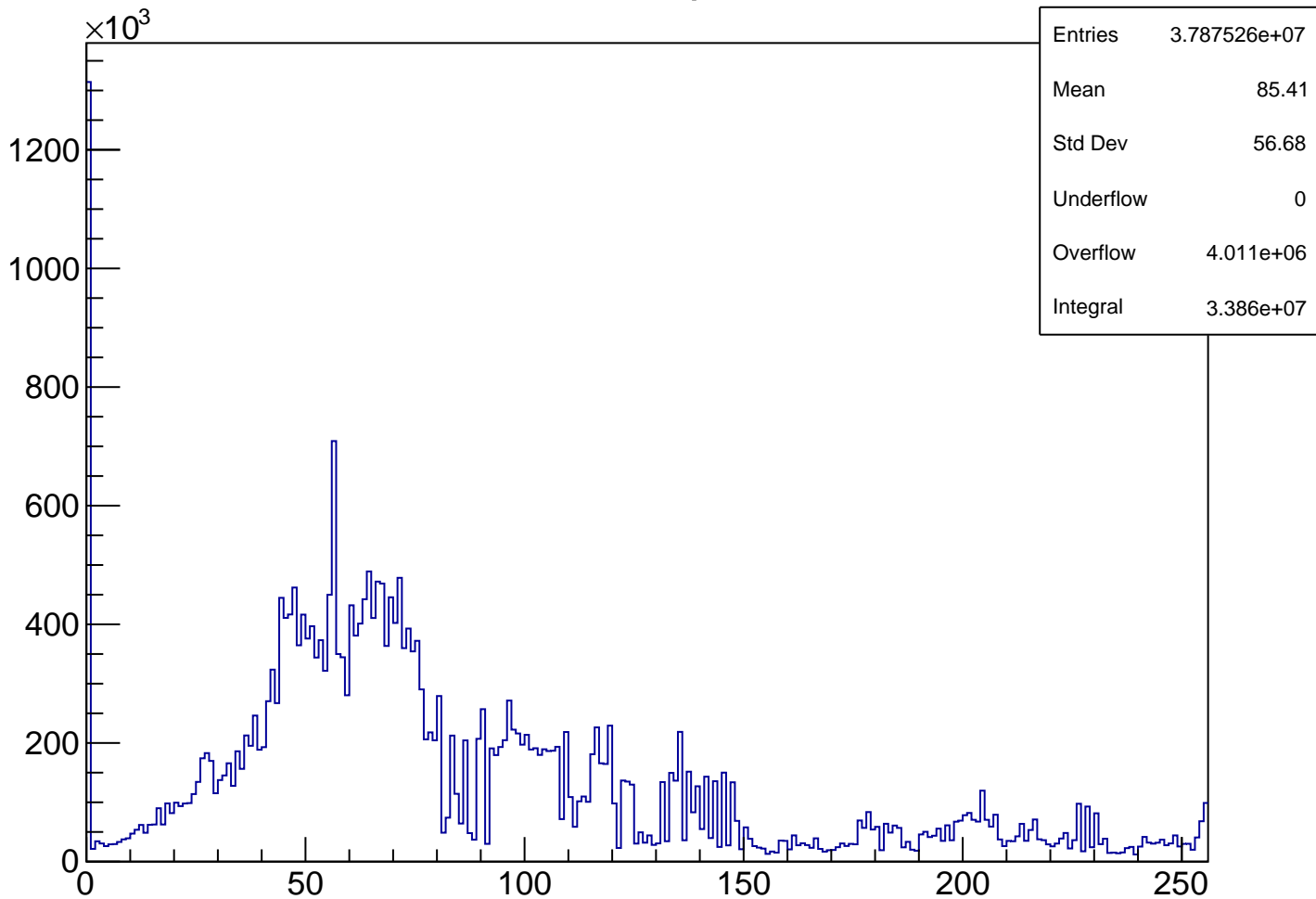


# SftX D Hitpat

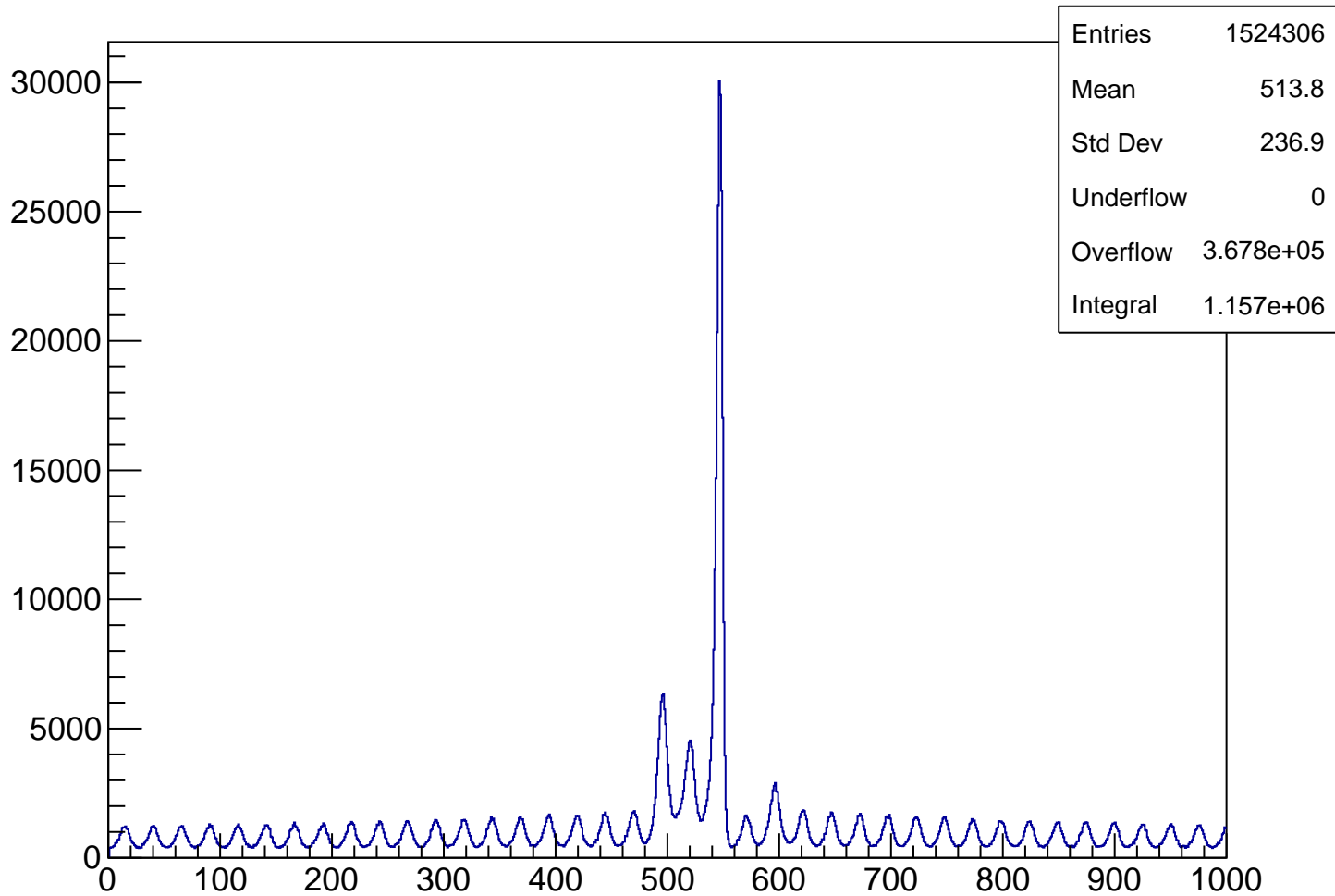


Entries	1.297824e+07
Mean	90.8
Std Dev	64.34
Underflow	0
Overflow	1.799e+05
Integral	1.28e+07

# SftXHitpat

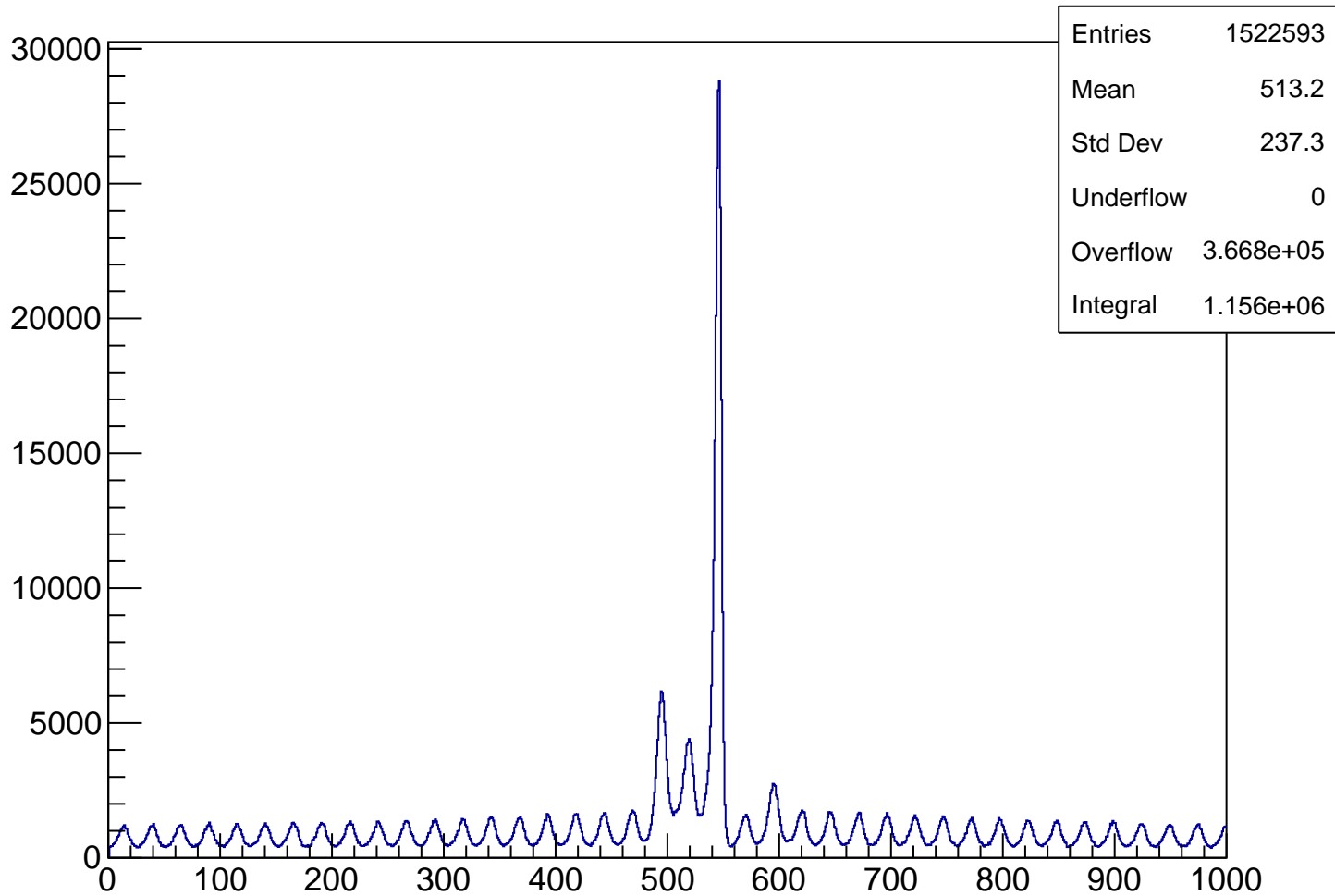


# SftX U Tdc

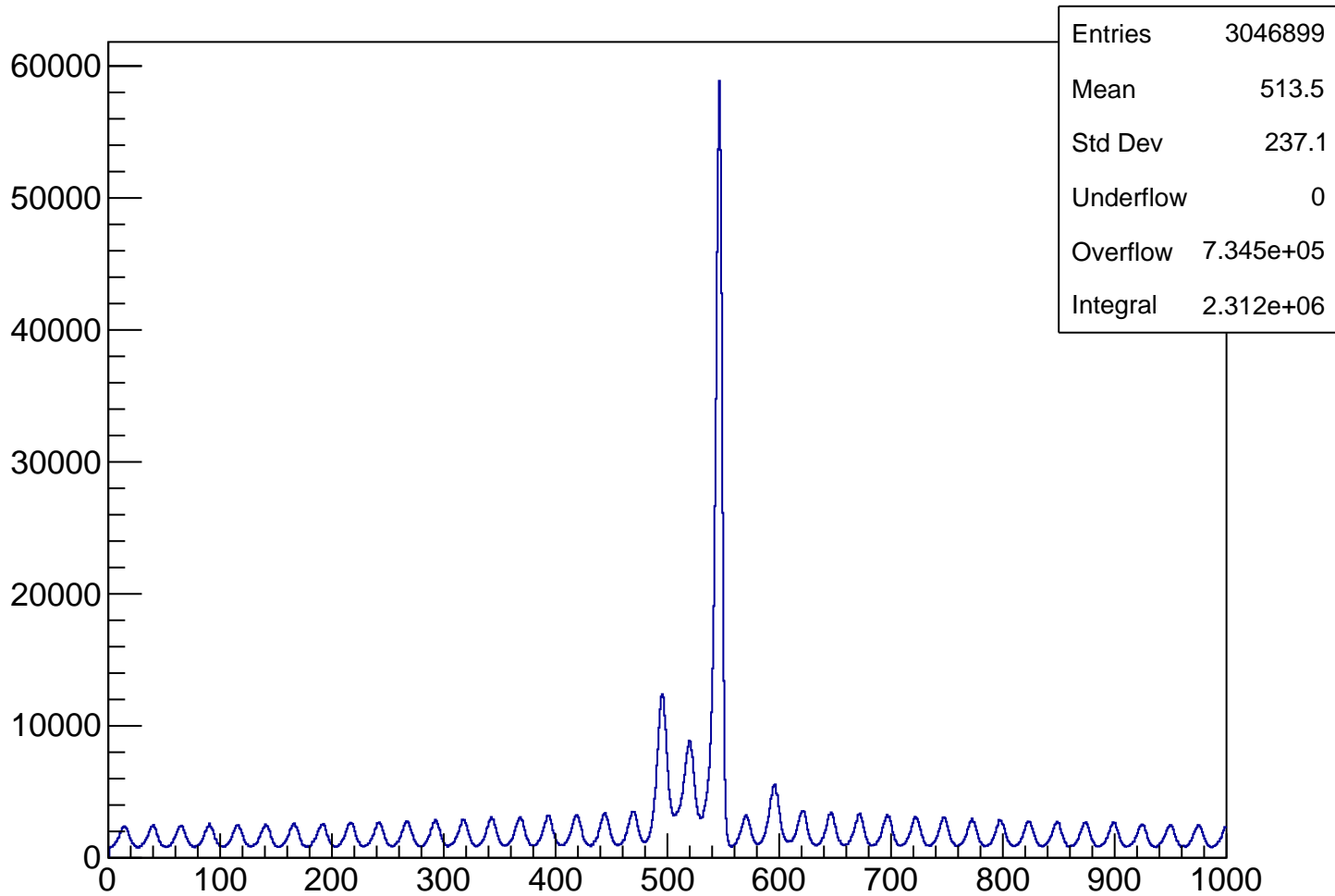




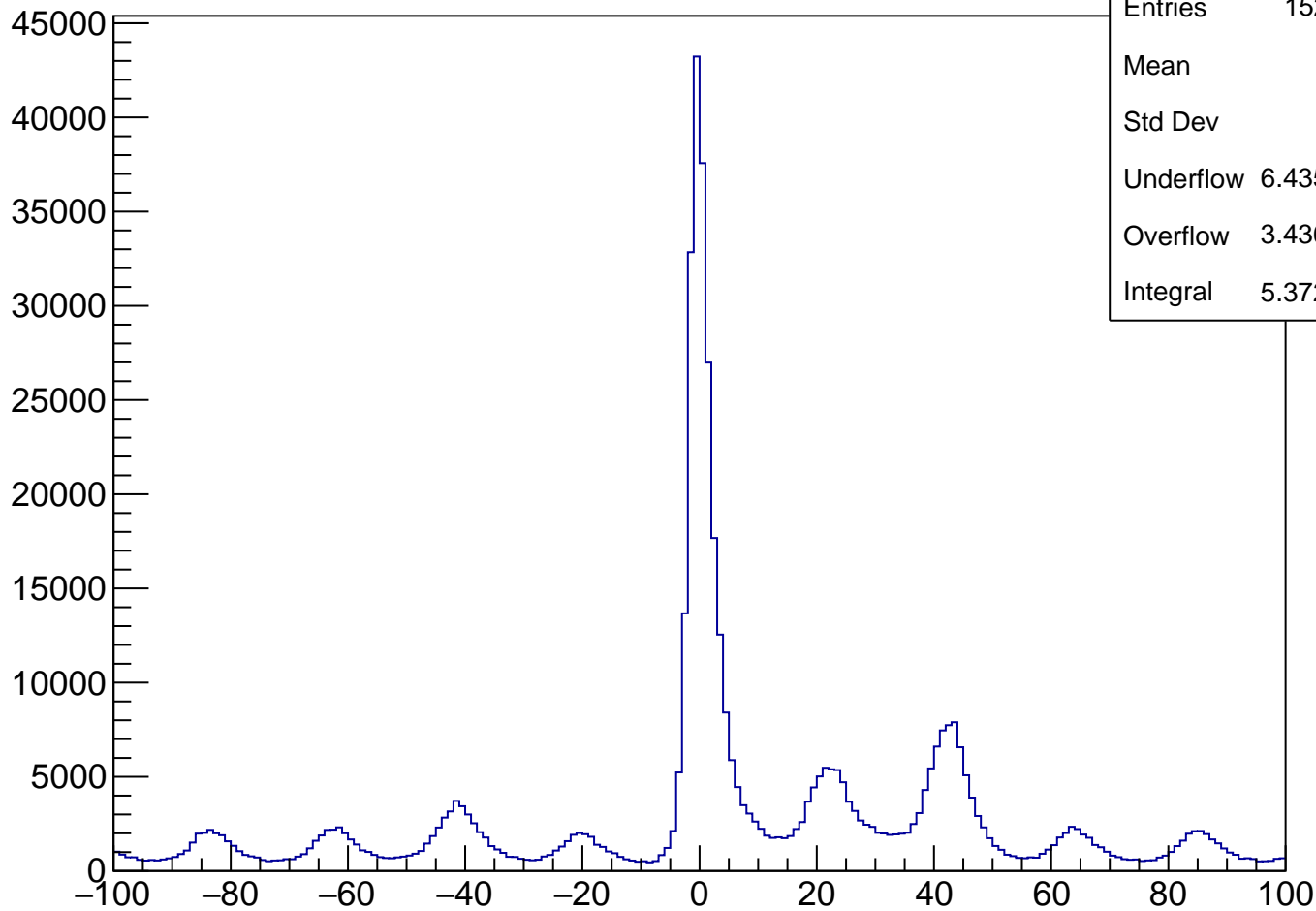
# SftX D Tdc



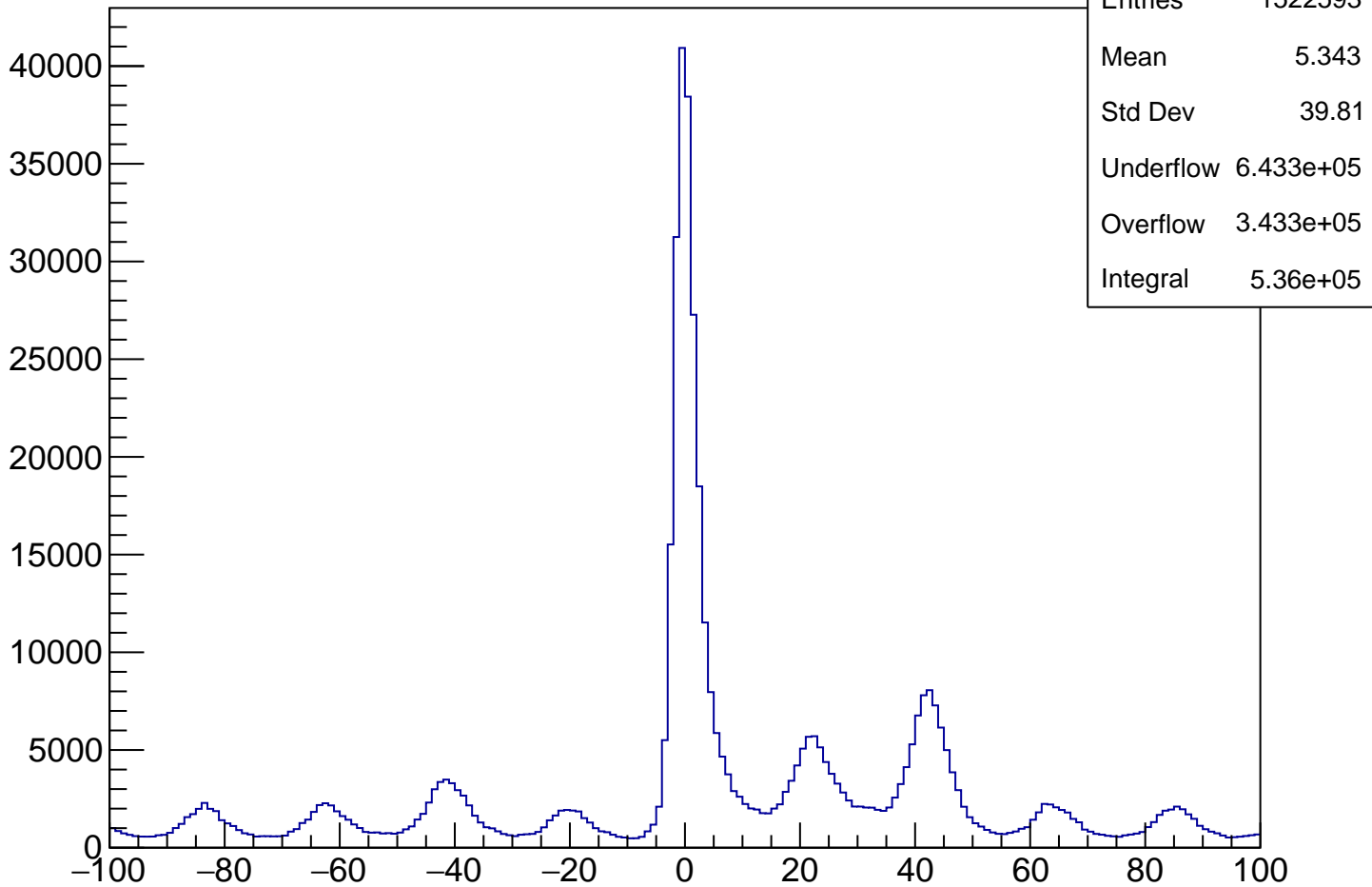
# SftXTdc



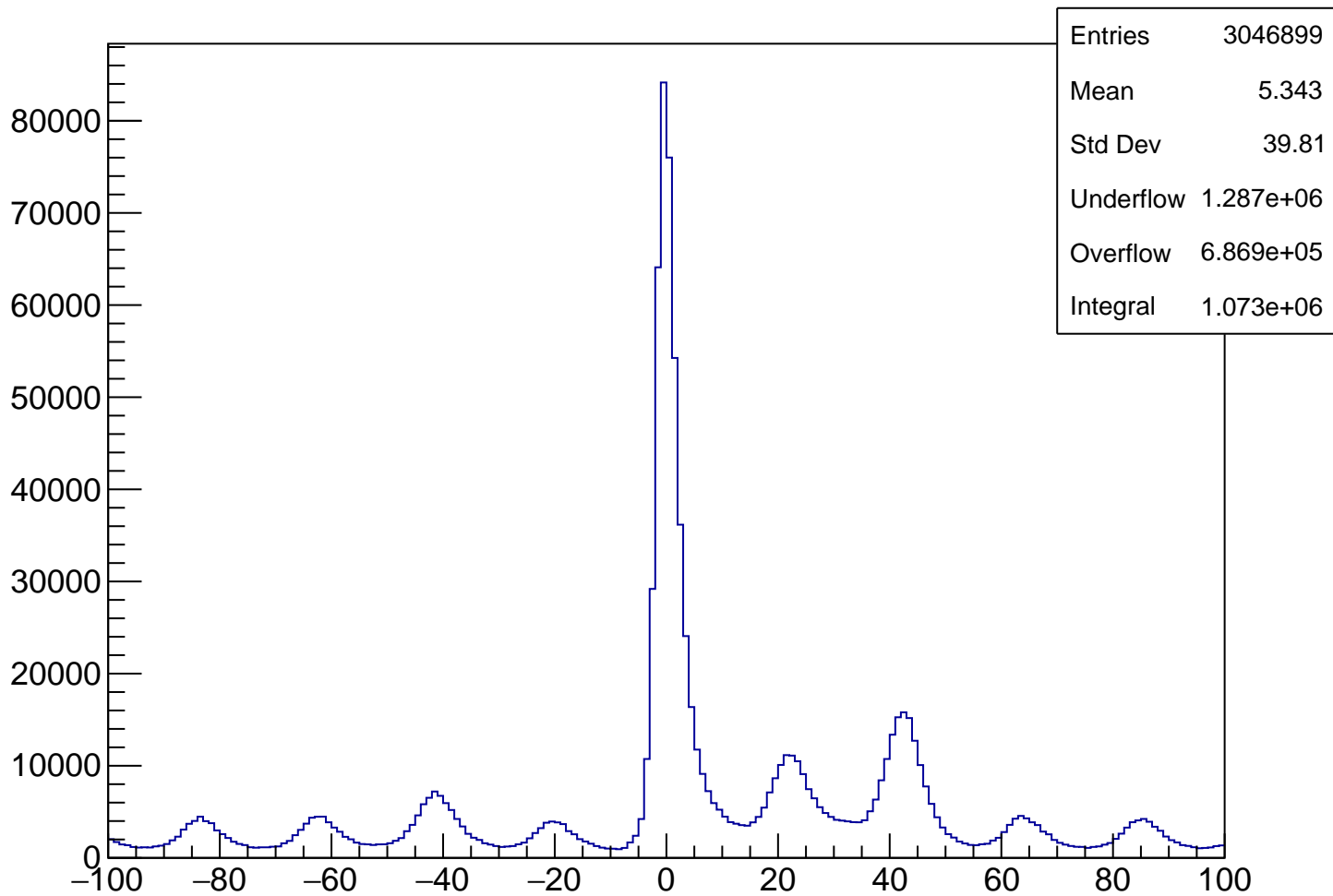
# SftX U Time



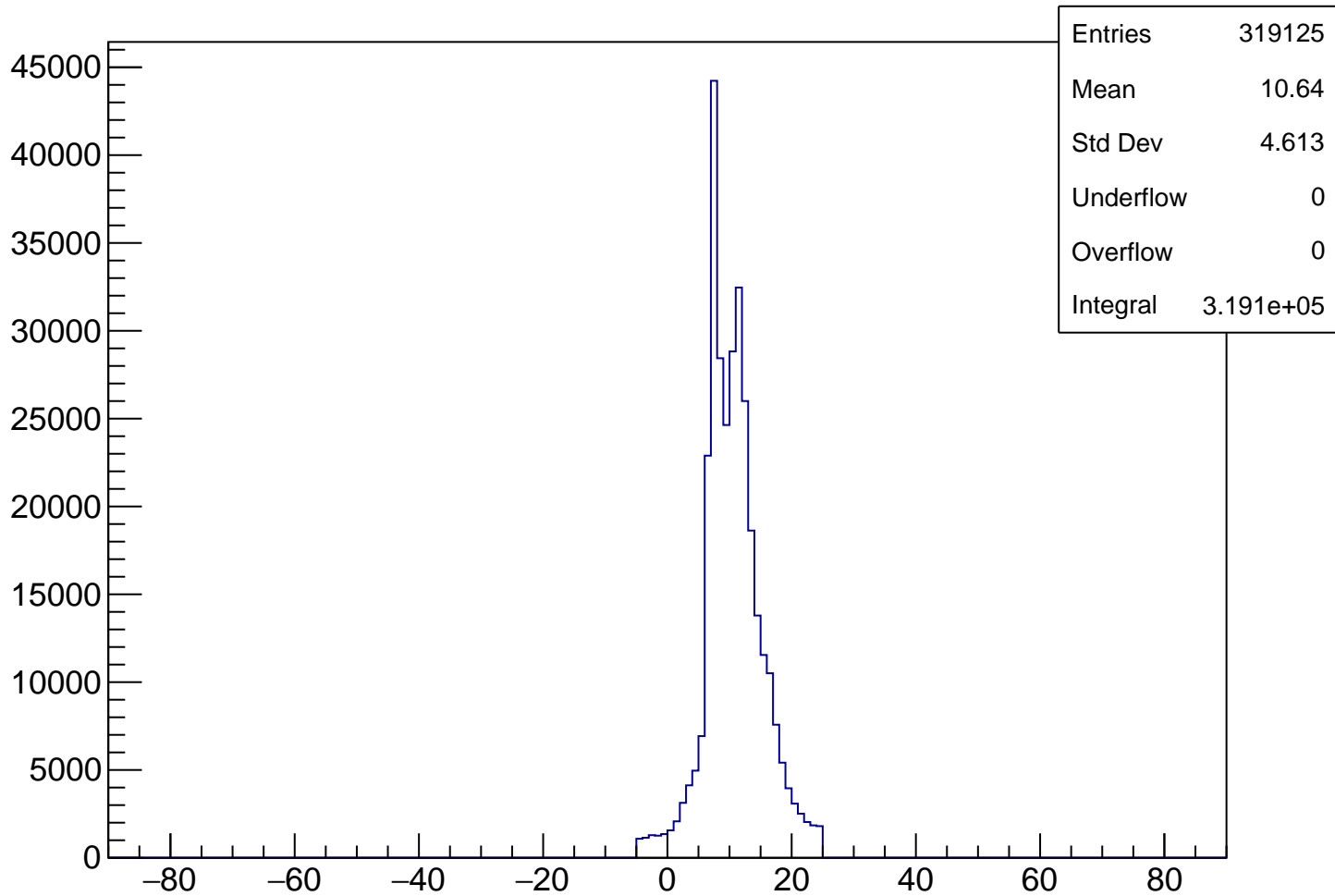
# SftX D Time



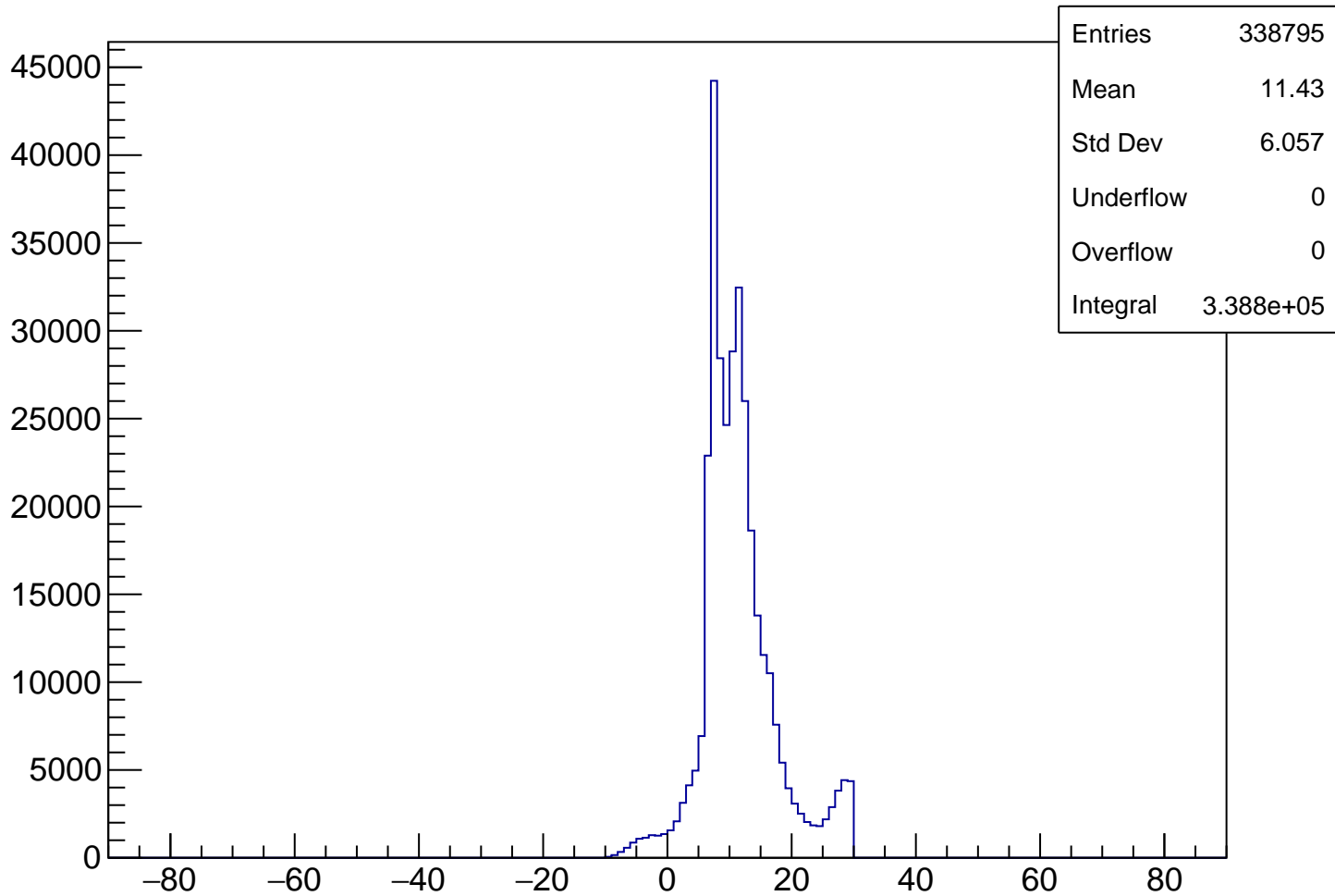
# SftXTime



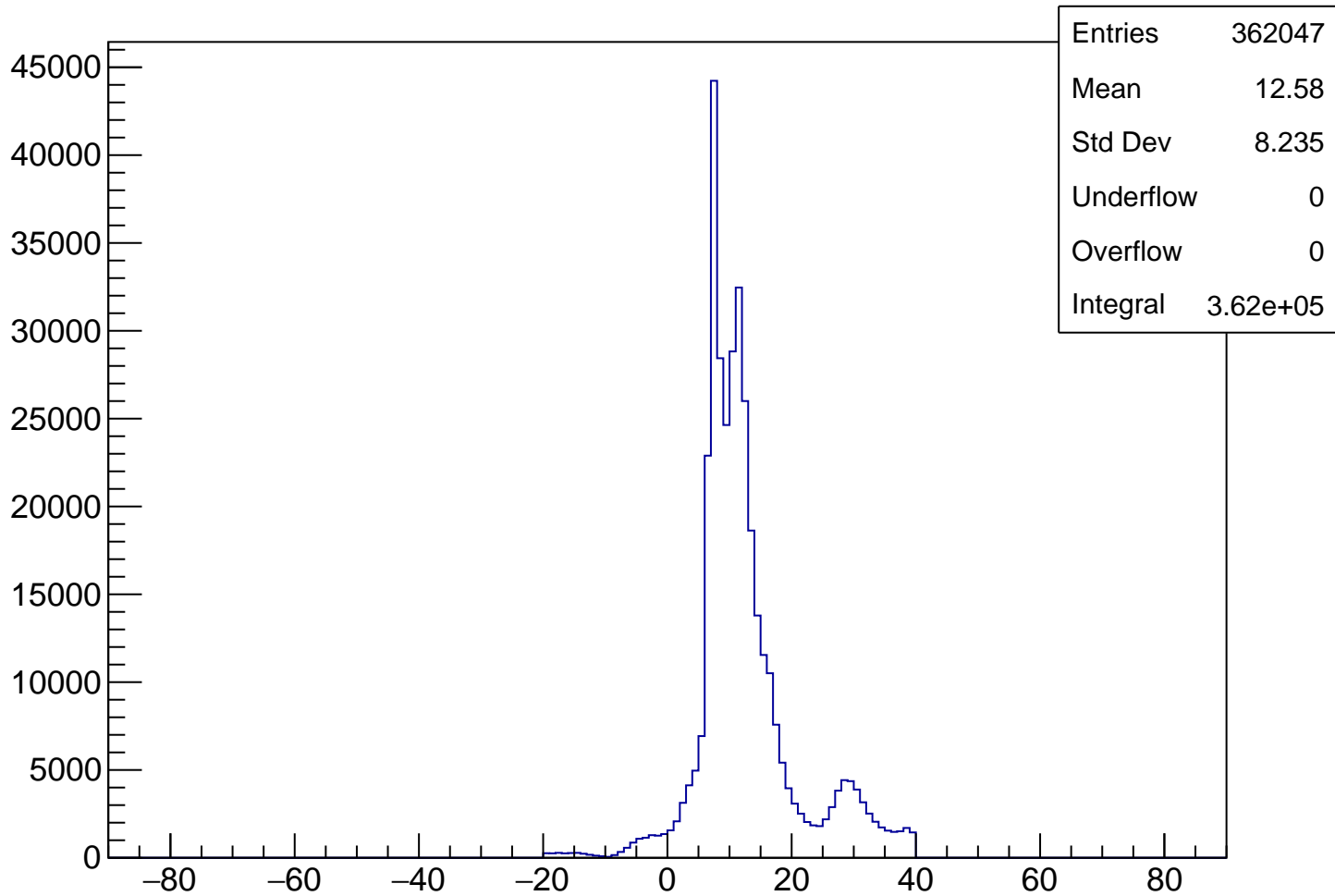
# TofMtOr Gate1



# TofMtOr Gate2

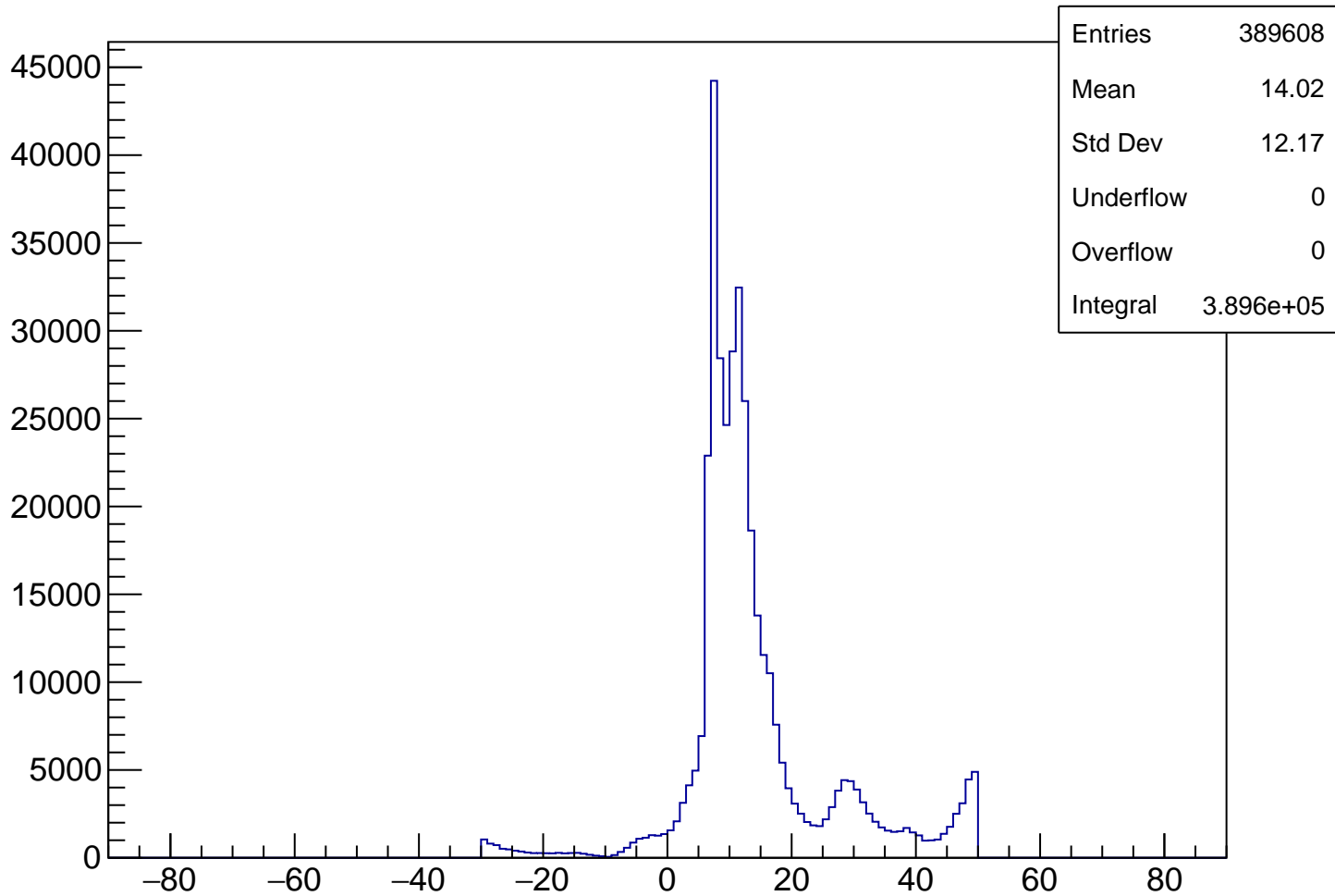


# TofMtOr Gate3

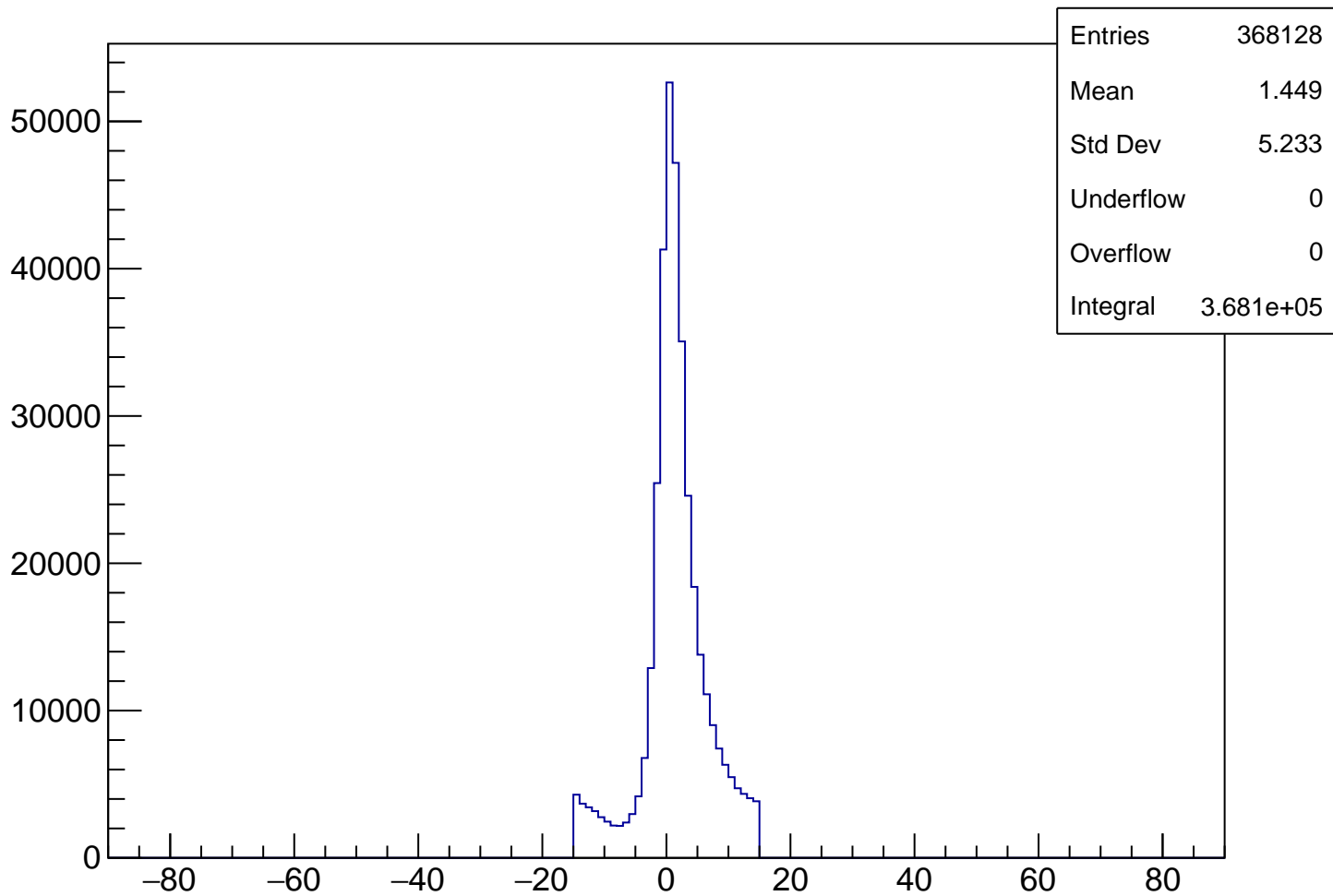




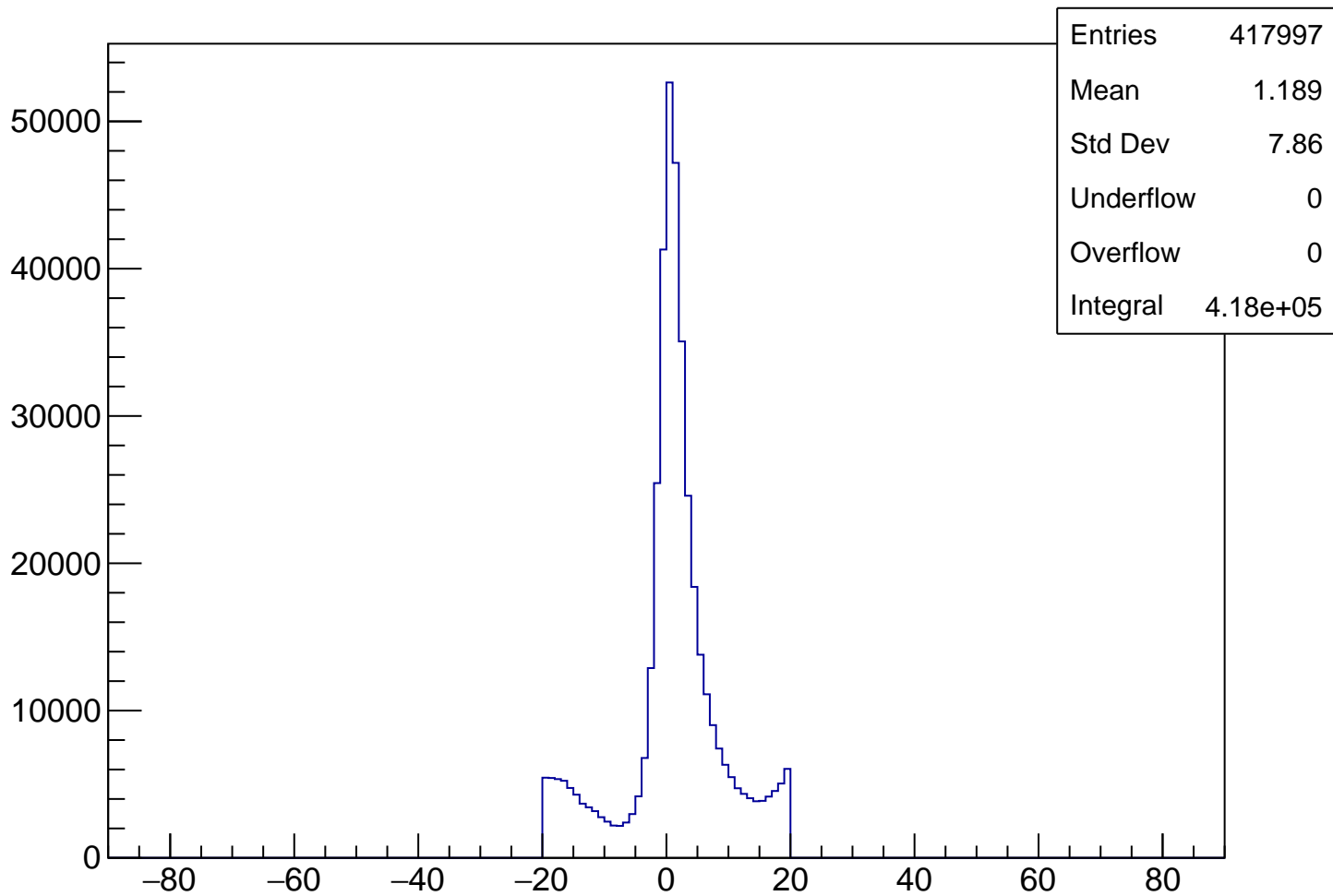
# TofMtOr Gate4



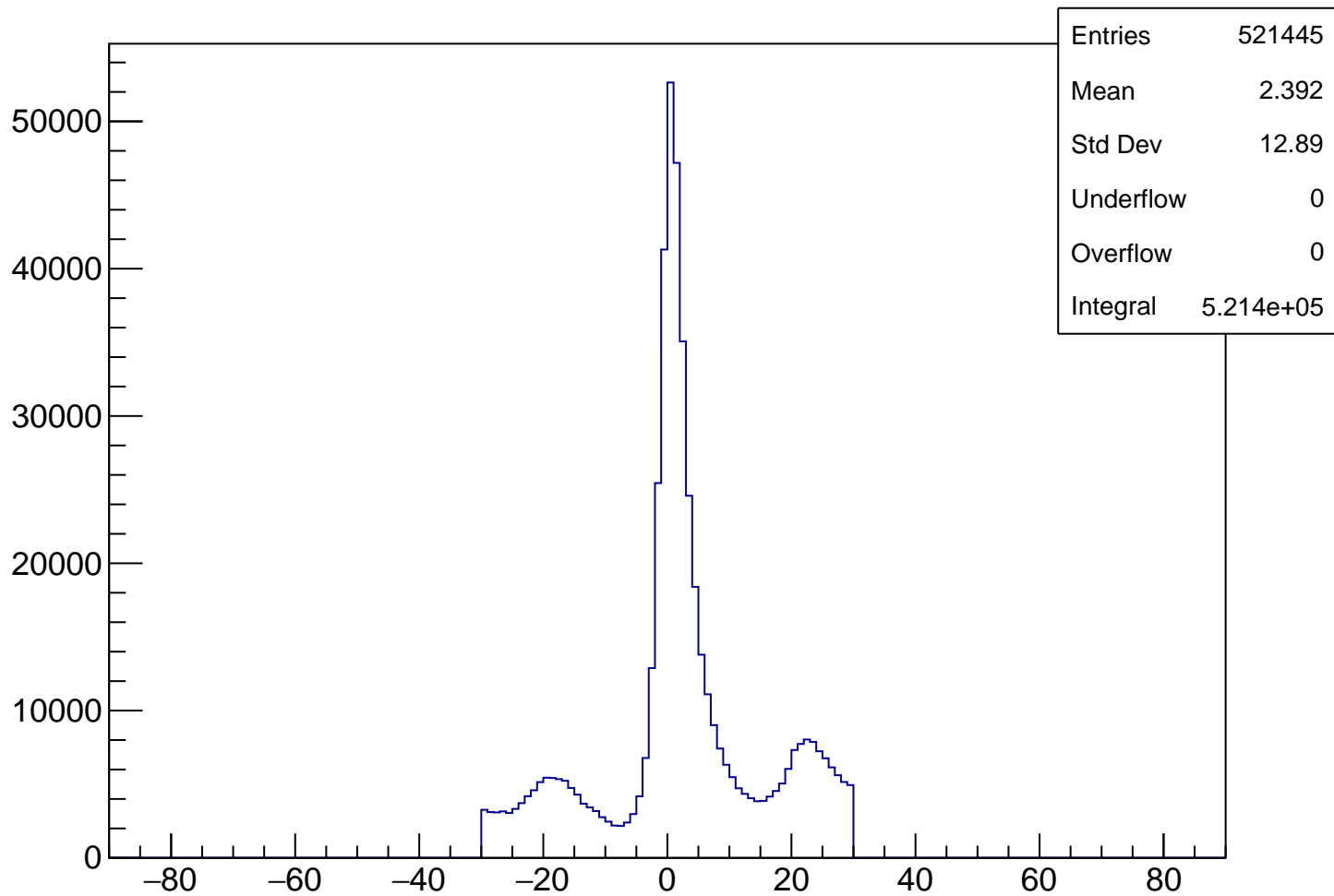
# Sch Time Gate1



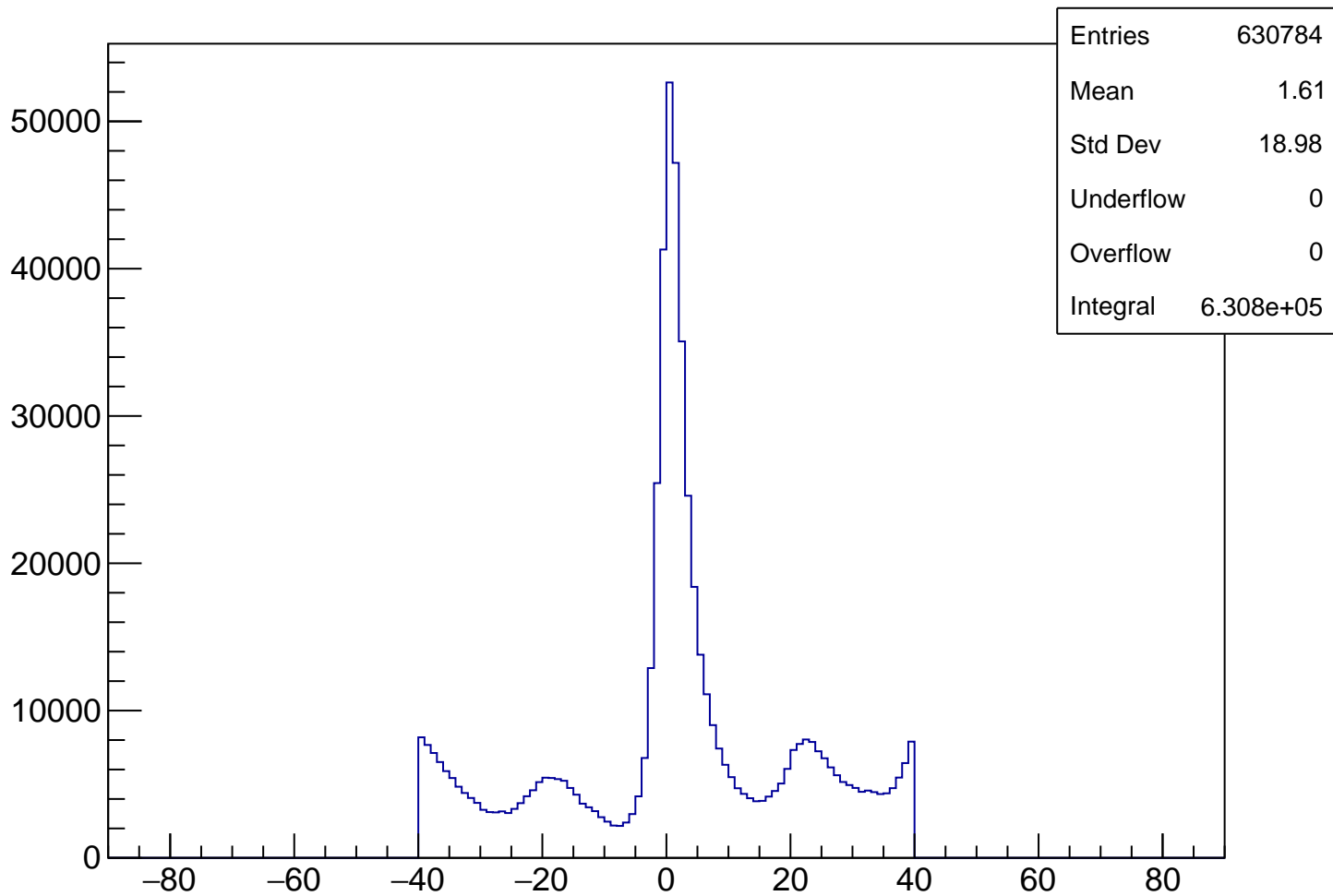
# Sch Time Gate2



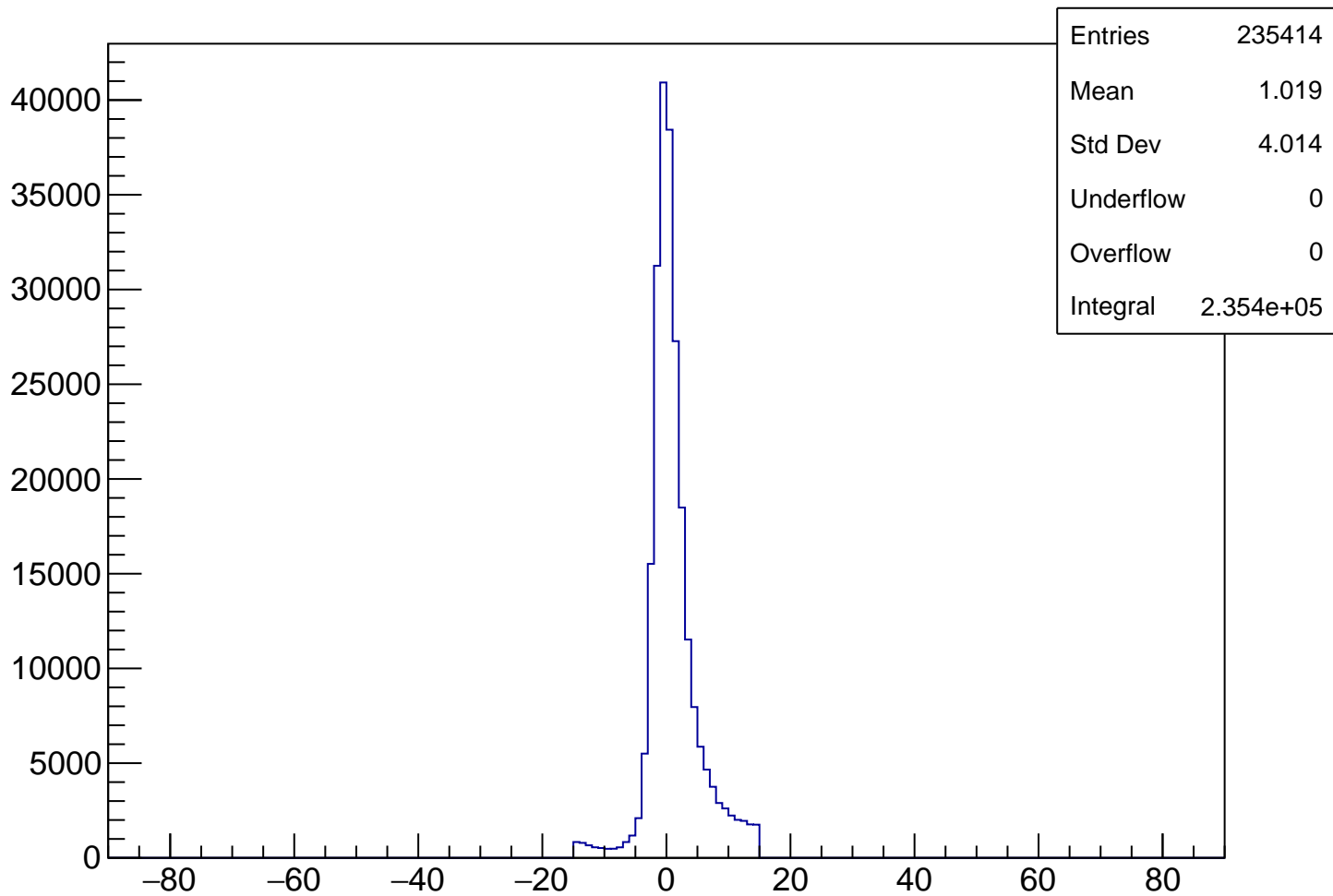
# Sch Time Gate3



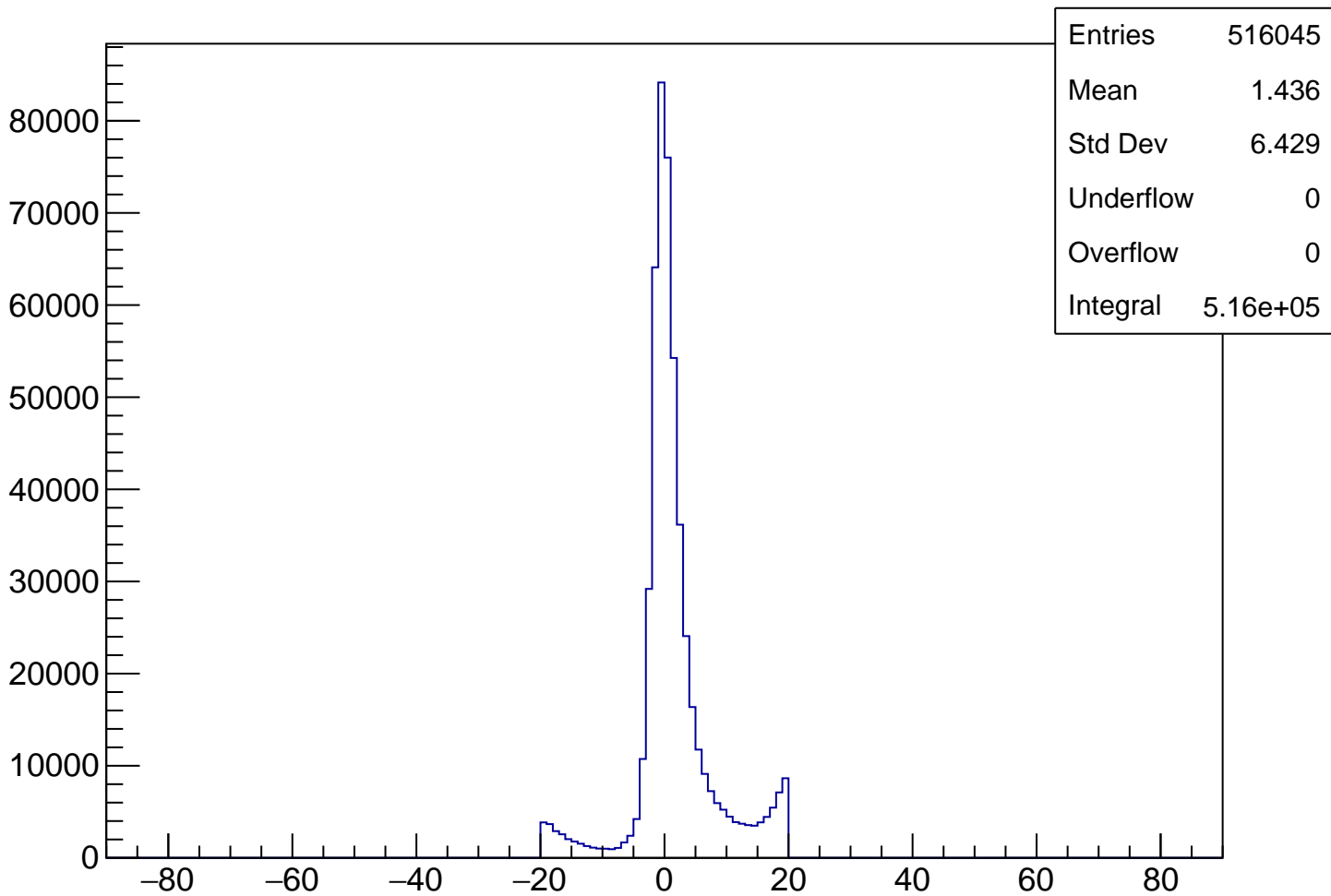
# Sch Time Gate4



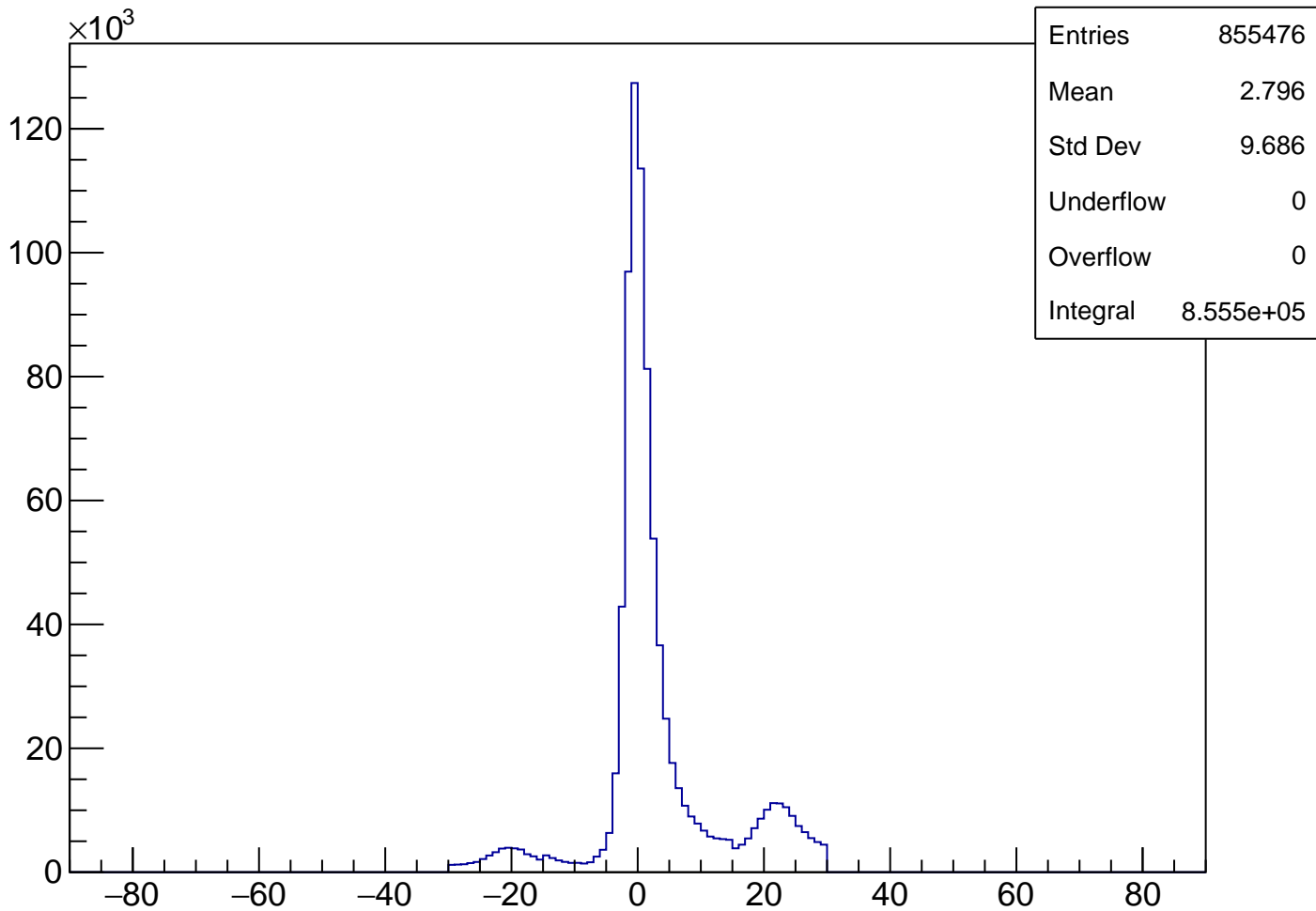
# SftXTime Gate1



# SftXTime Gate2



# SftXTime Gate3





# SftXTime Gate4

