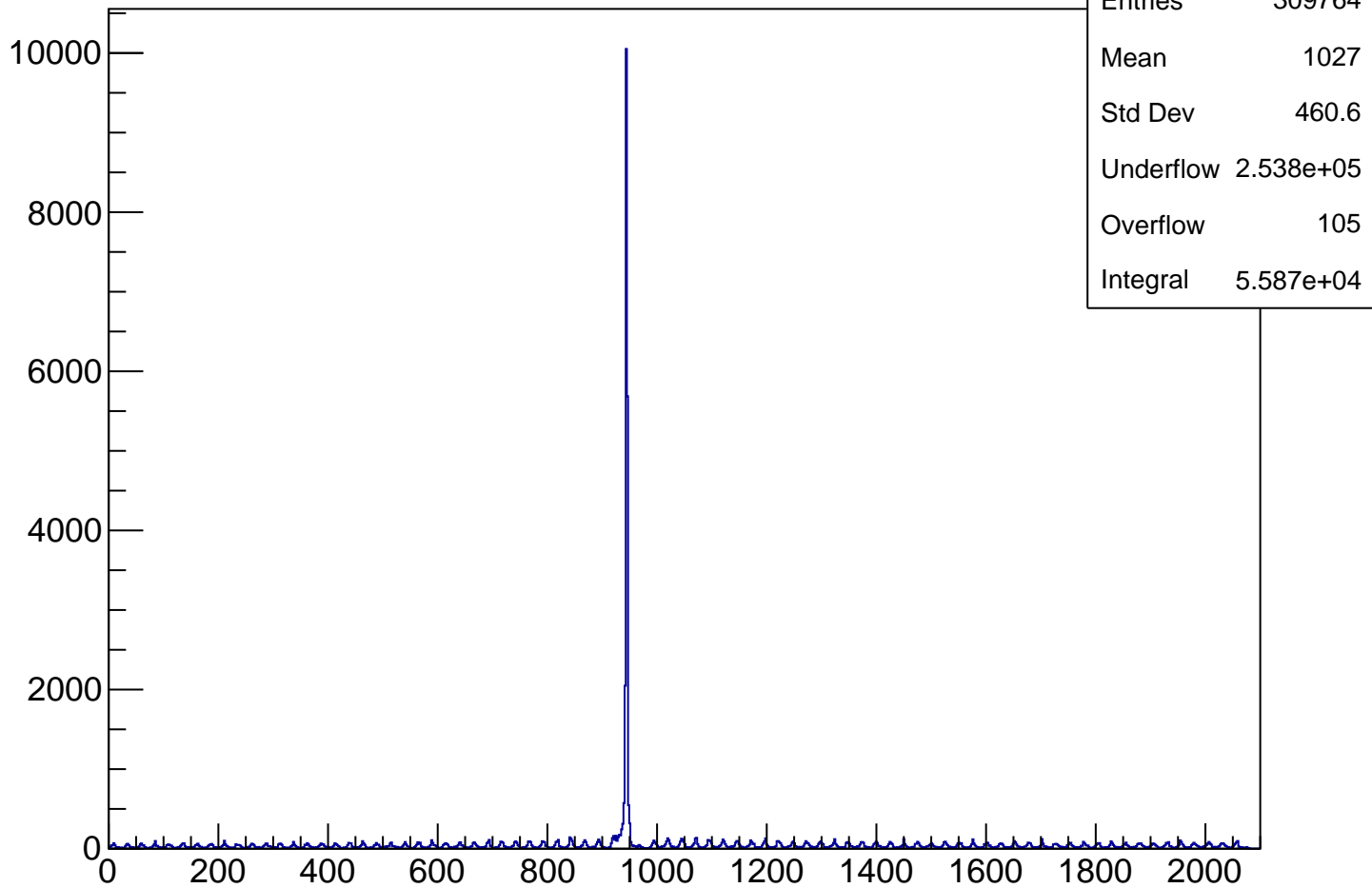
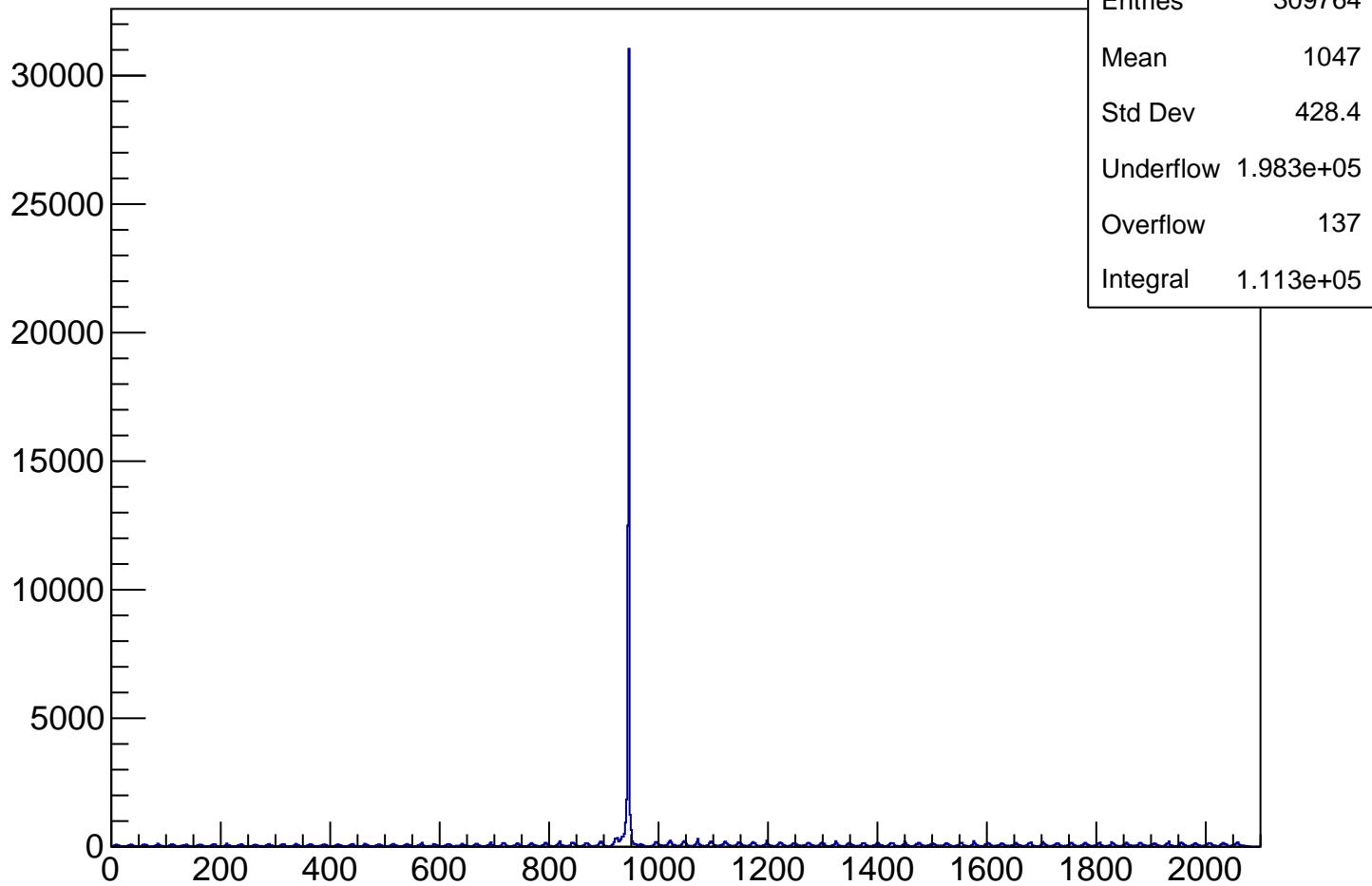


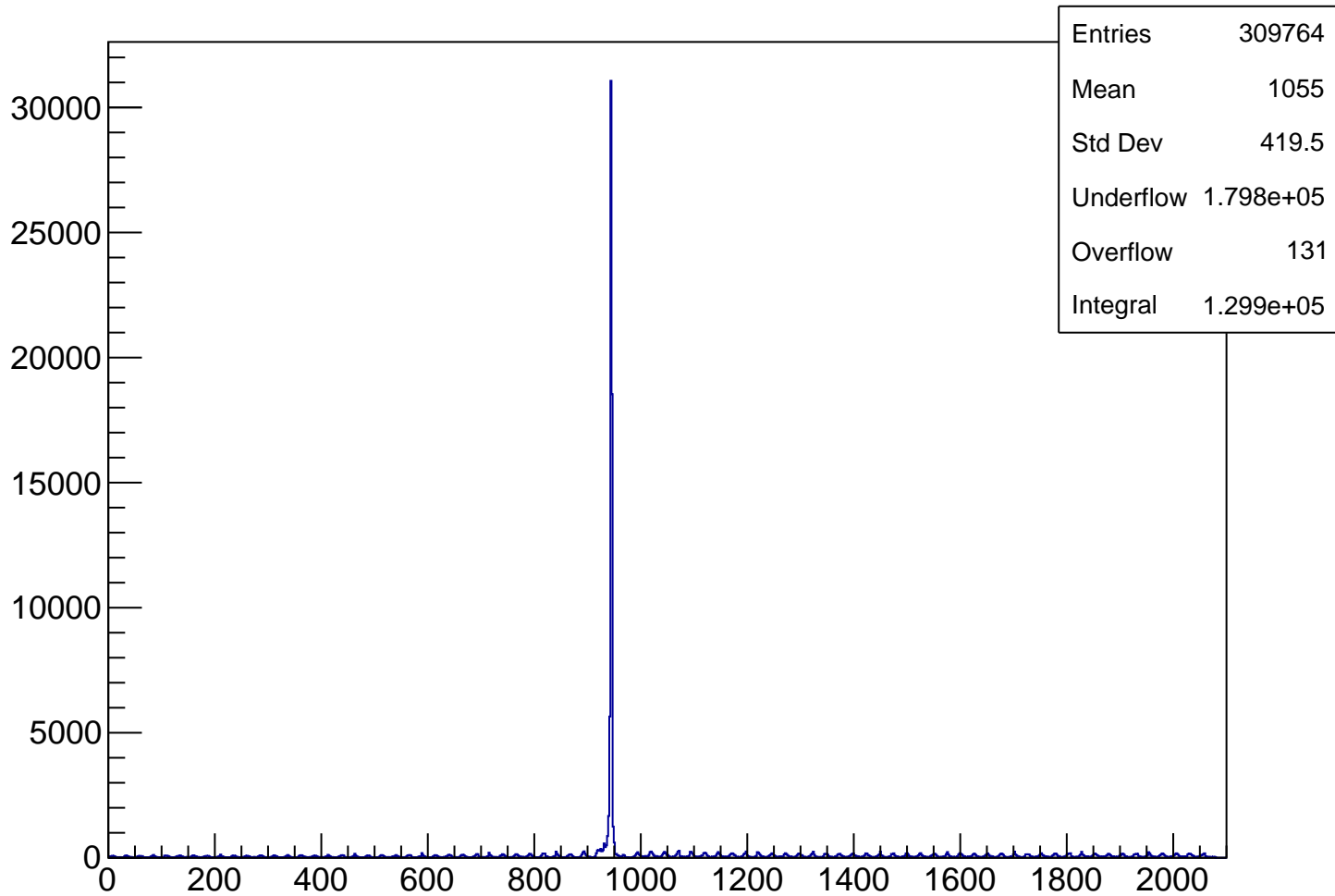
# TrigFlag Bh21K



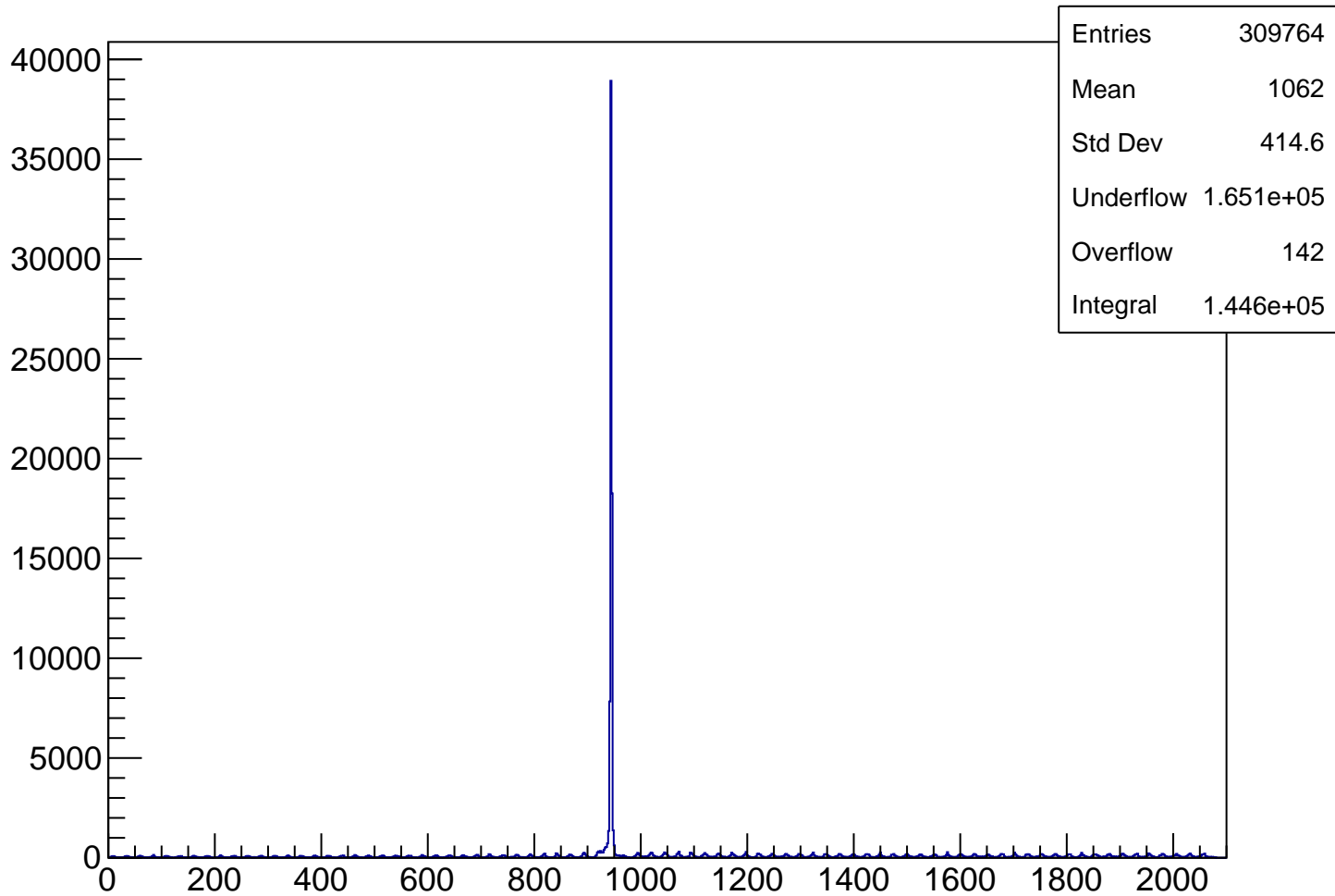
# TrigFlag Bh22K



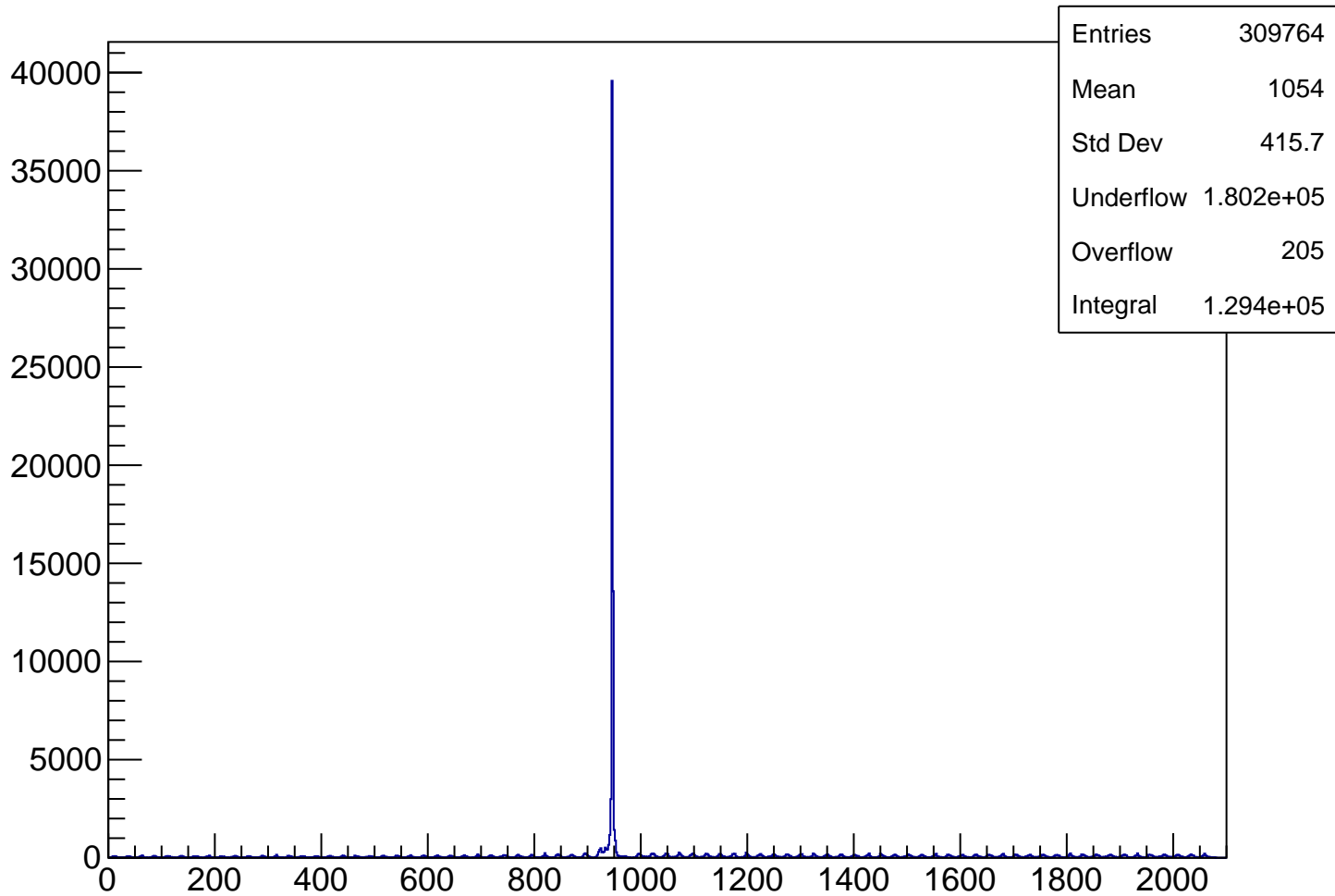
# TrigFlag Bh23K



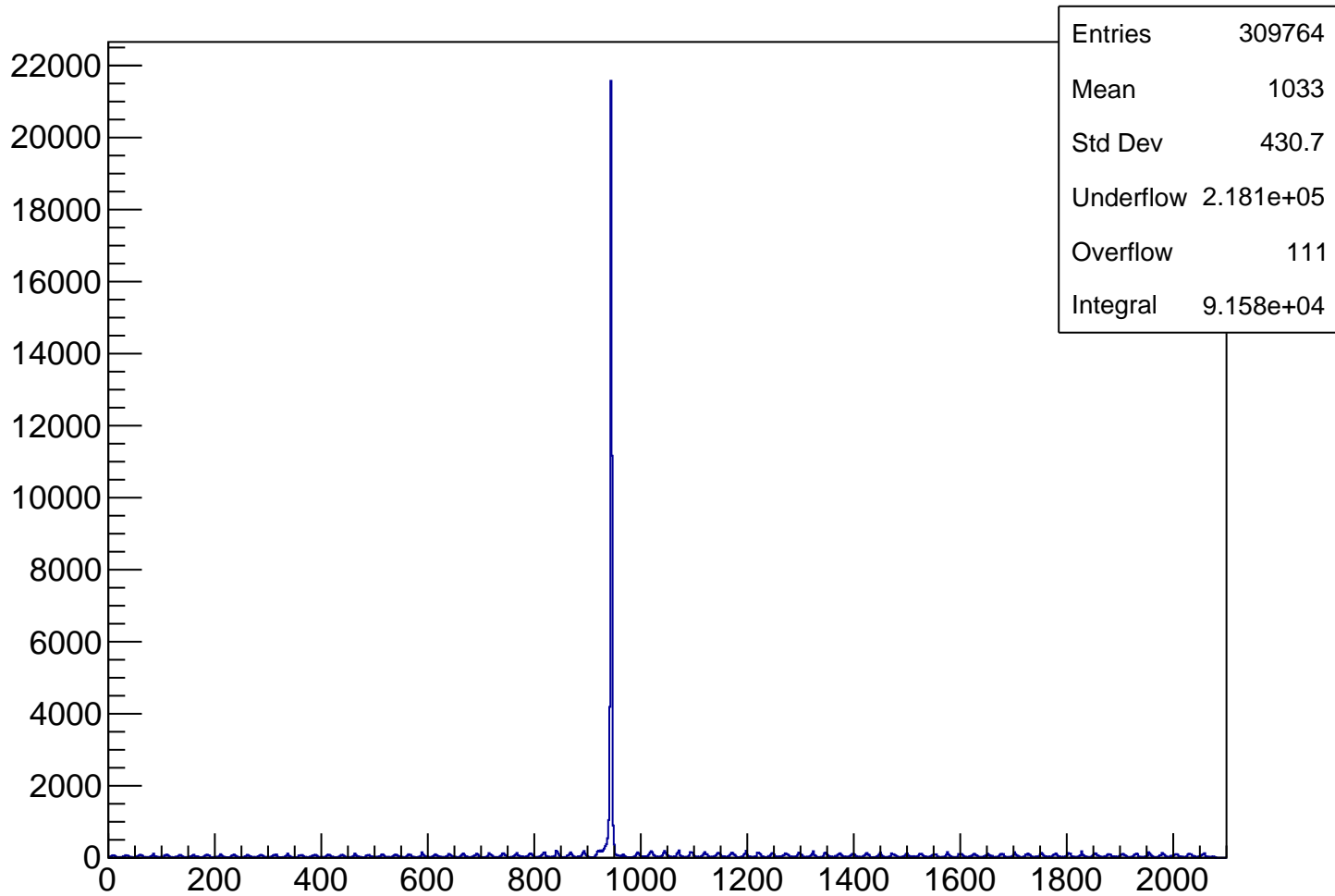
# TrigFlag Bh24K



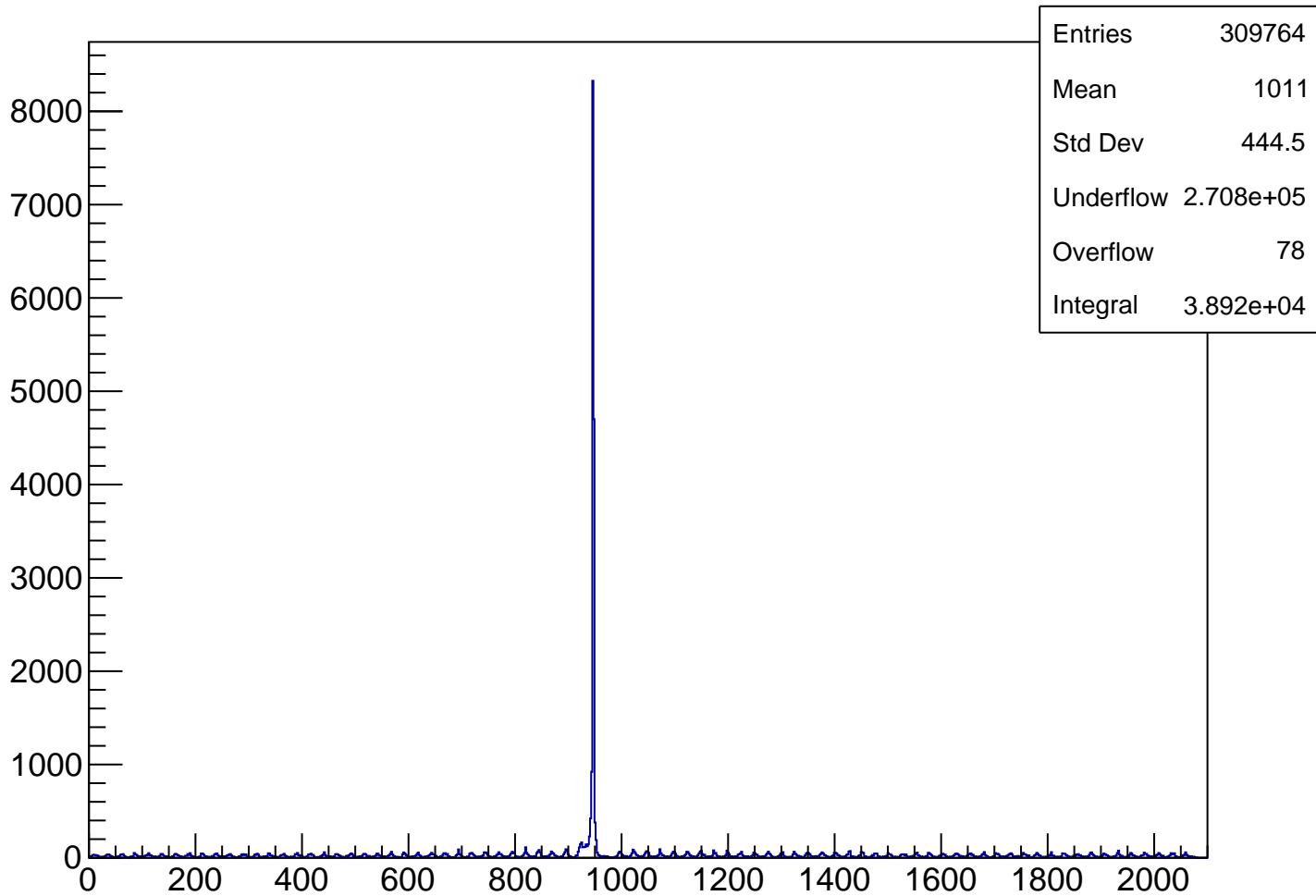
# TrigFlag Bh25K



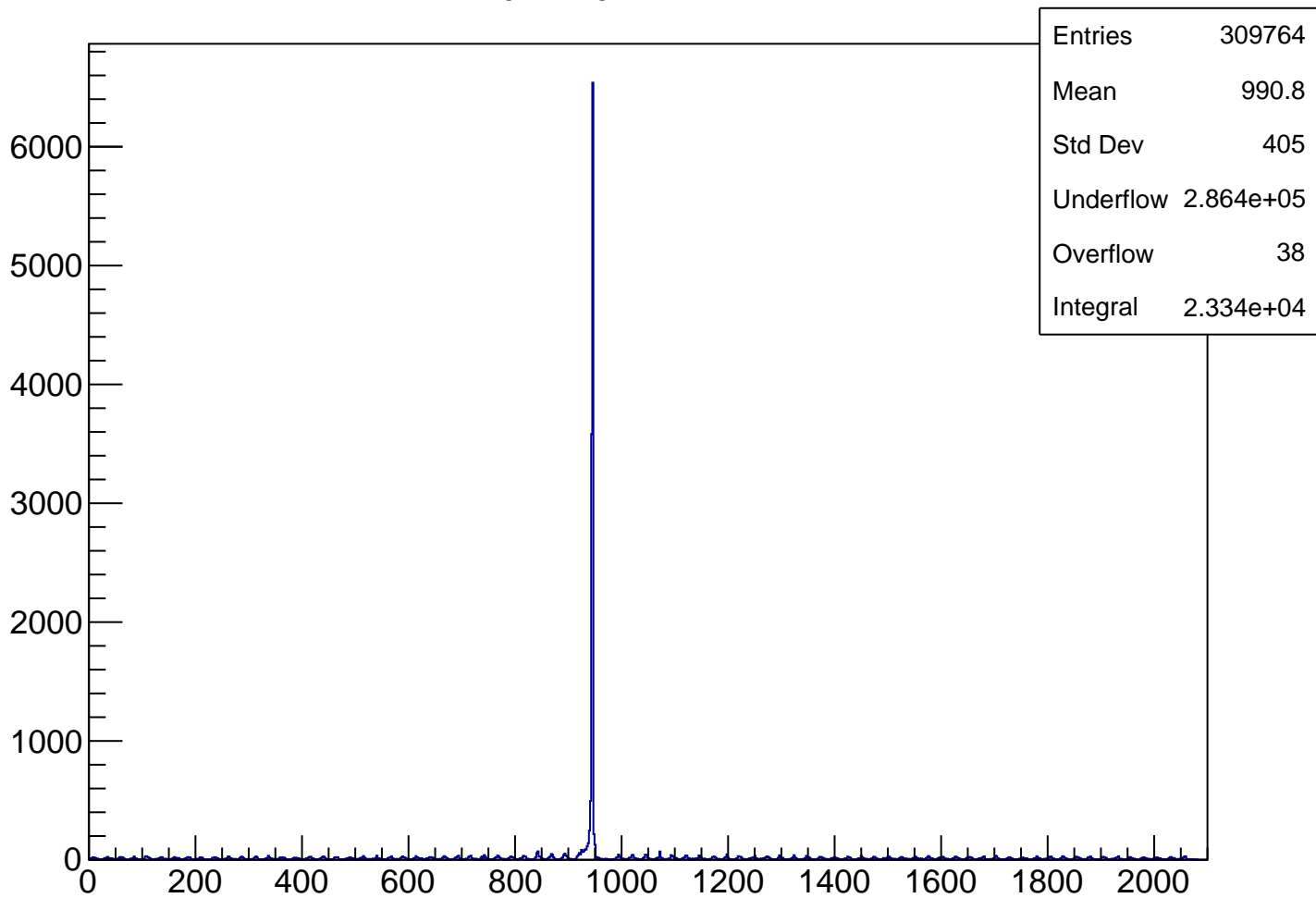
# TrigFlag Bh26K



# TrigFlag Bh27K

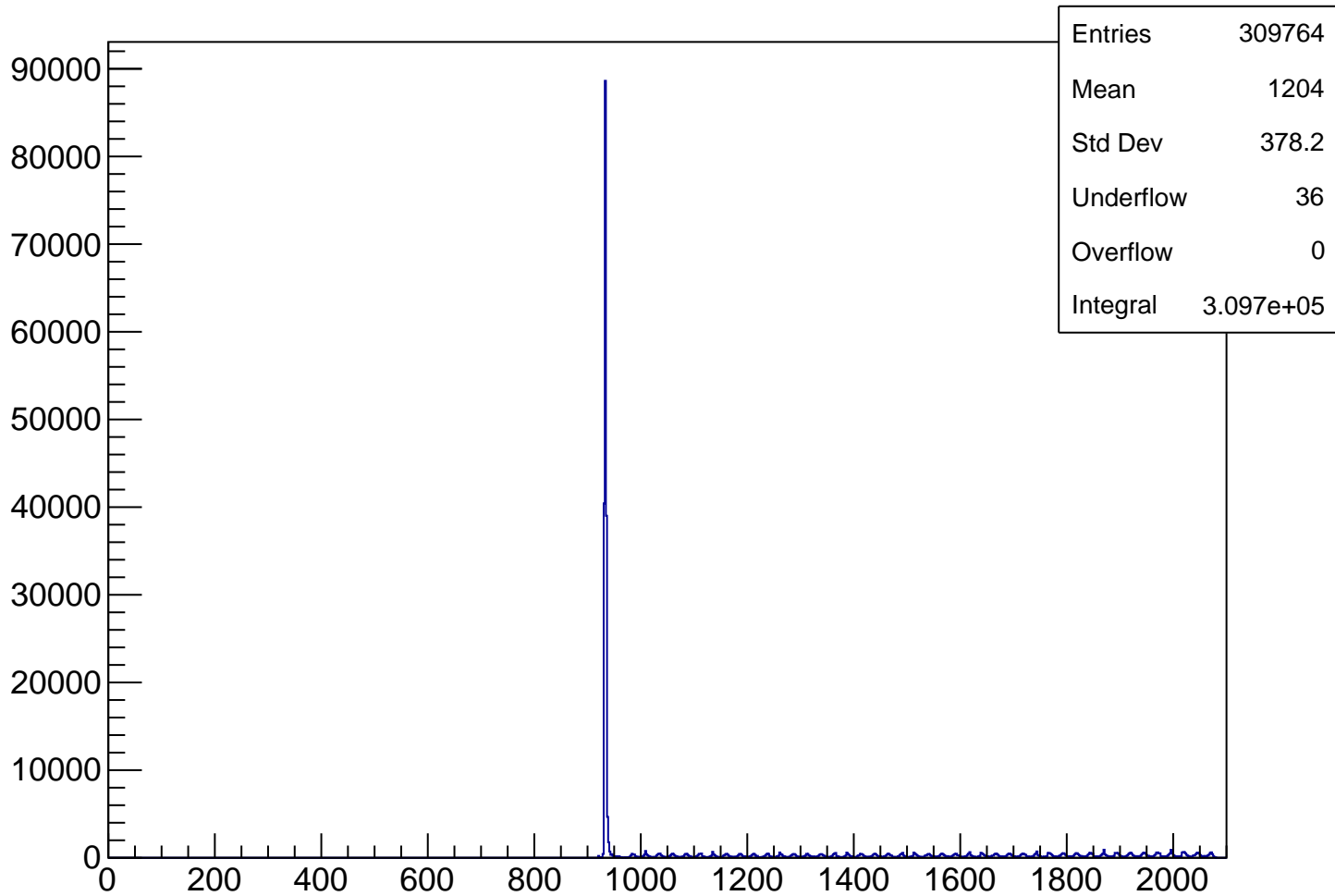


# TrigFlag Bh28K

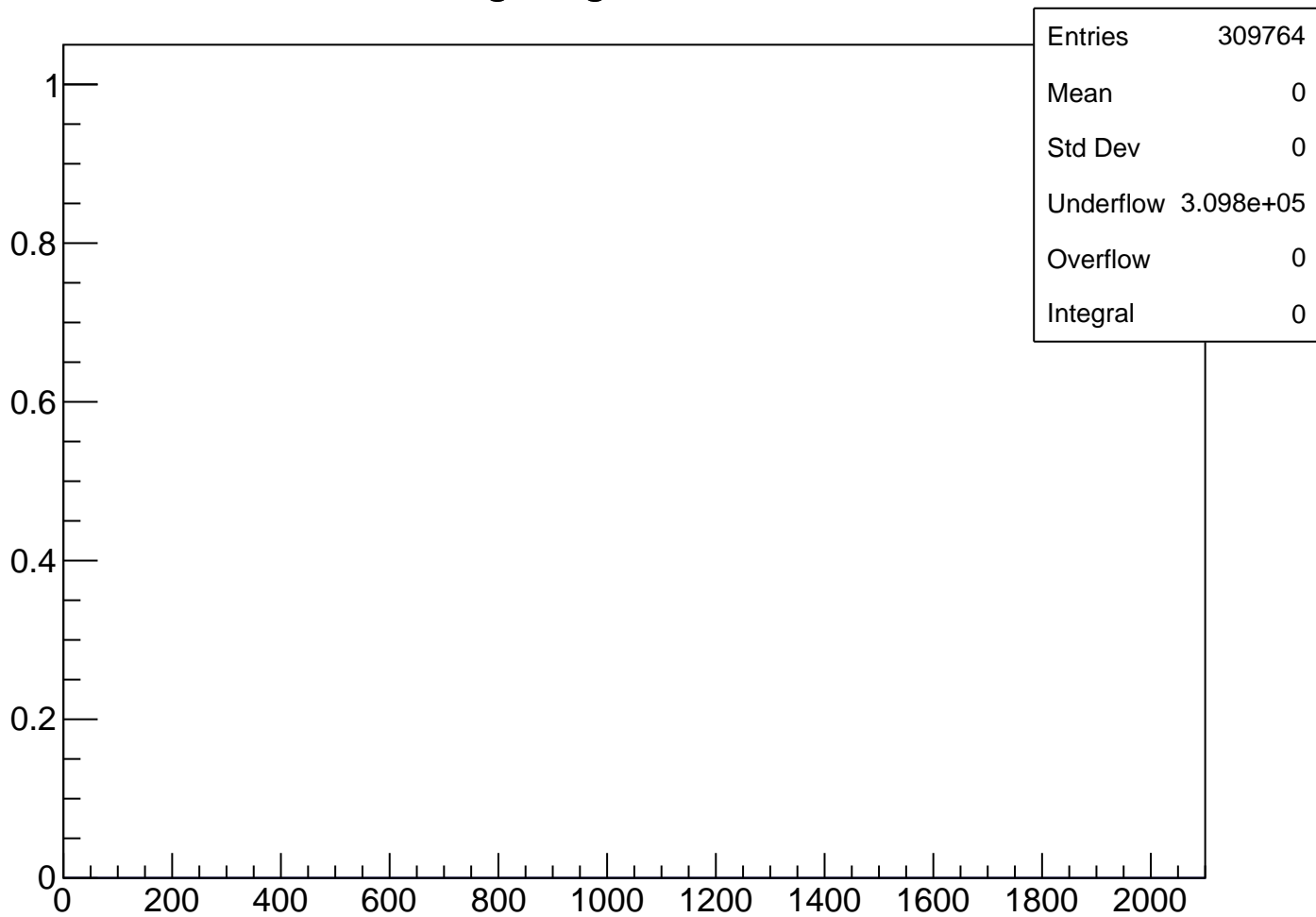




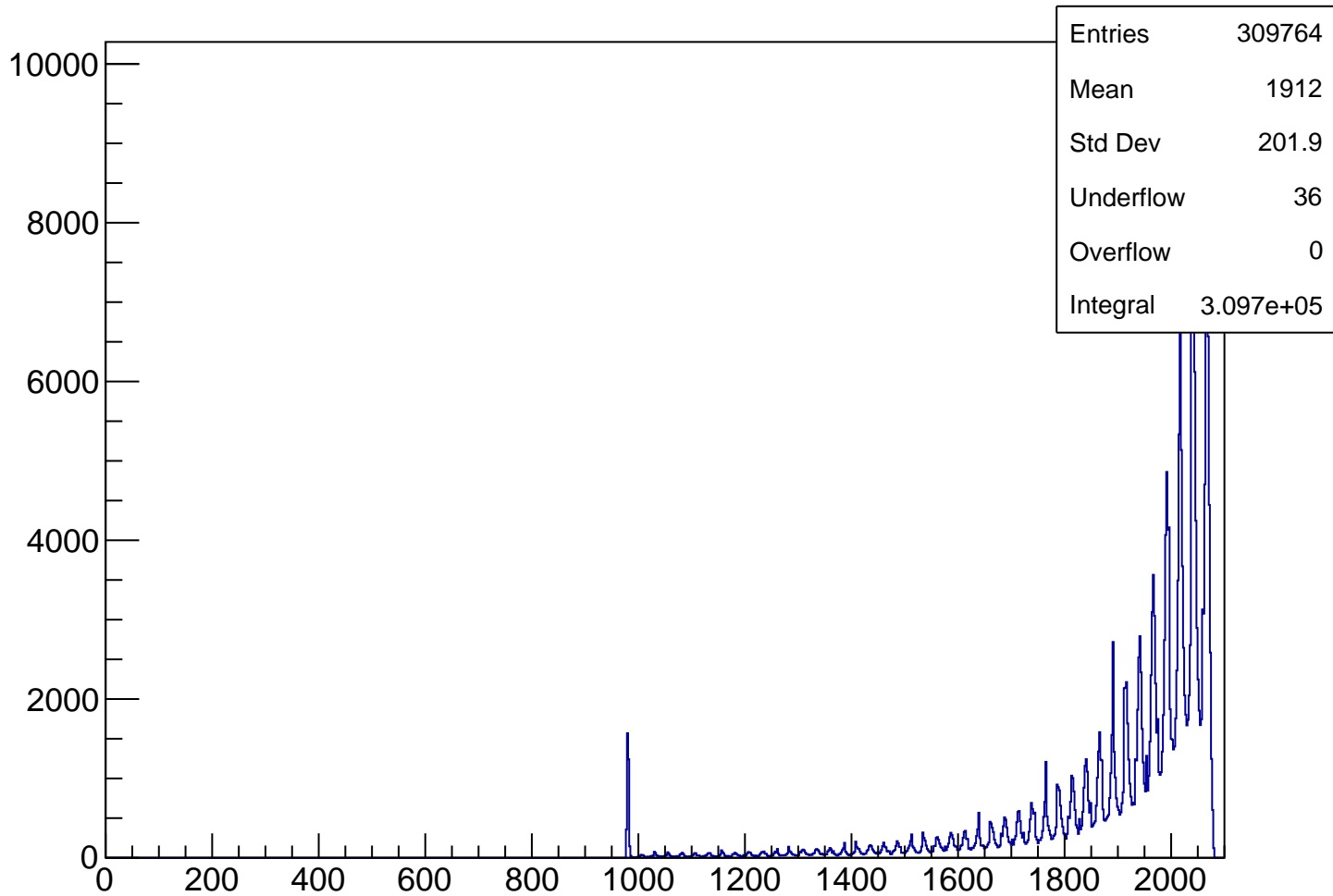
# TrigFlag Bh2K



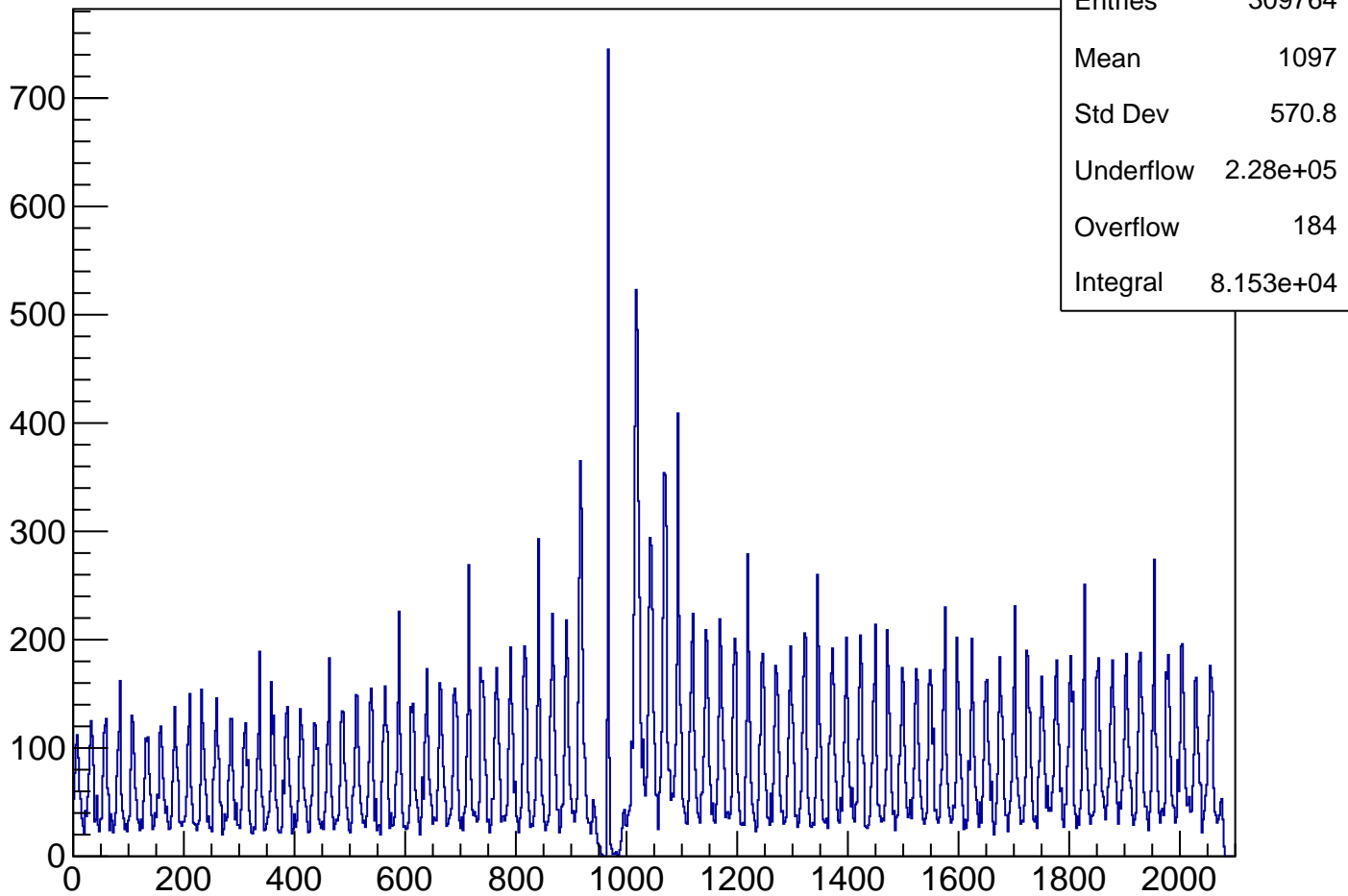
# TrigFlag ElseOr



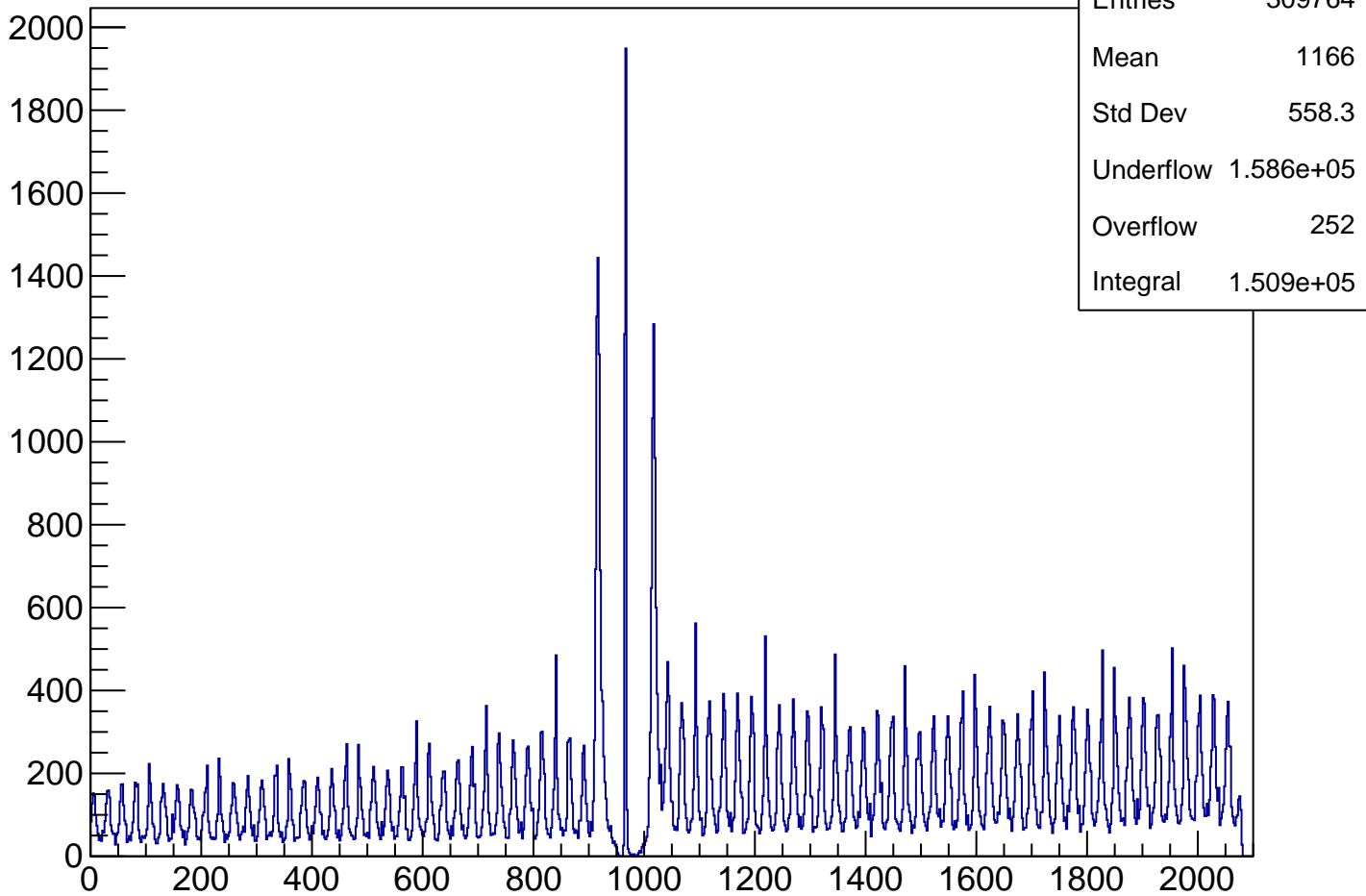
# TrigFlag Beam



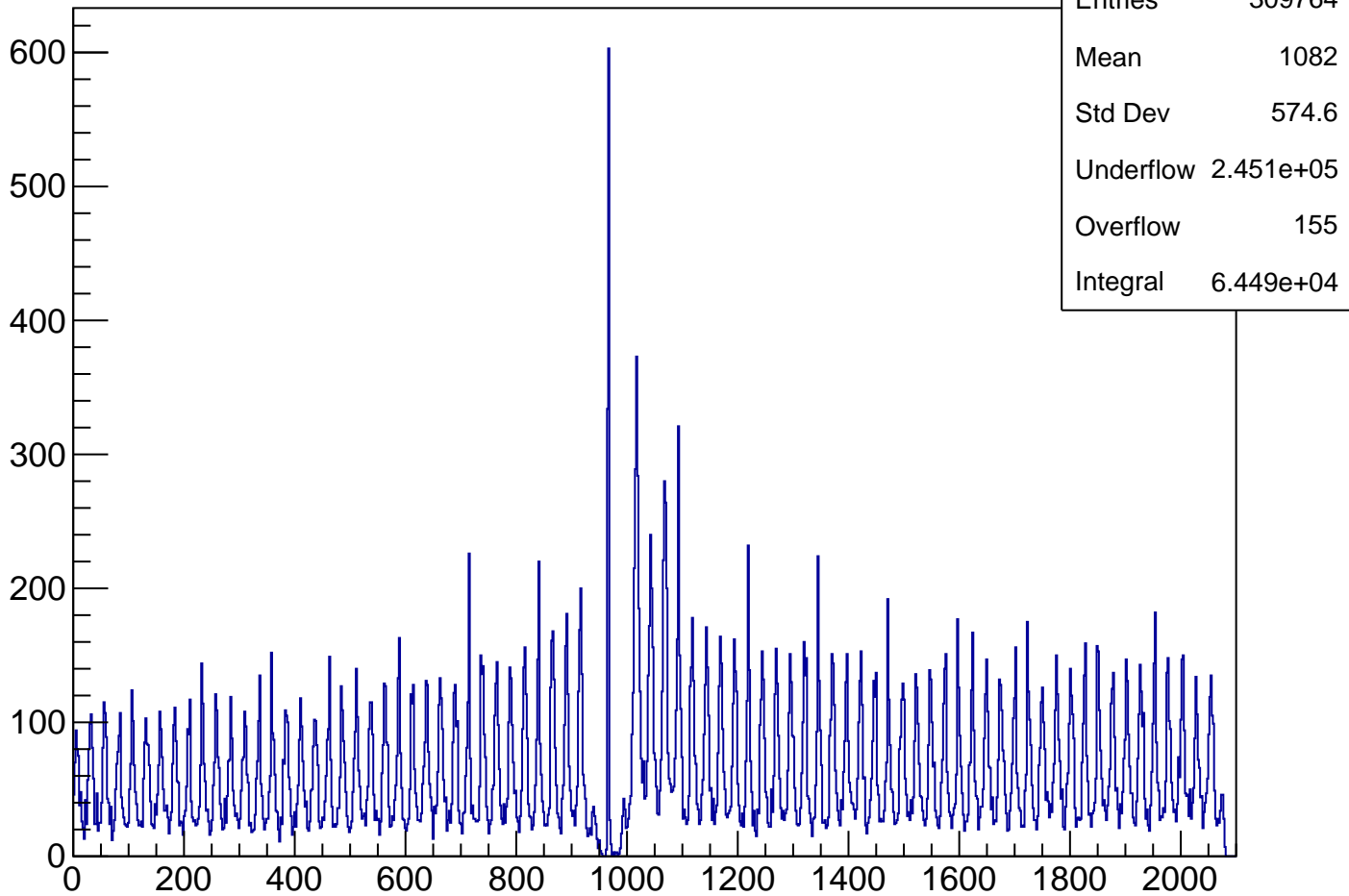
# TrigFlag BeamTof



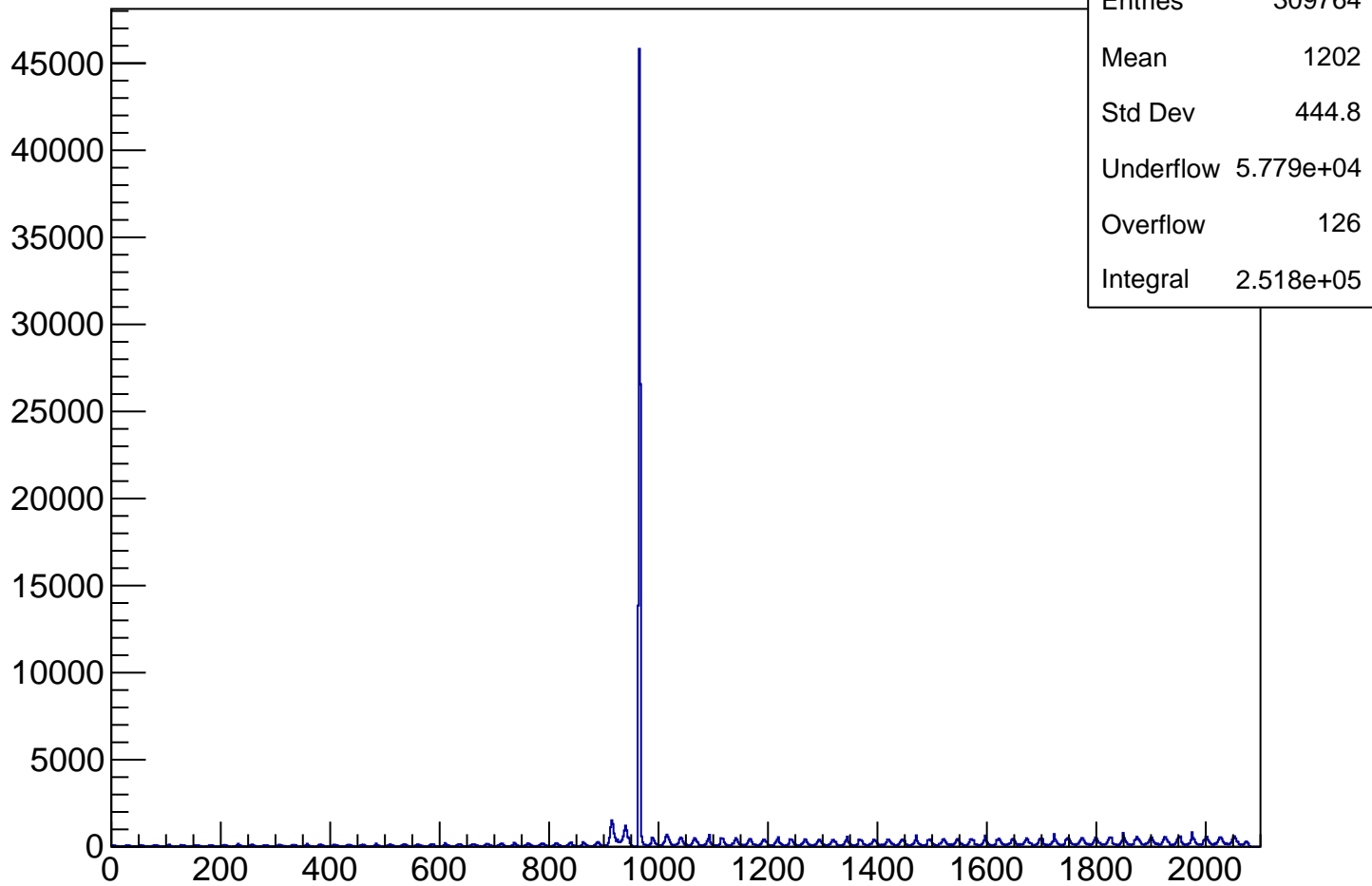
# TrigFlag BeamPi



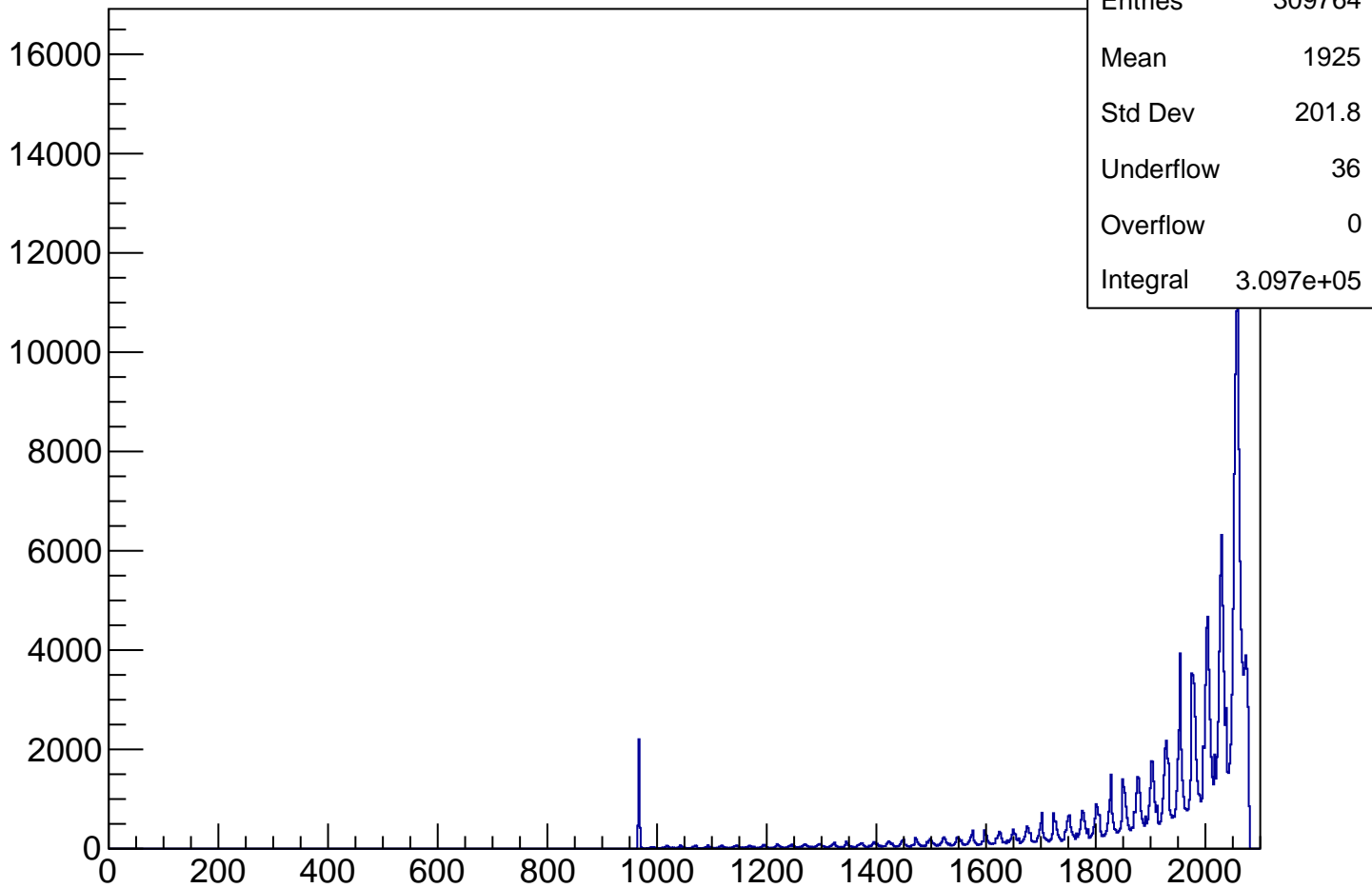
# TrigFlag BeamP



# TrigFlag Coin1

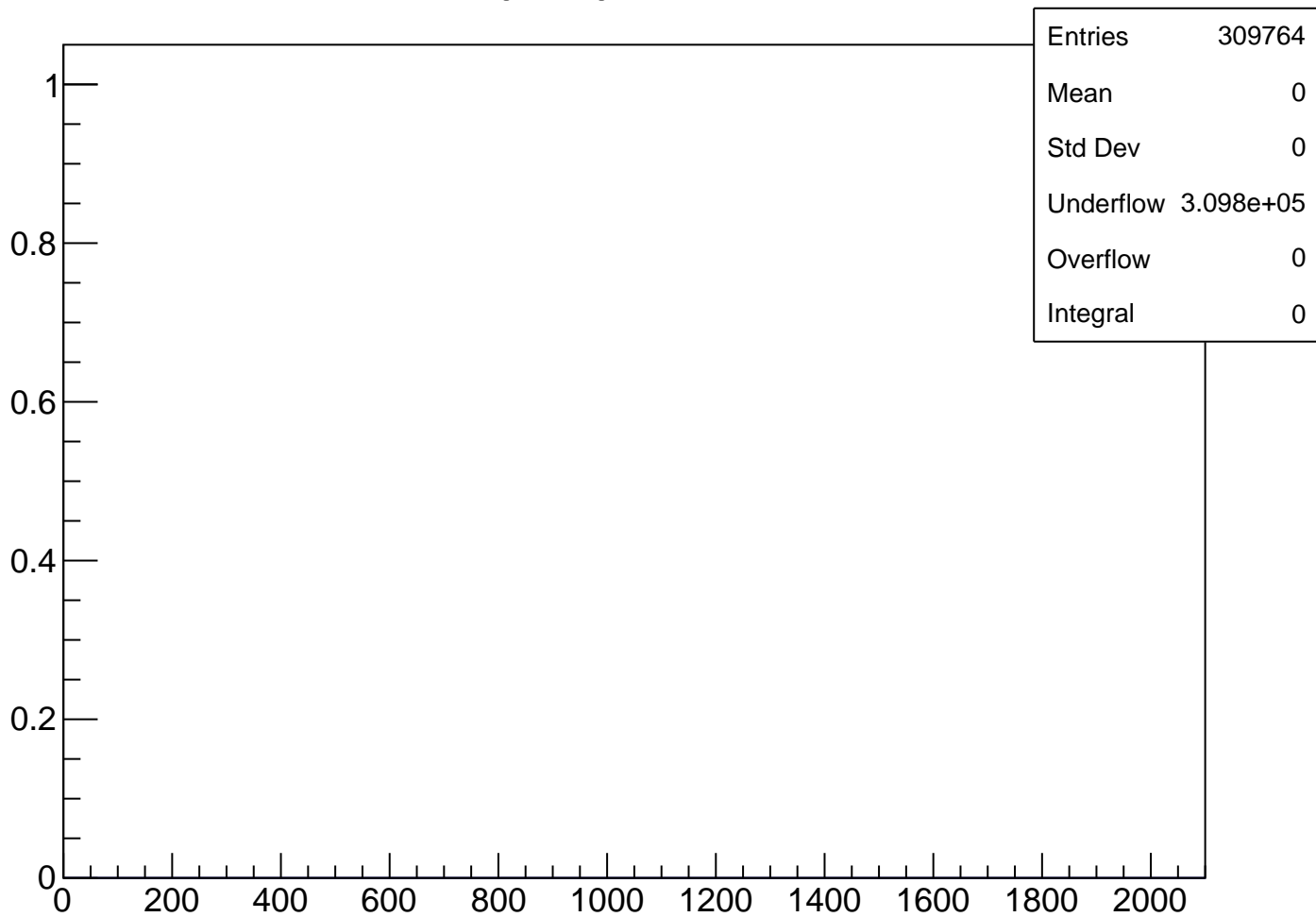


# TrigFlag Coin2

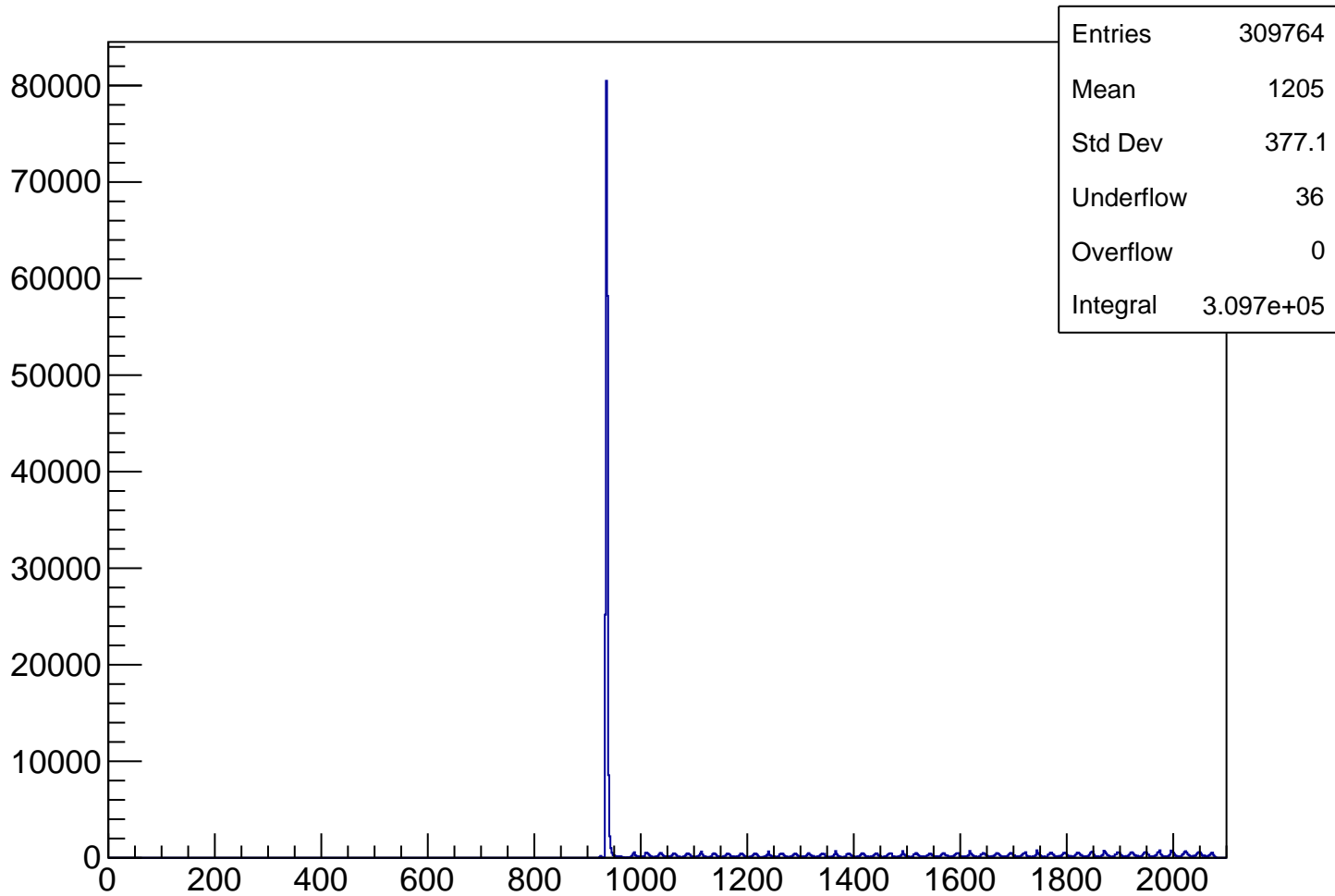




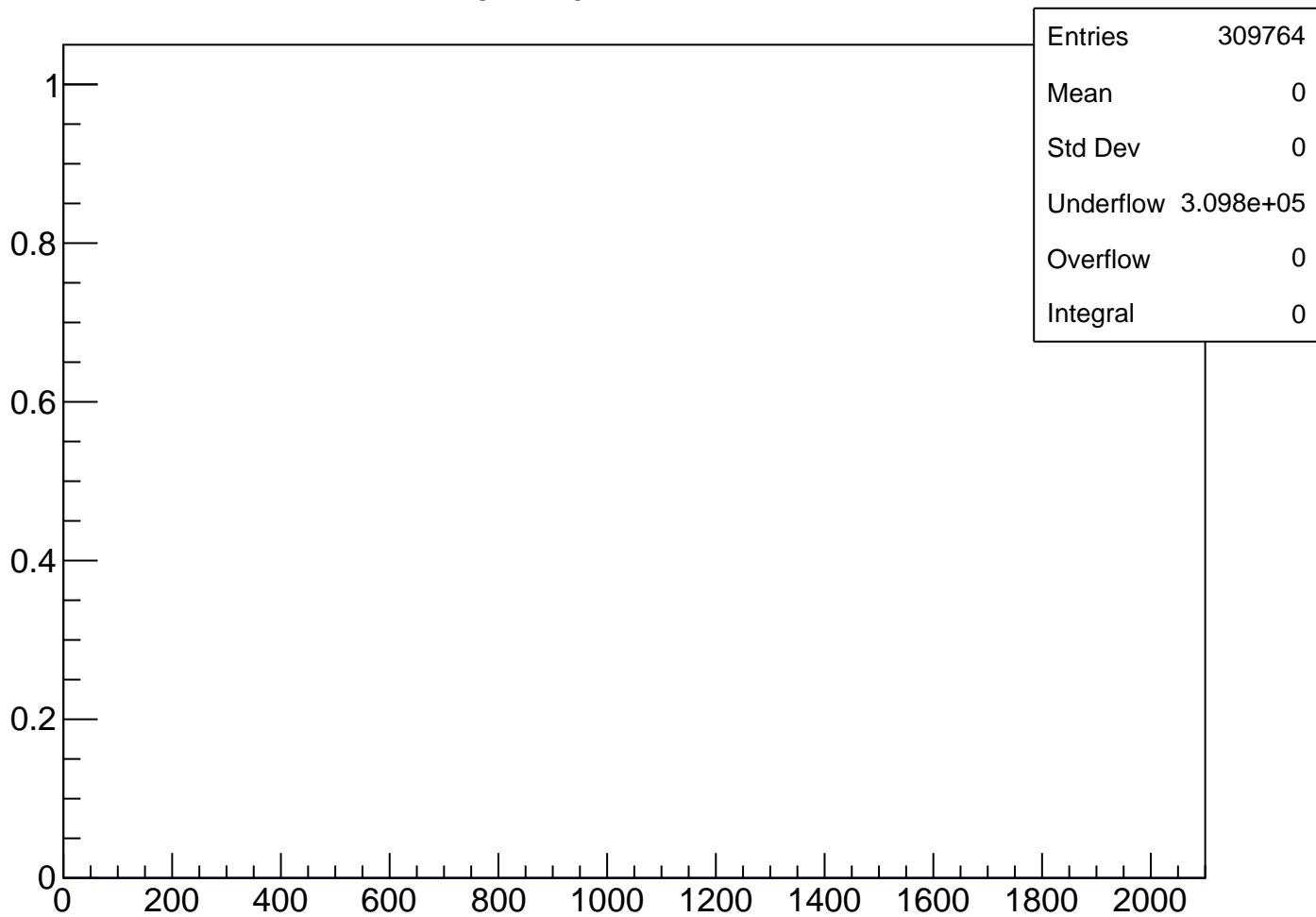
# TrigFlag E03



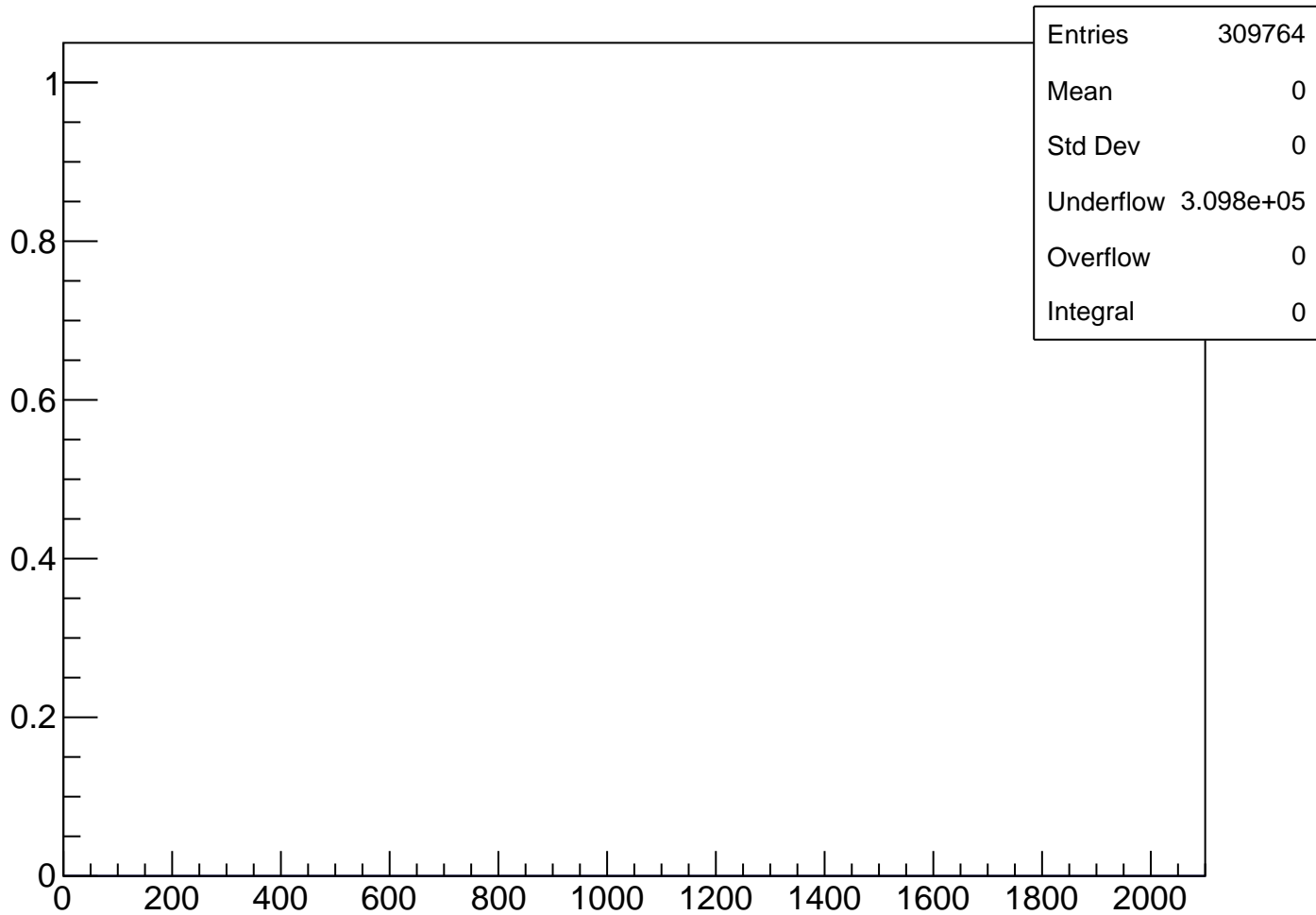
# TrigFlag Bh2KPs



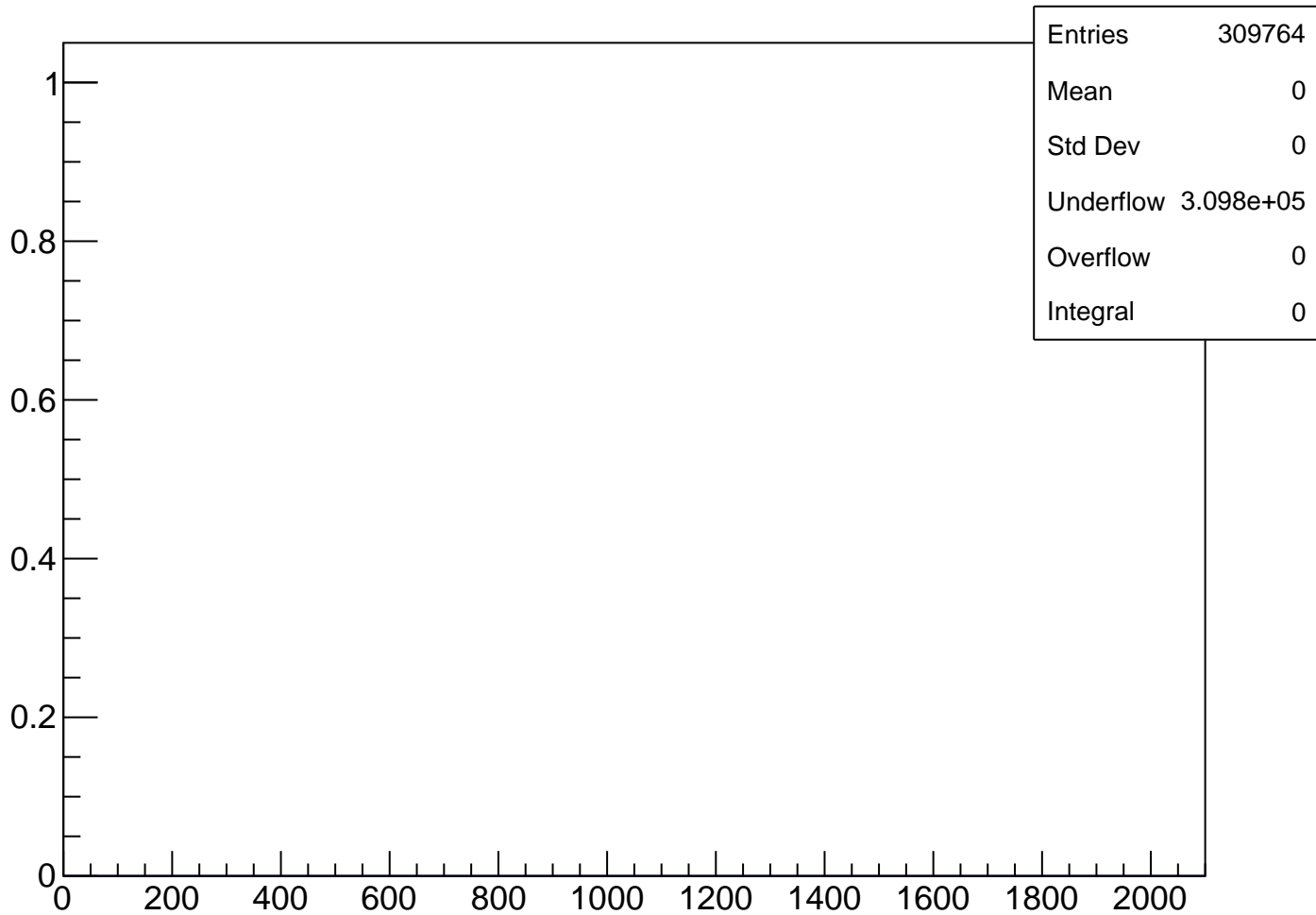
# TrigFlag BeamPs



# TrigFlag BeamTofPs



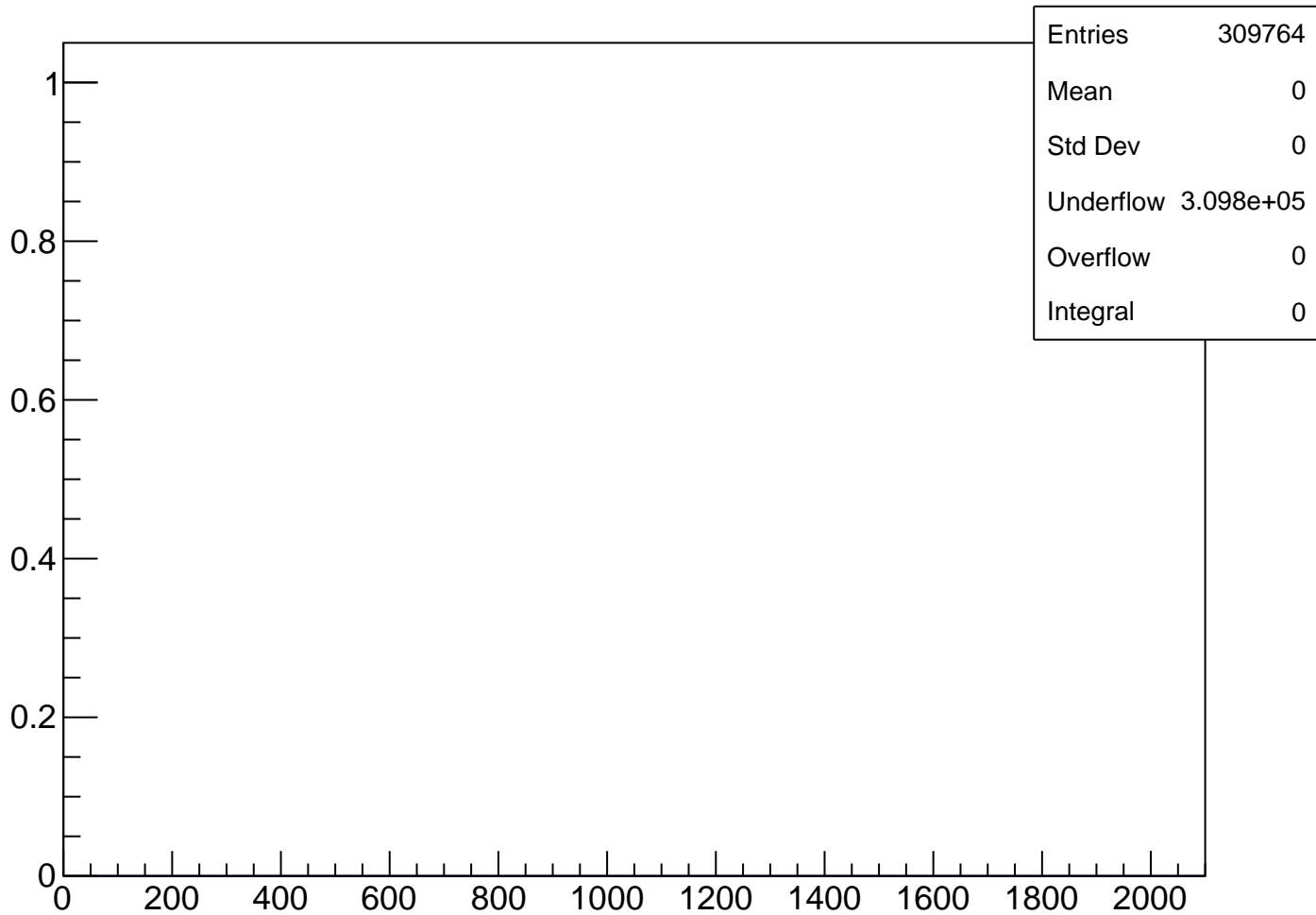
# TrigFlag BeamPiPs



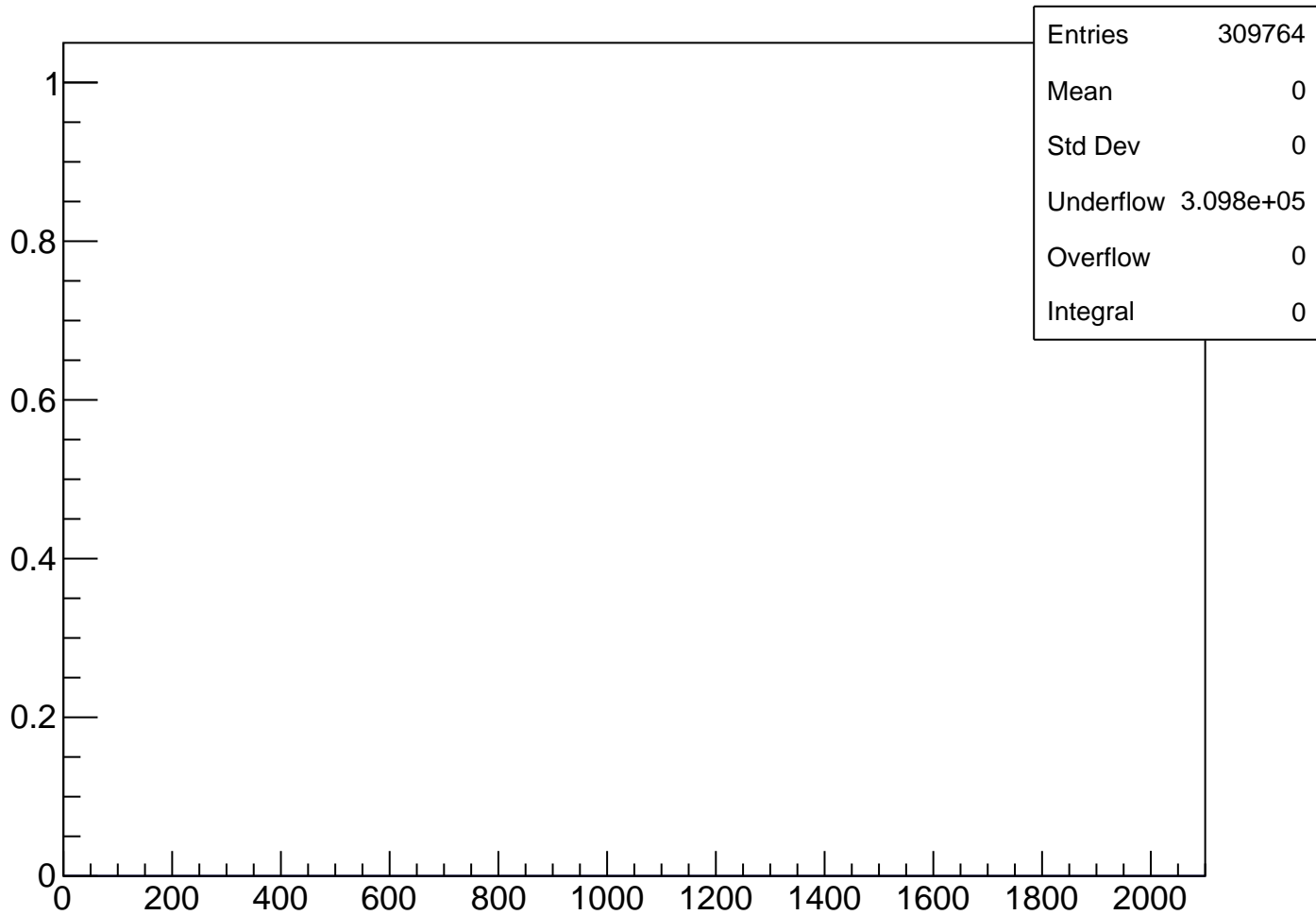
# TrigFlag BeamPPs



# TrigFlag Coin1Ps

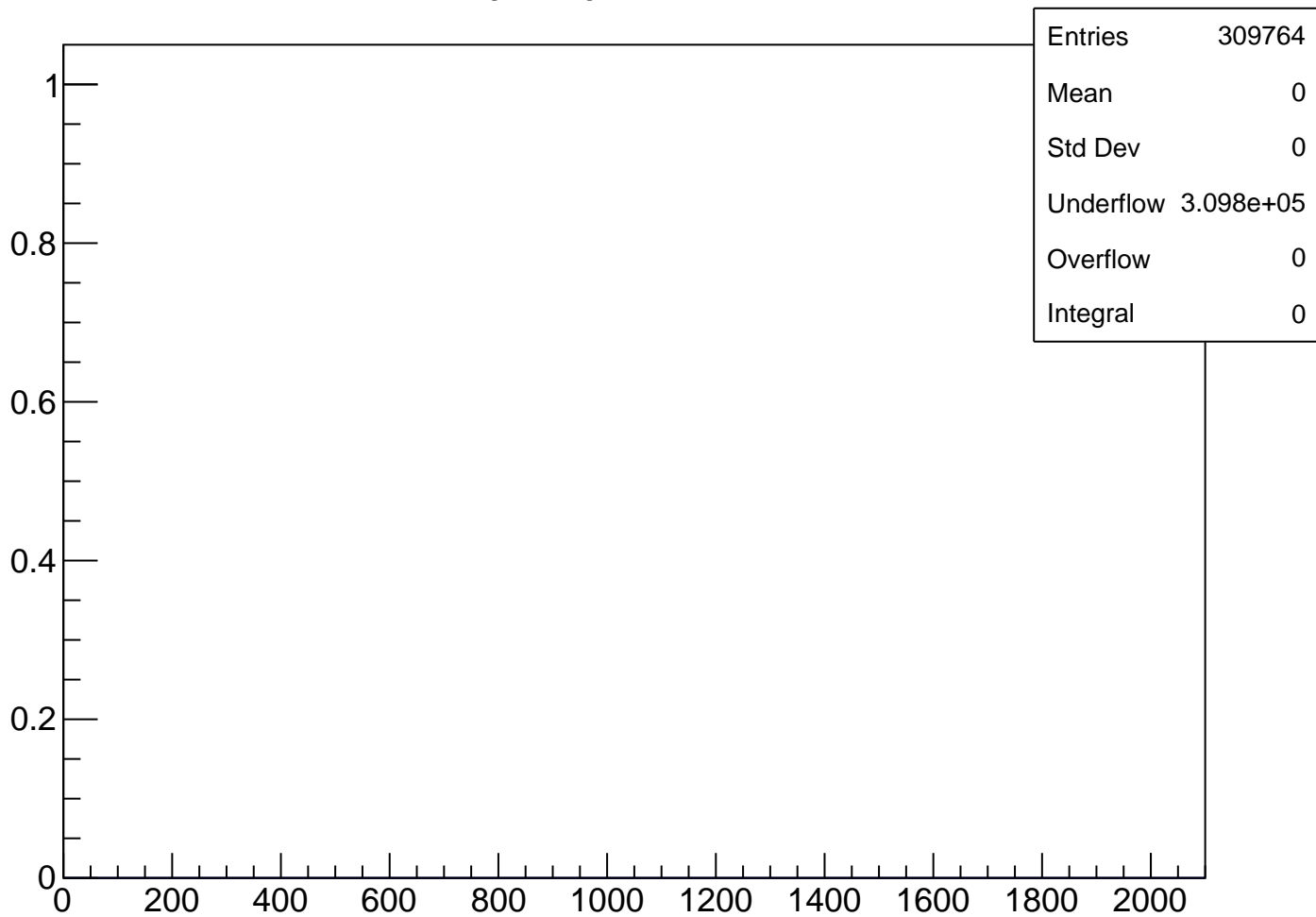


# TrigFlag Coin2Ps

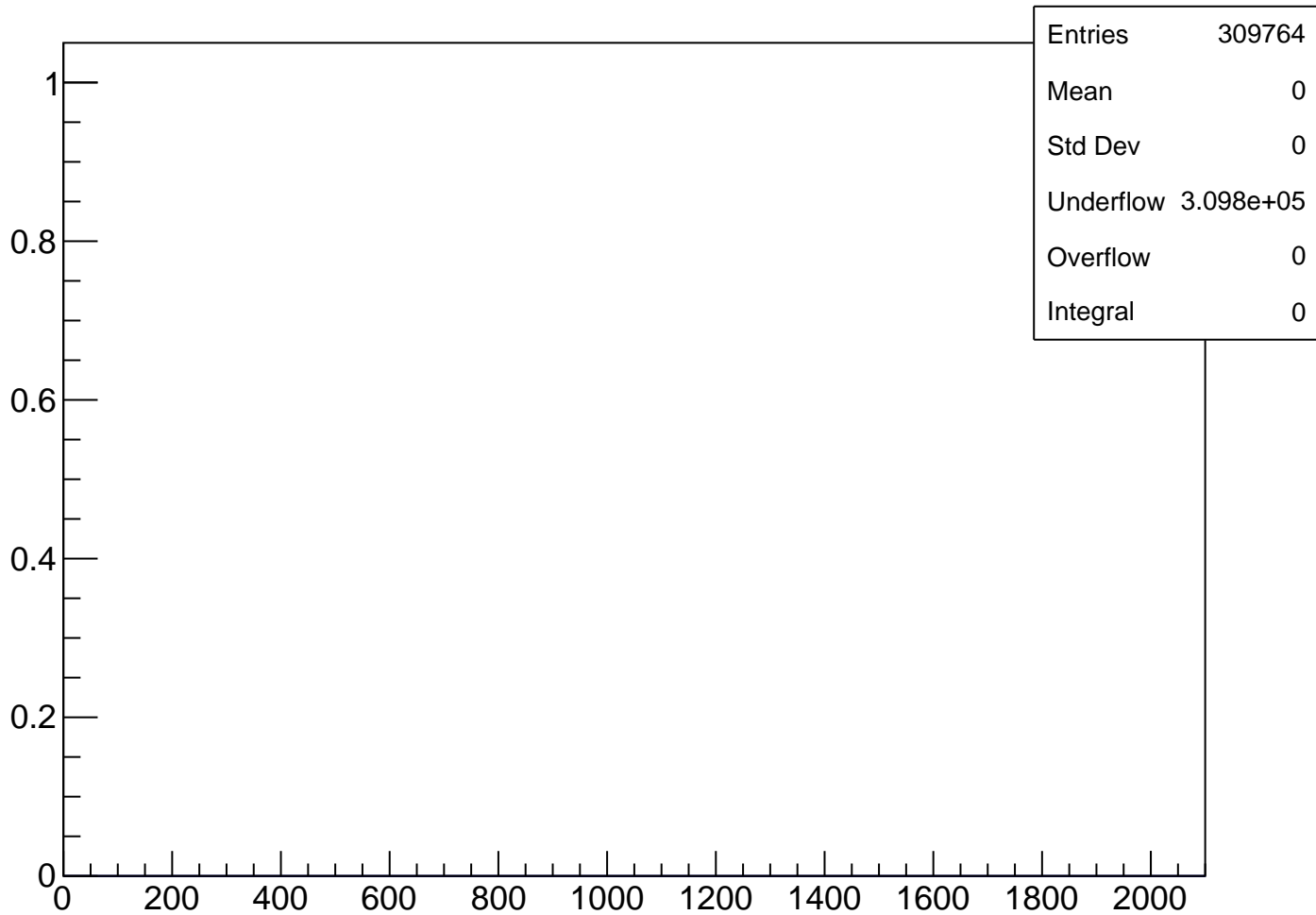




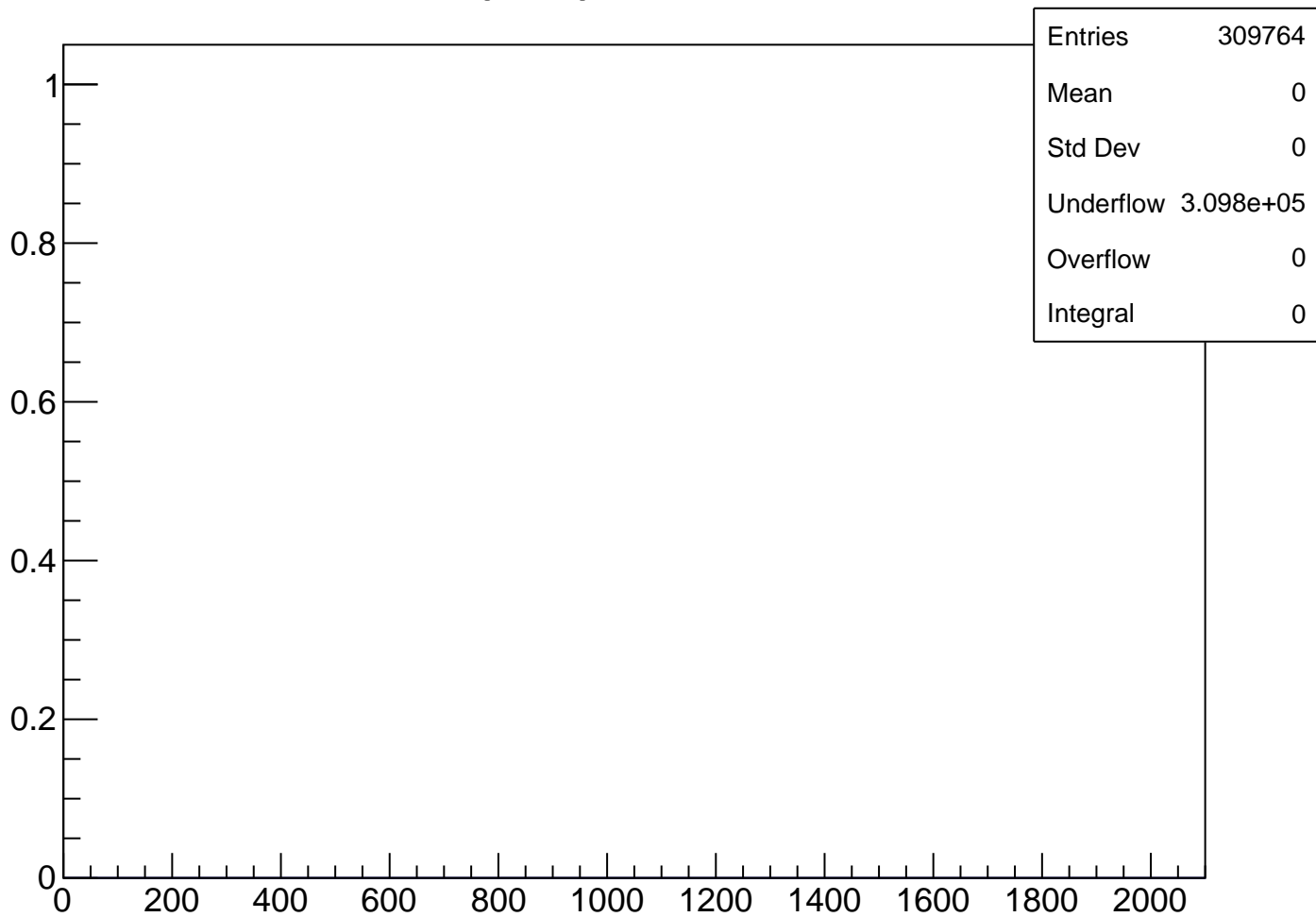
# TrigFlag E03Ps



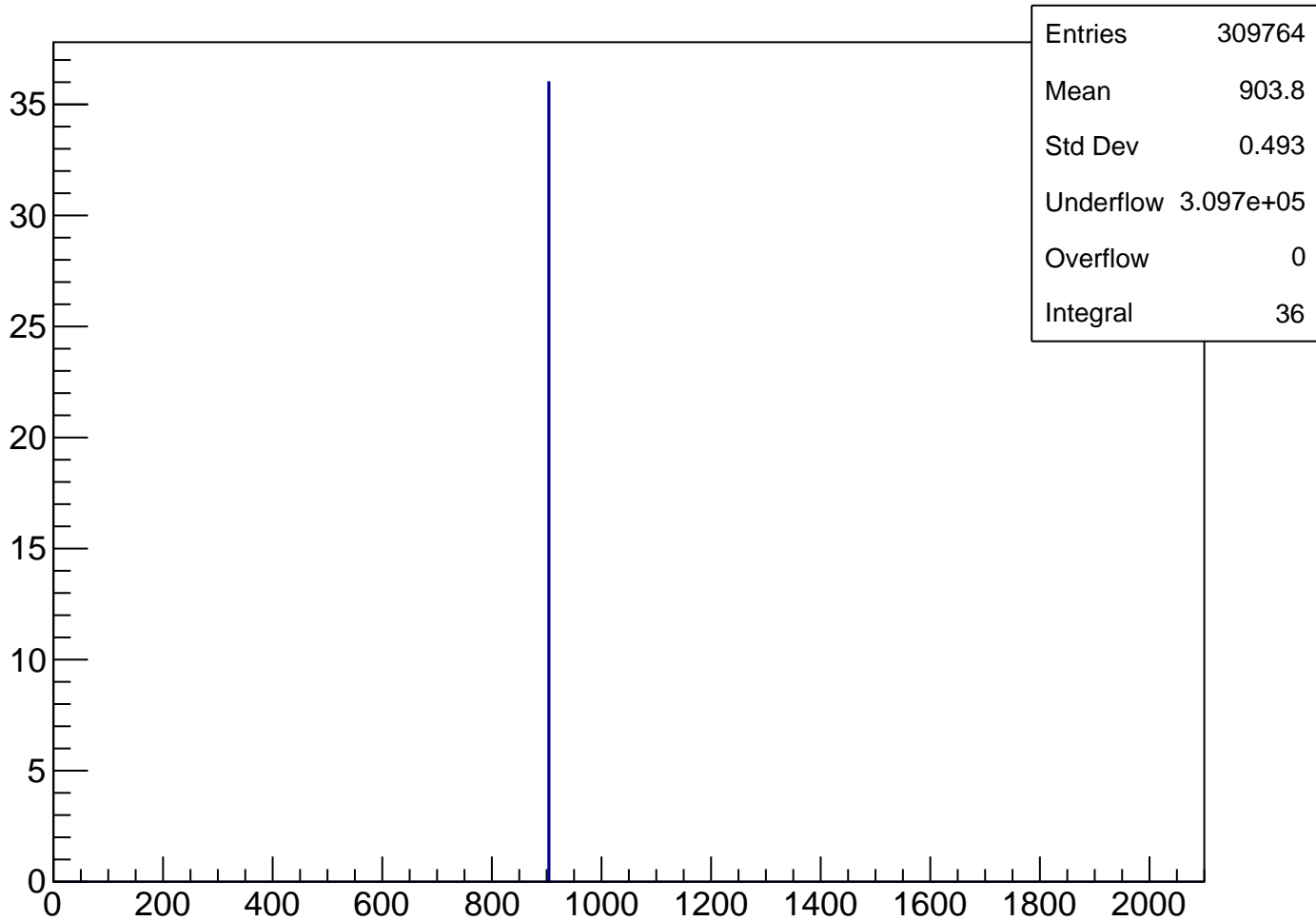
# TrigFlag Clock



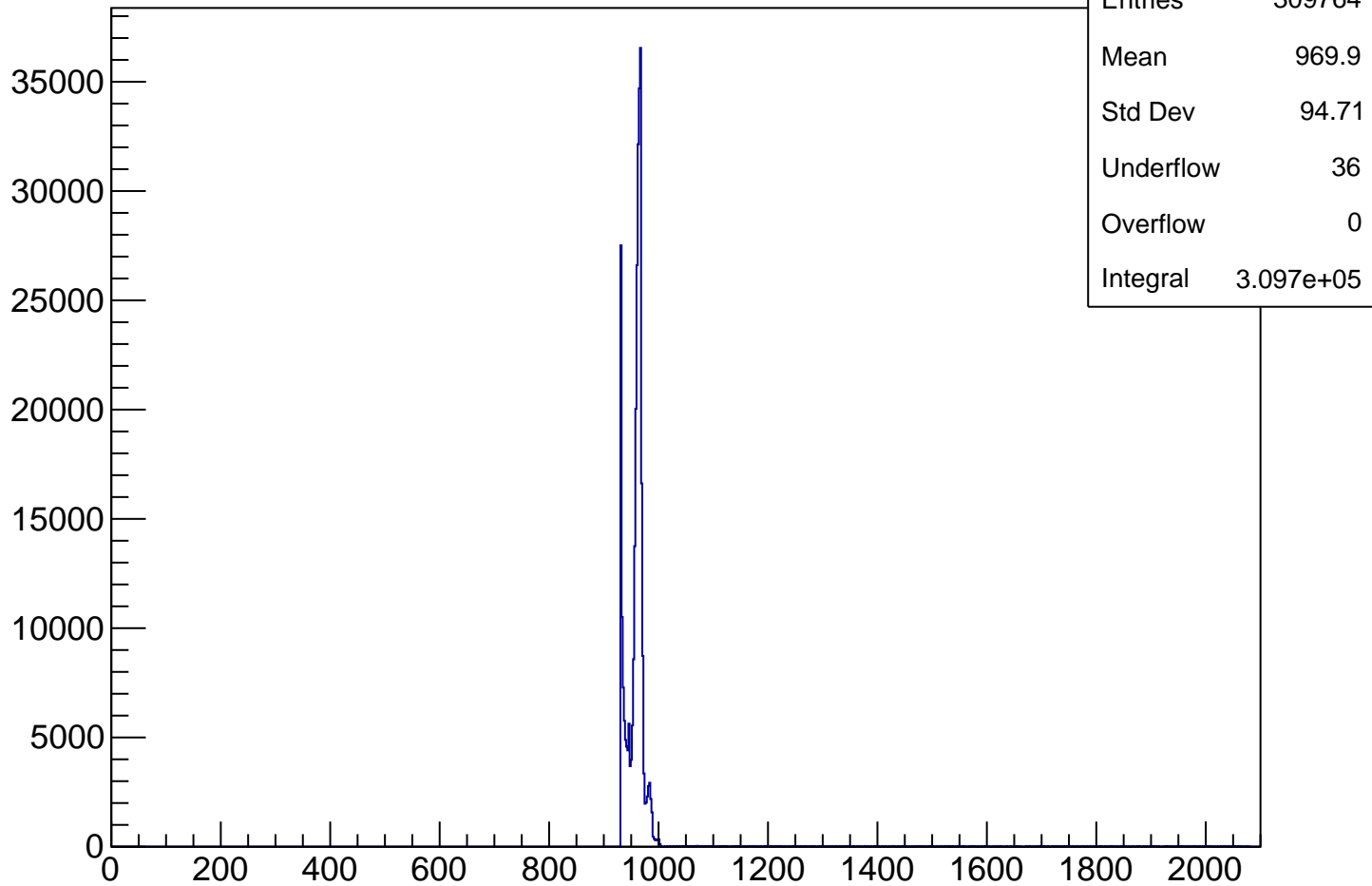
# TrigFlag Reserve2



# TrigFlag SpillEnd



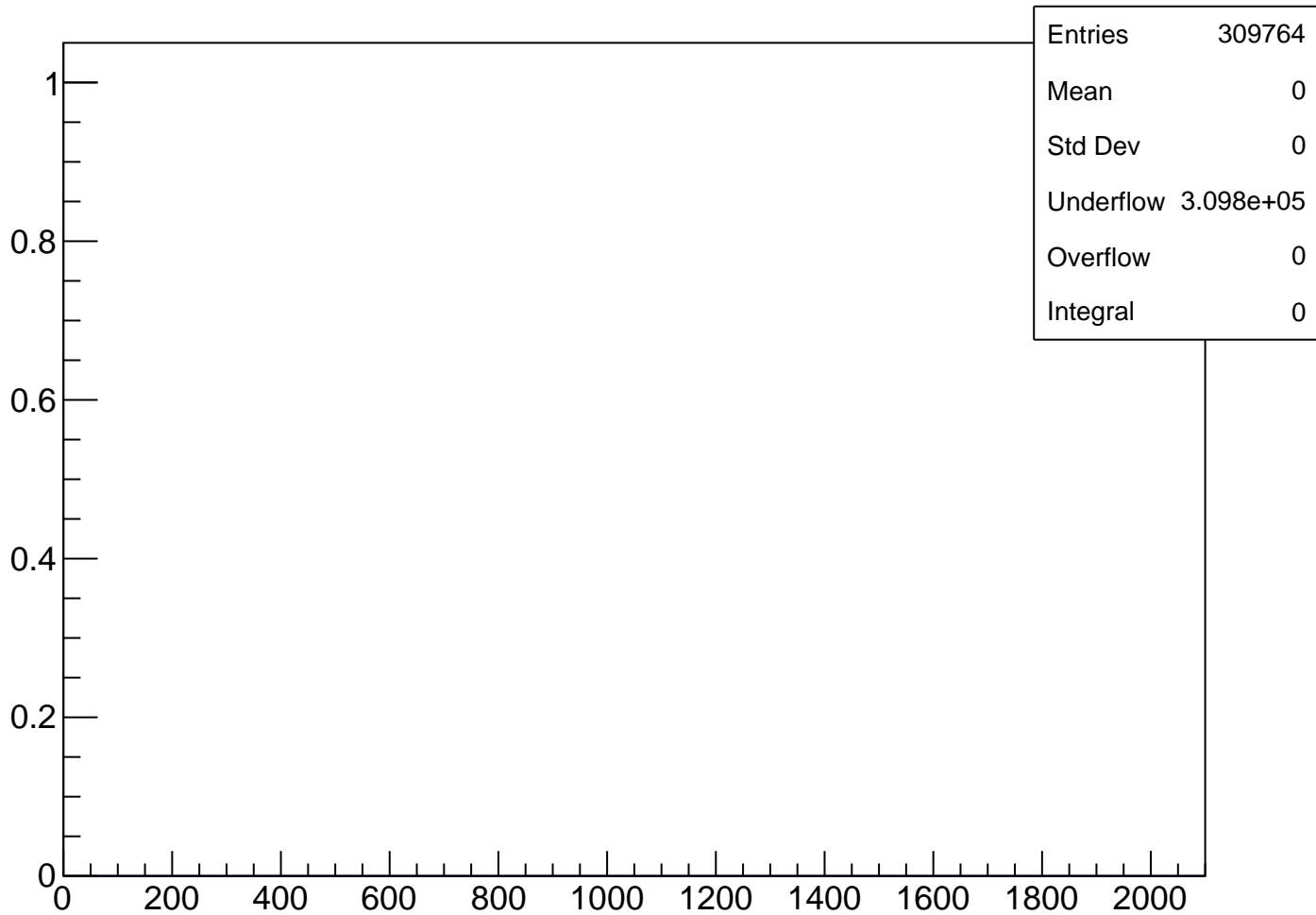
# TrigFlag Matrix



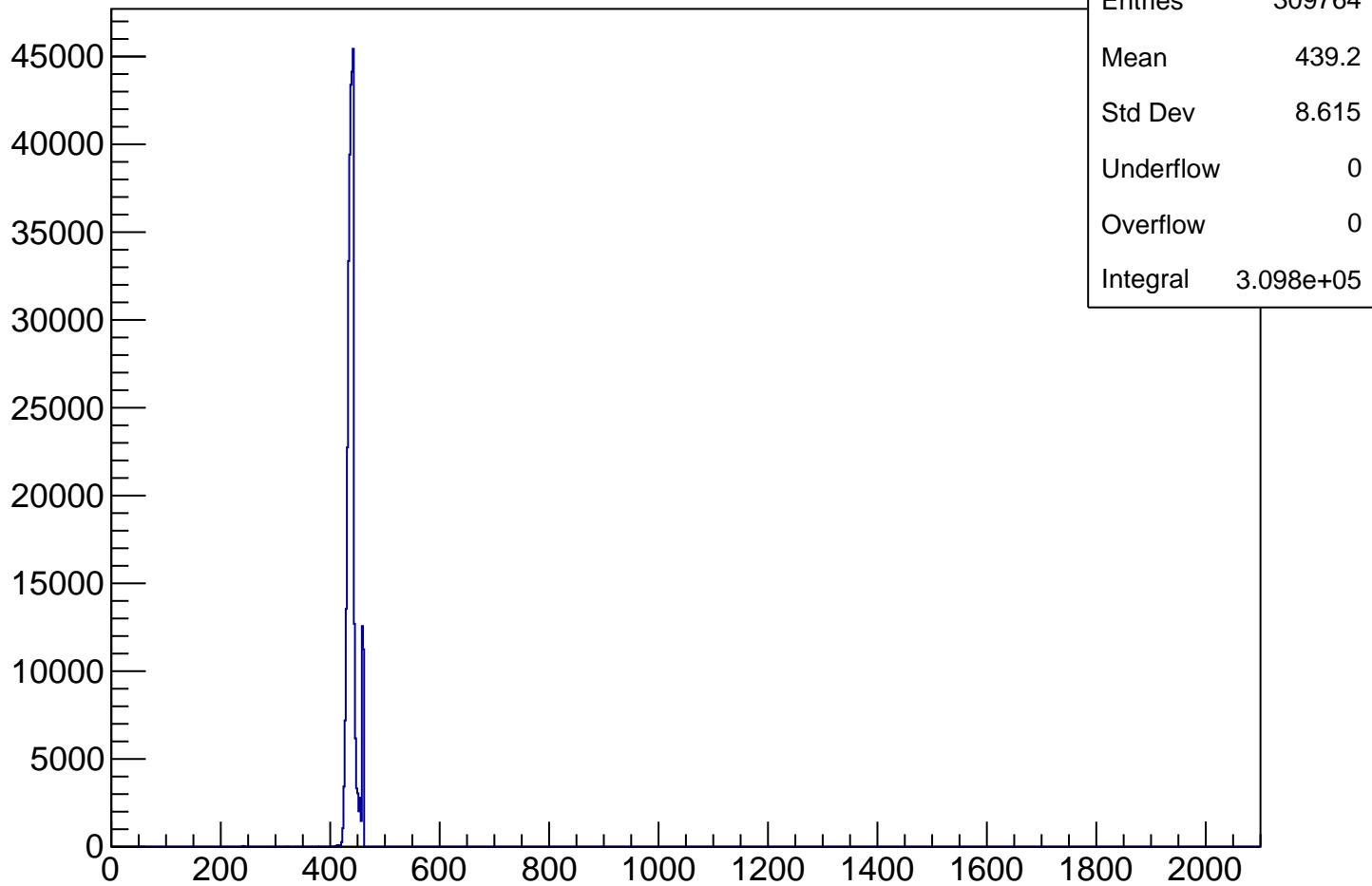
# TrigFlag MstAccept



# TrigFlag MstClear

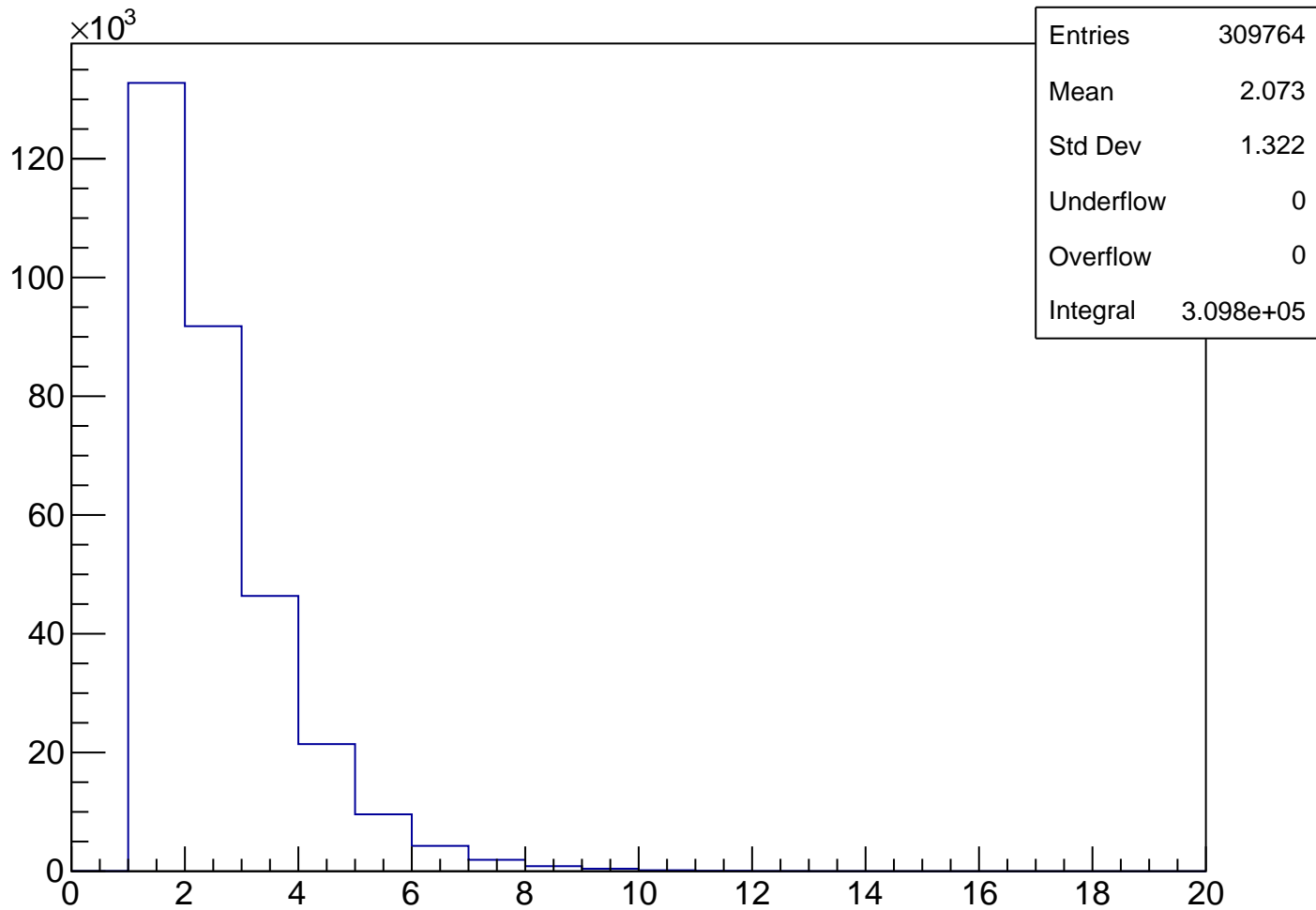


# TrigFlag TofTiming

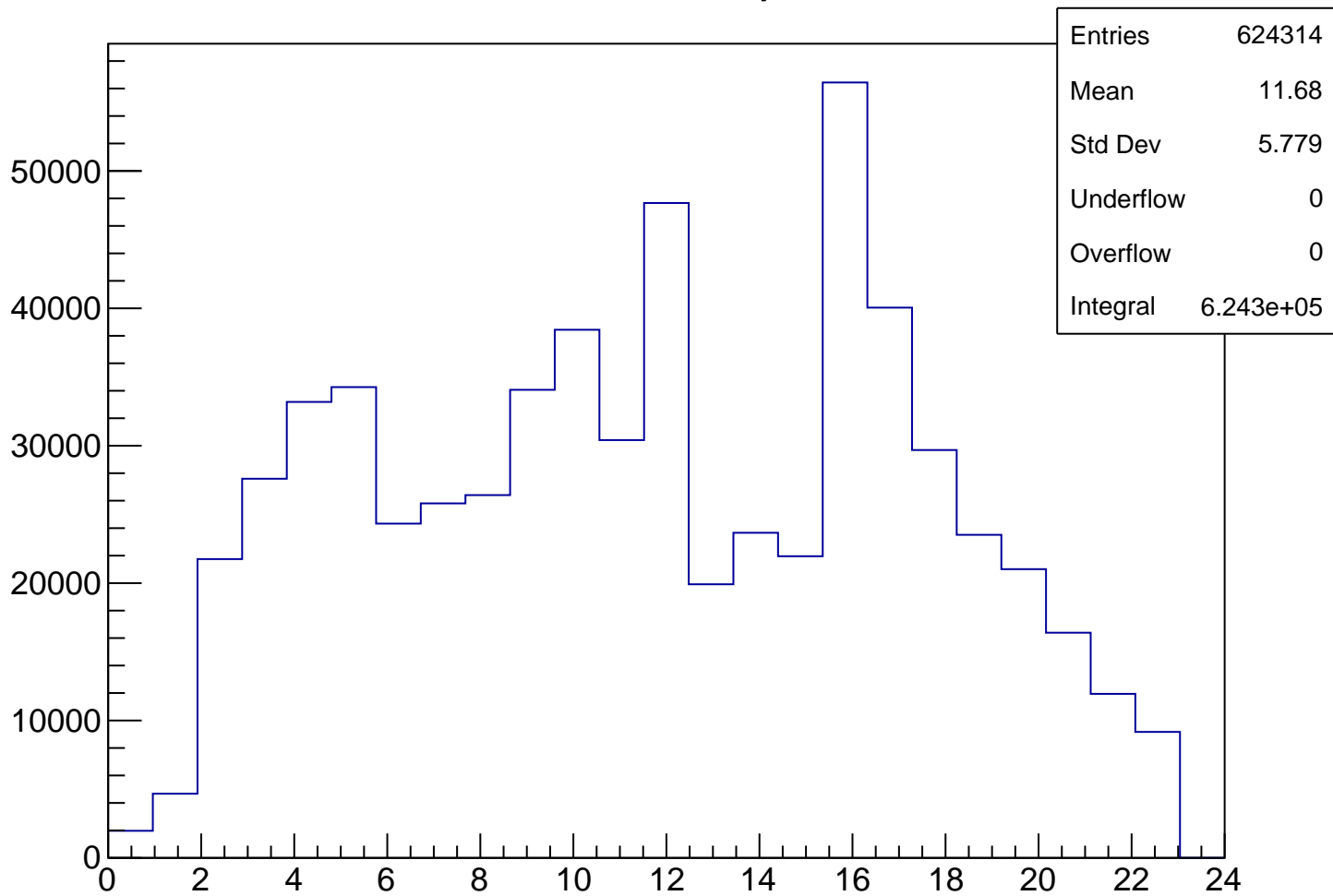




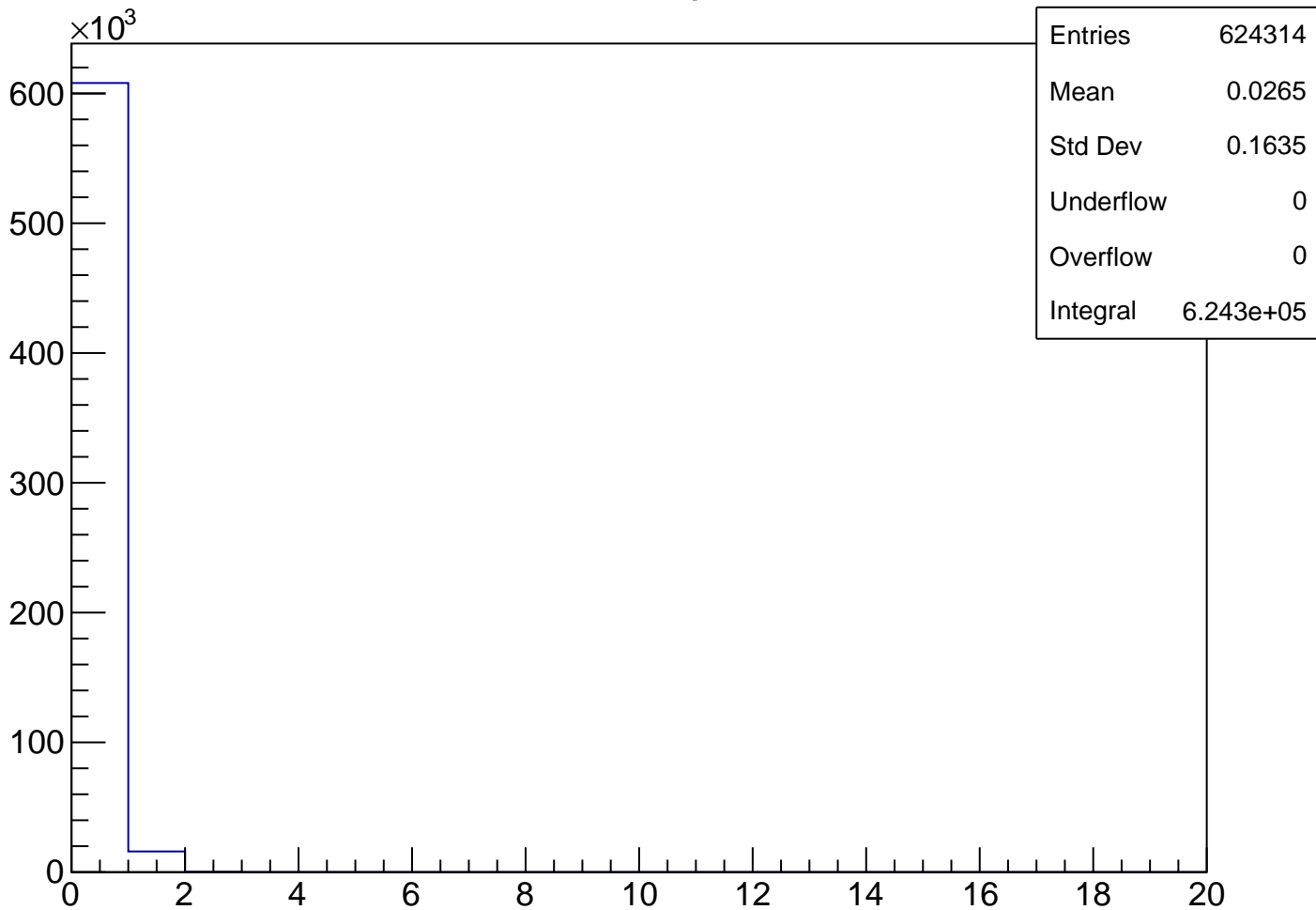
# TofMtOr Nhits



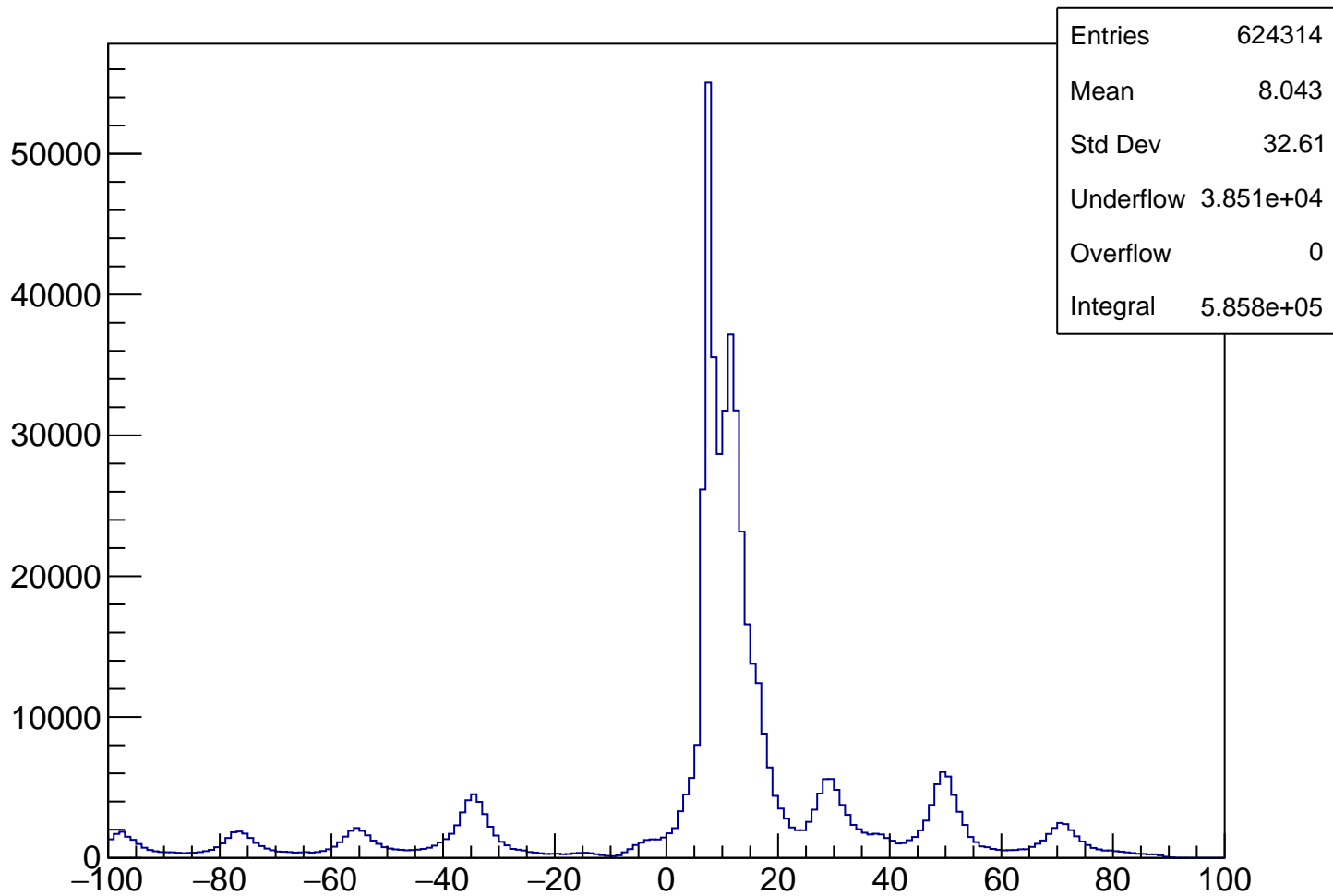
# TofMtOr Hitpat



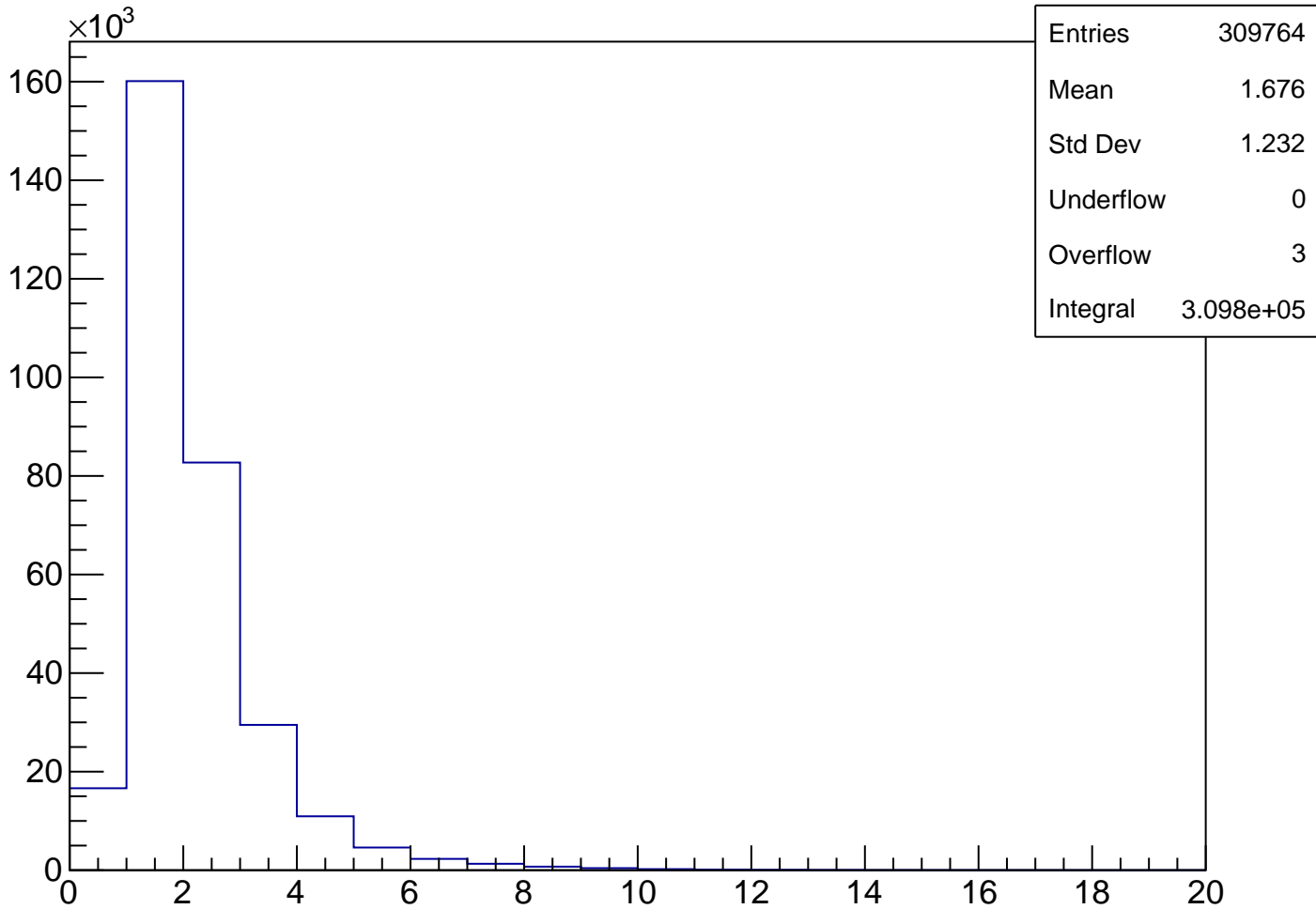
# TofMtOrDepthPat



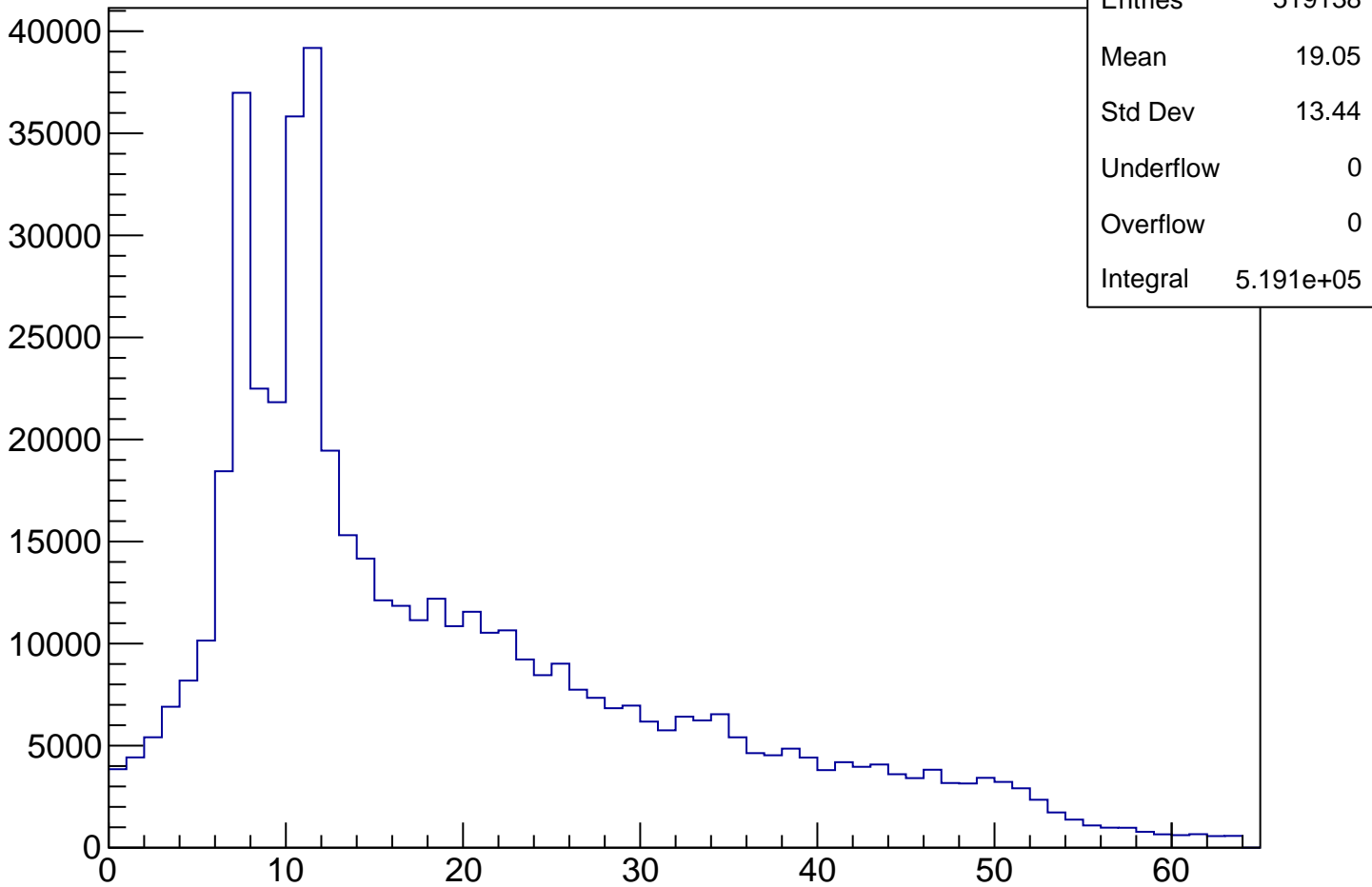
# TofMtOr



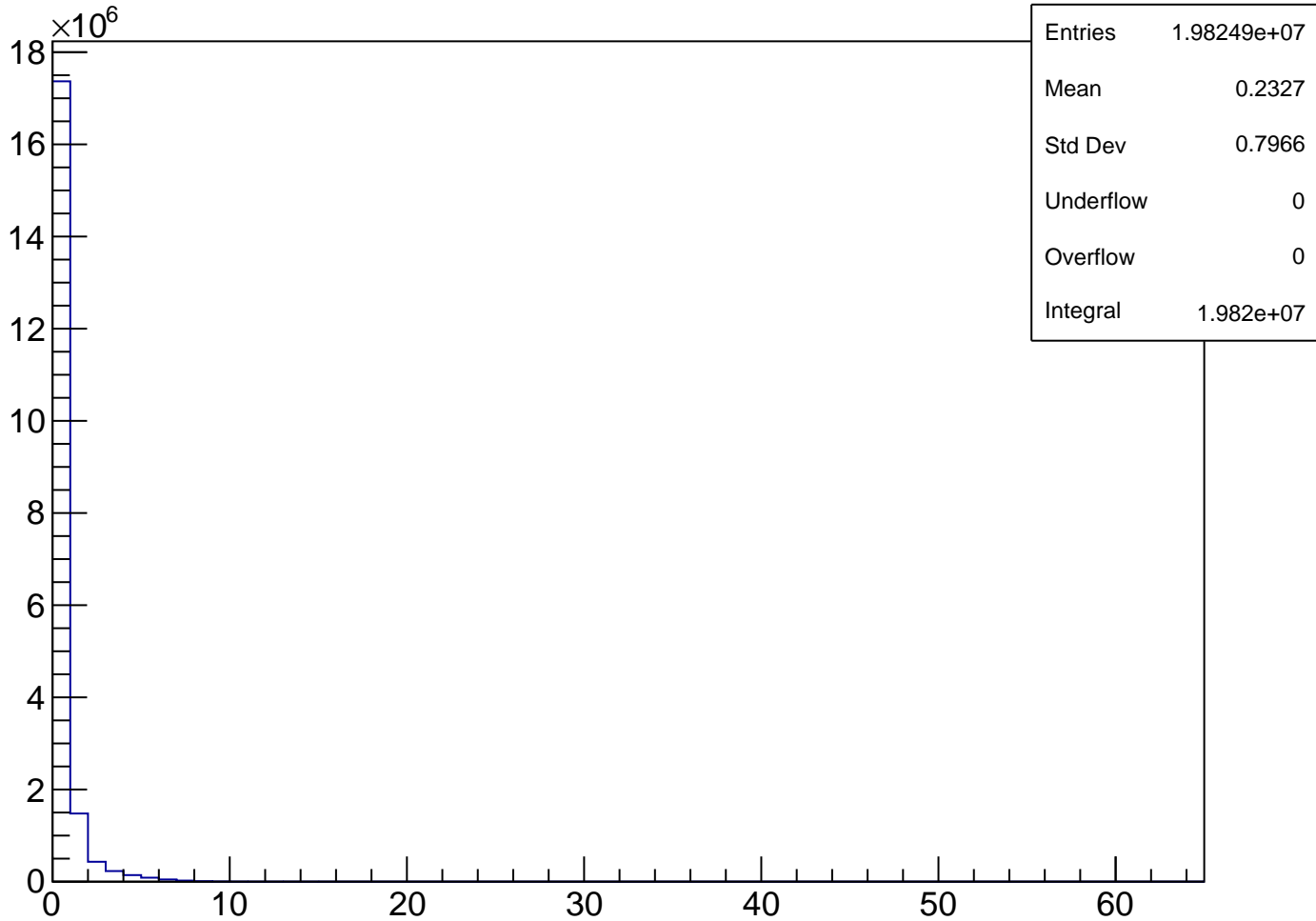
# Sch Nhits



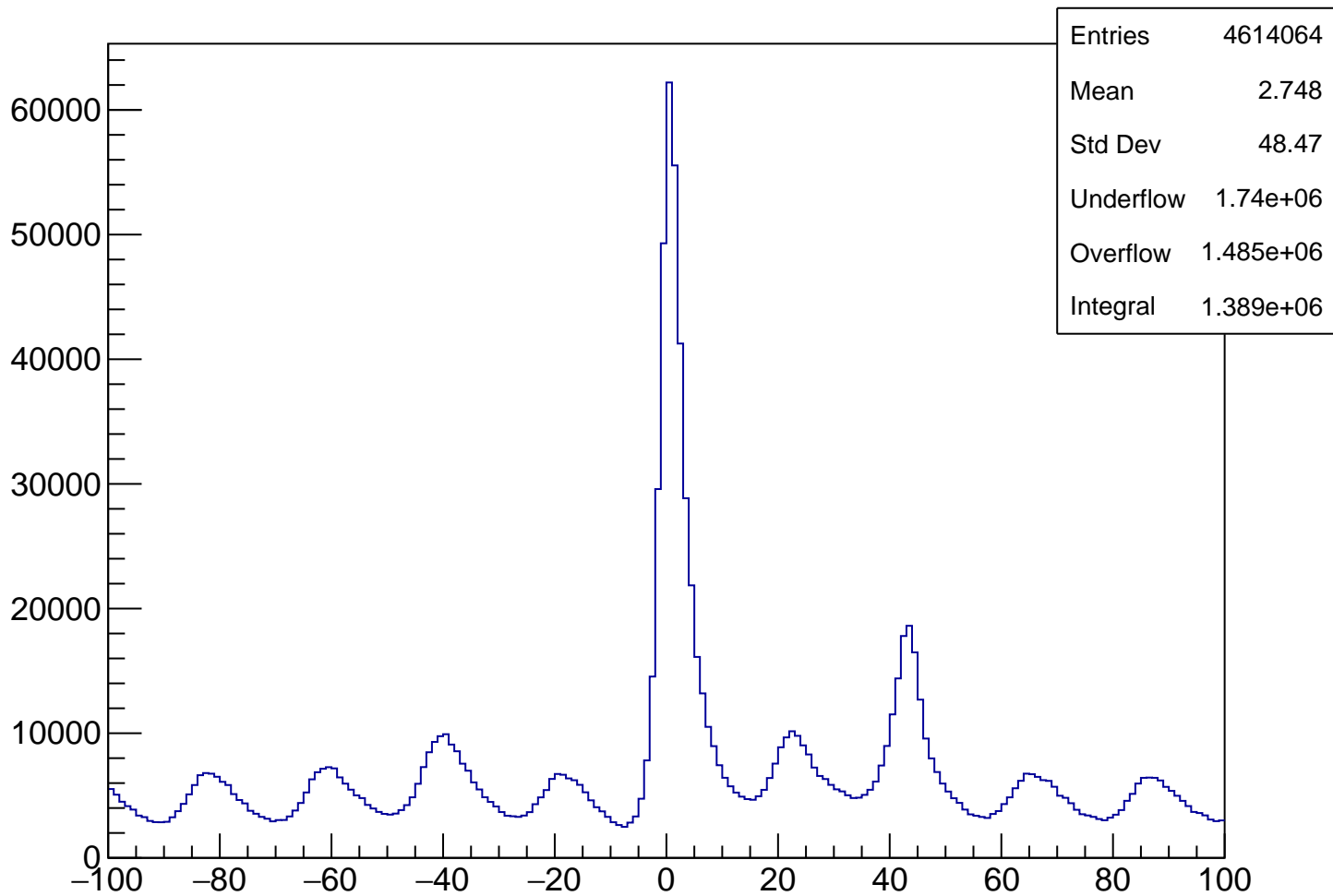
# Sch Hitpat



# Sch Depthpat

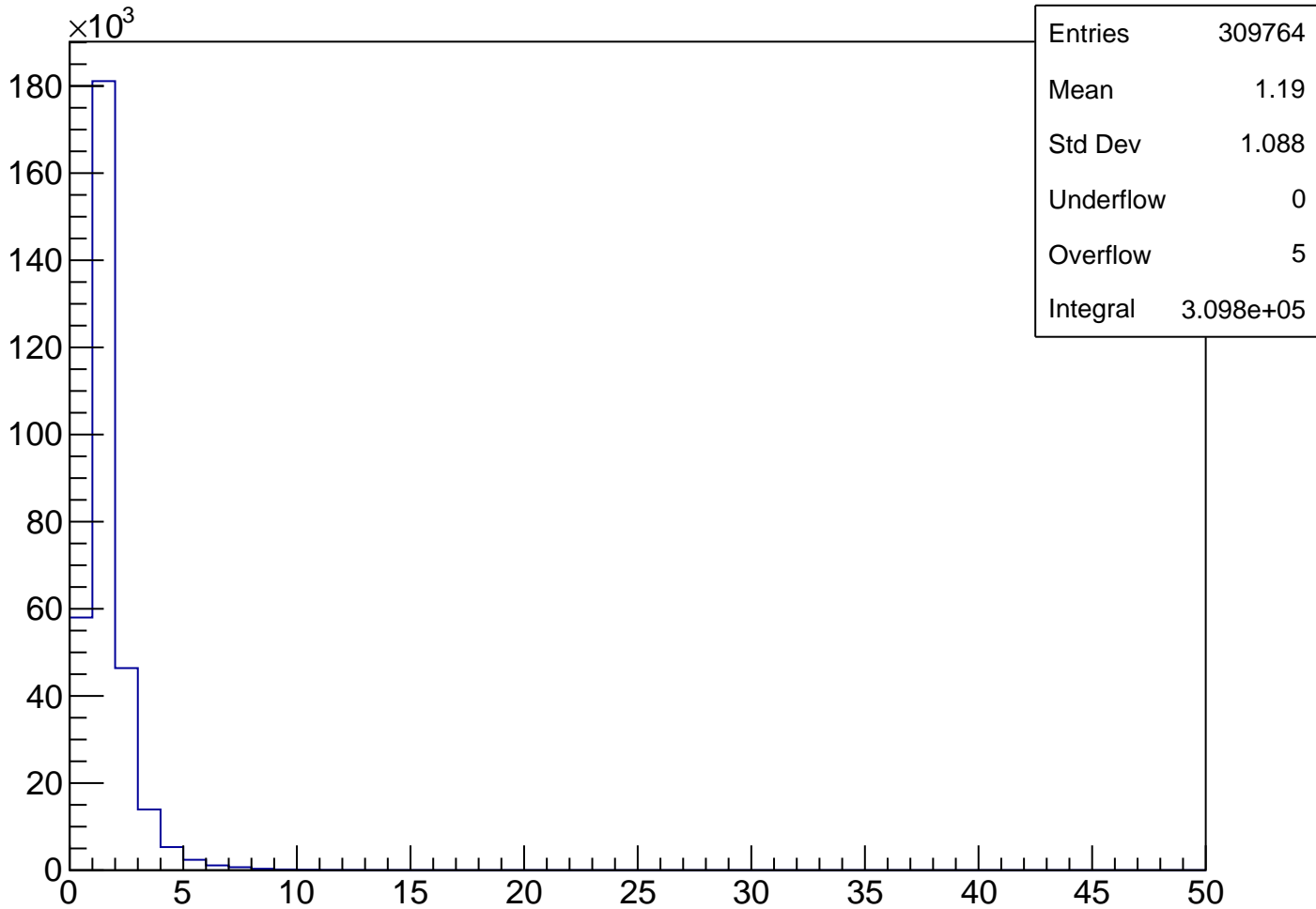


# Sch Time

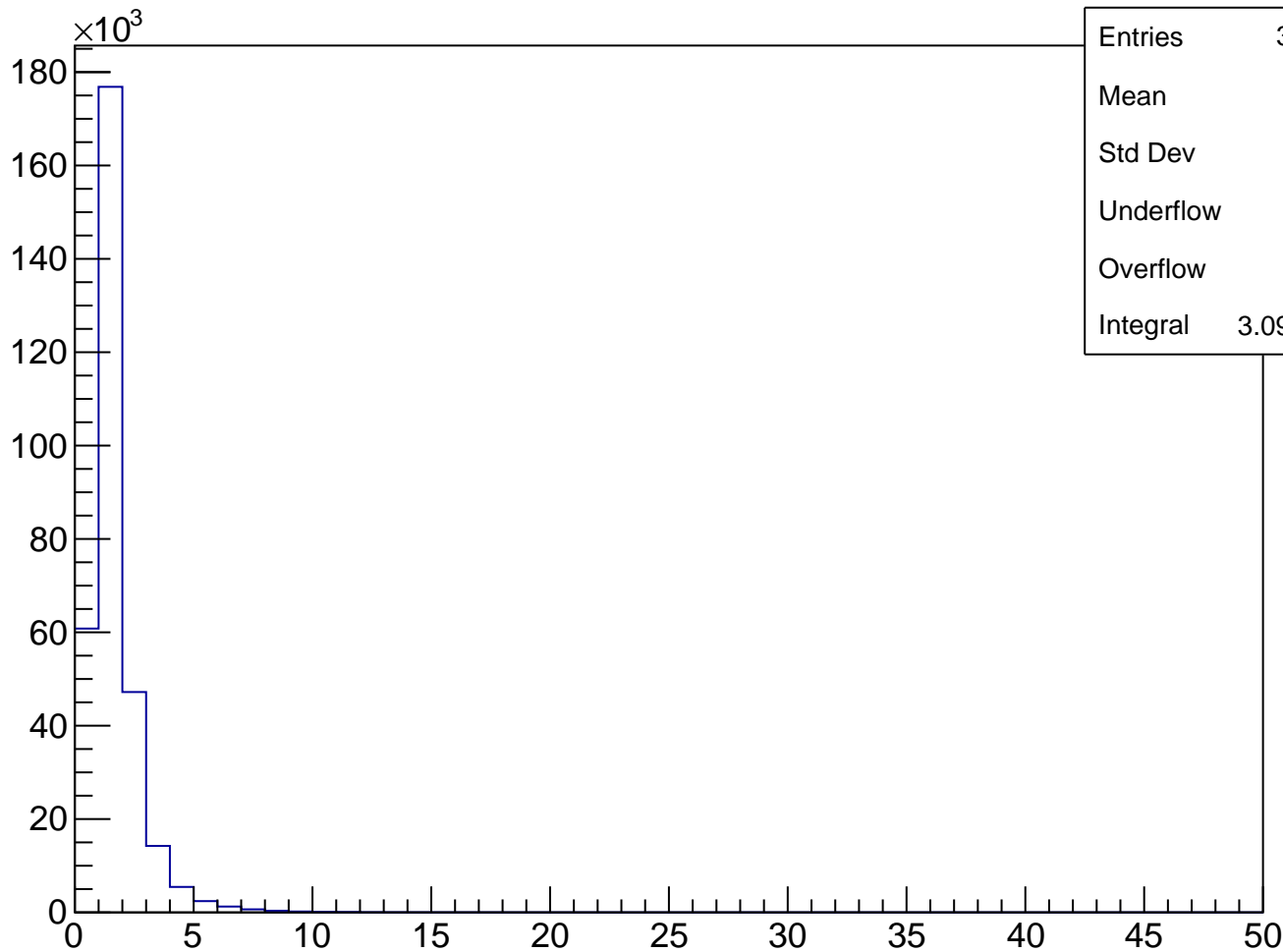




# SftX U Nhits

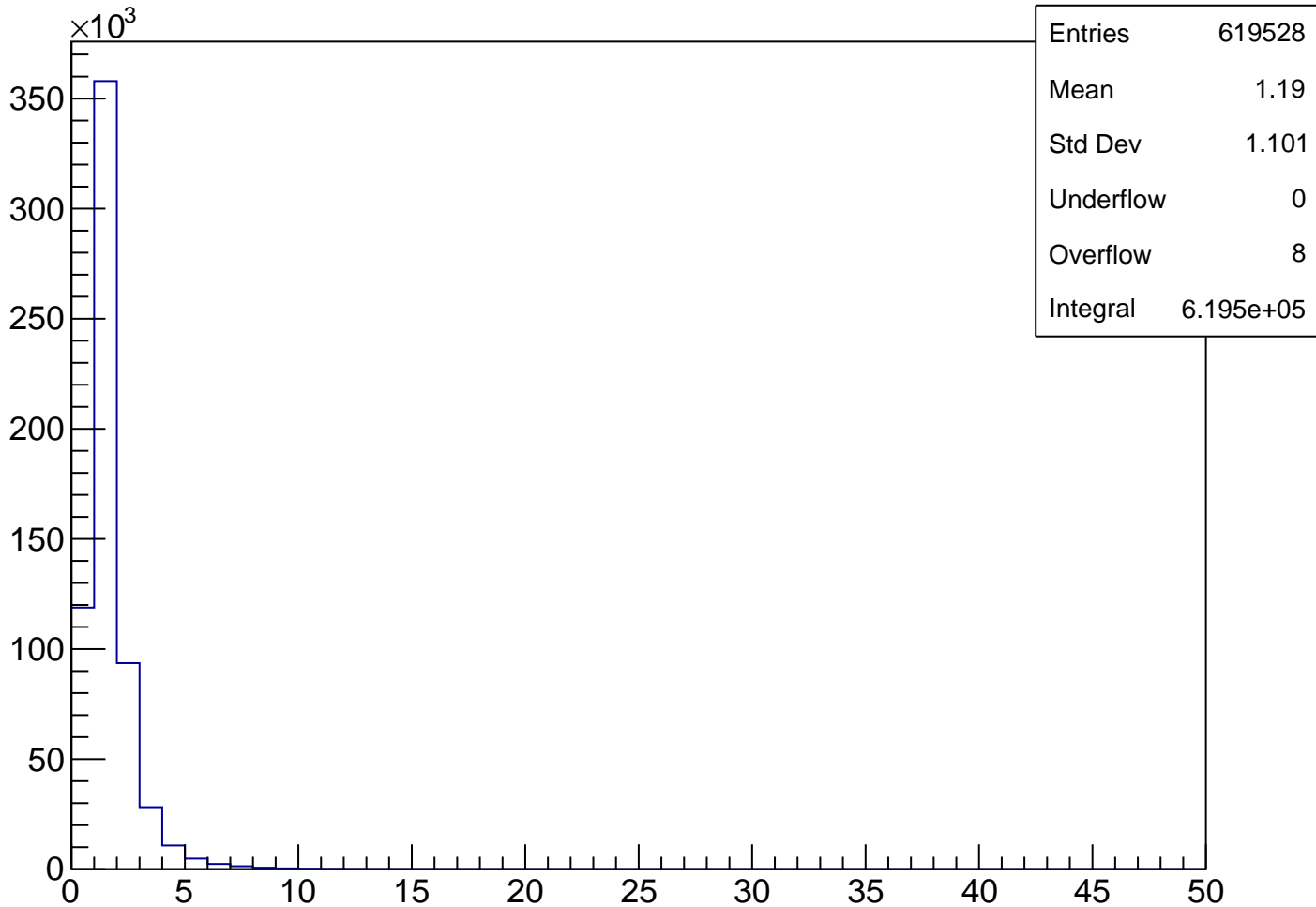


# SftX D Nhits

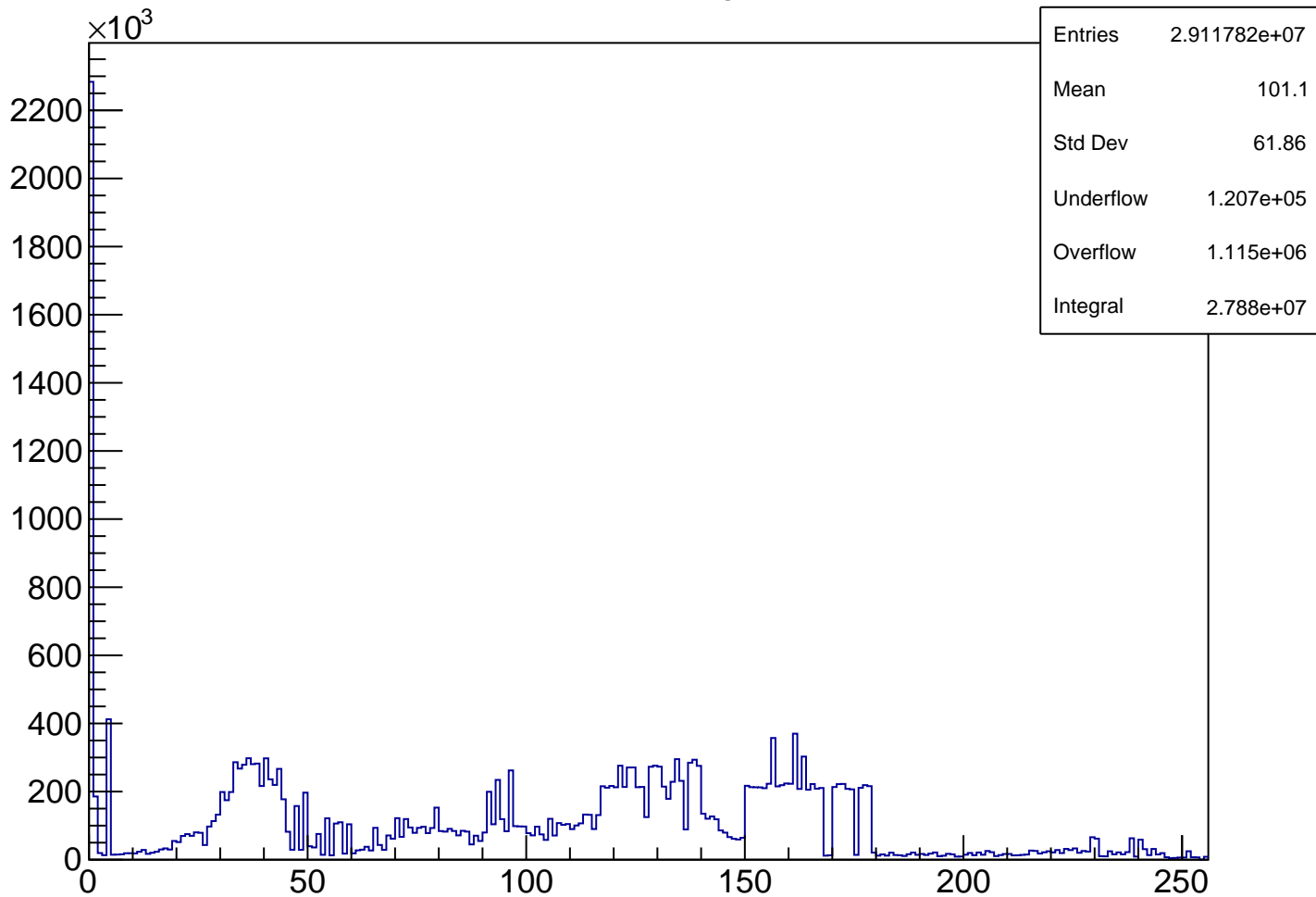


|           |           |
|-----------|-----------|
| Entries   | 309764    |
| Mean      | 1.191     |
| Std Dev   | 1.114     |
| Underflow | 0         |
| Overflow  | 3         |
| Integral  | 3.098e+05 |

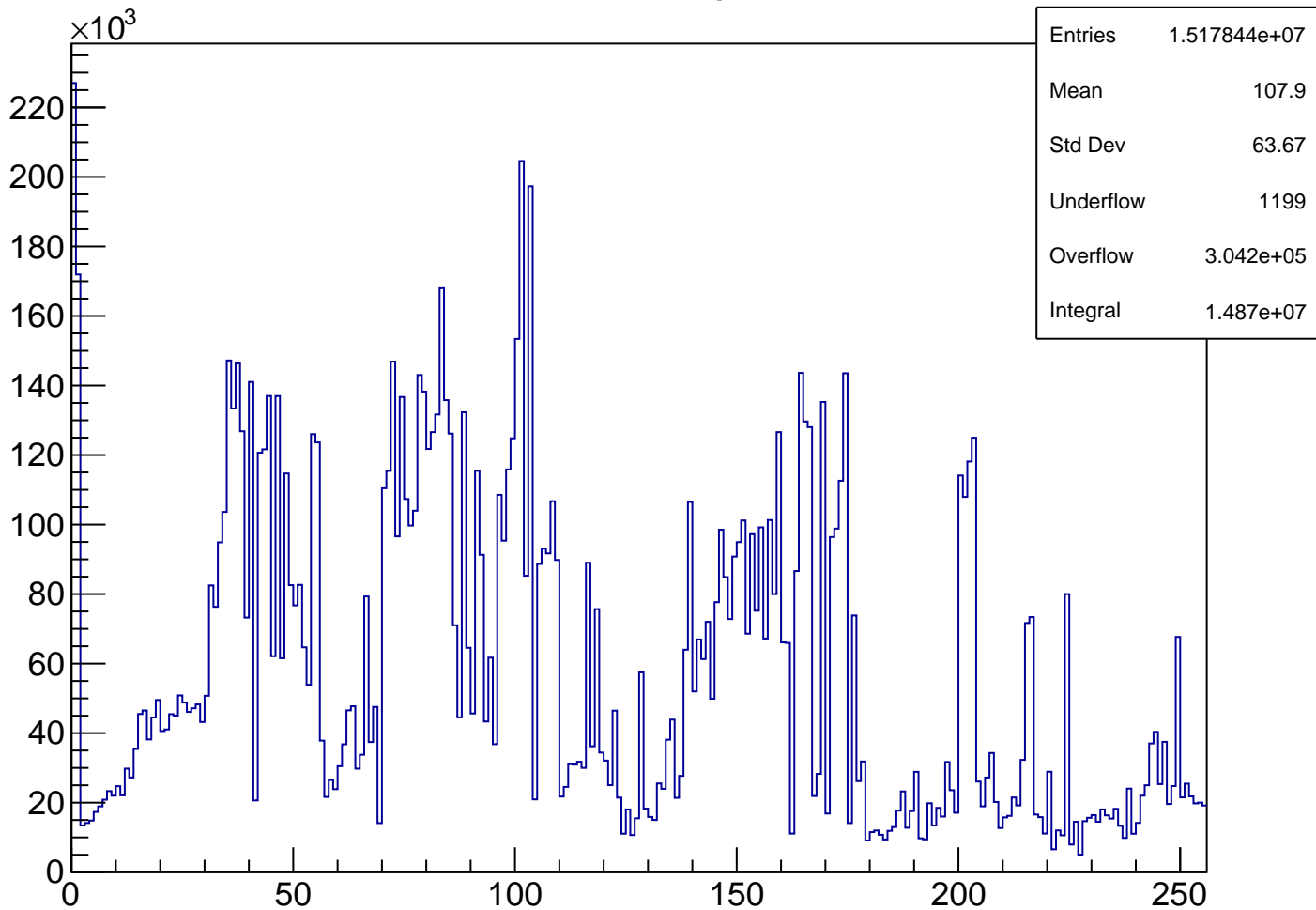
# SftXNhits



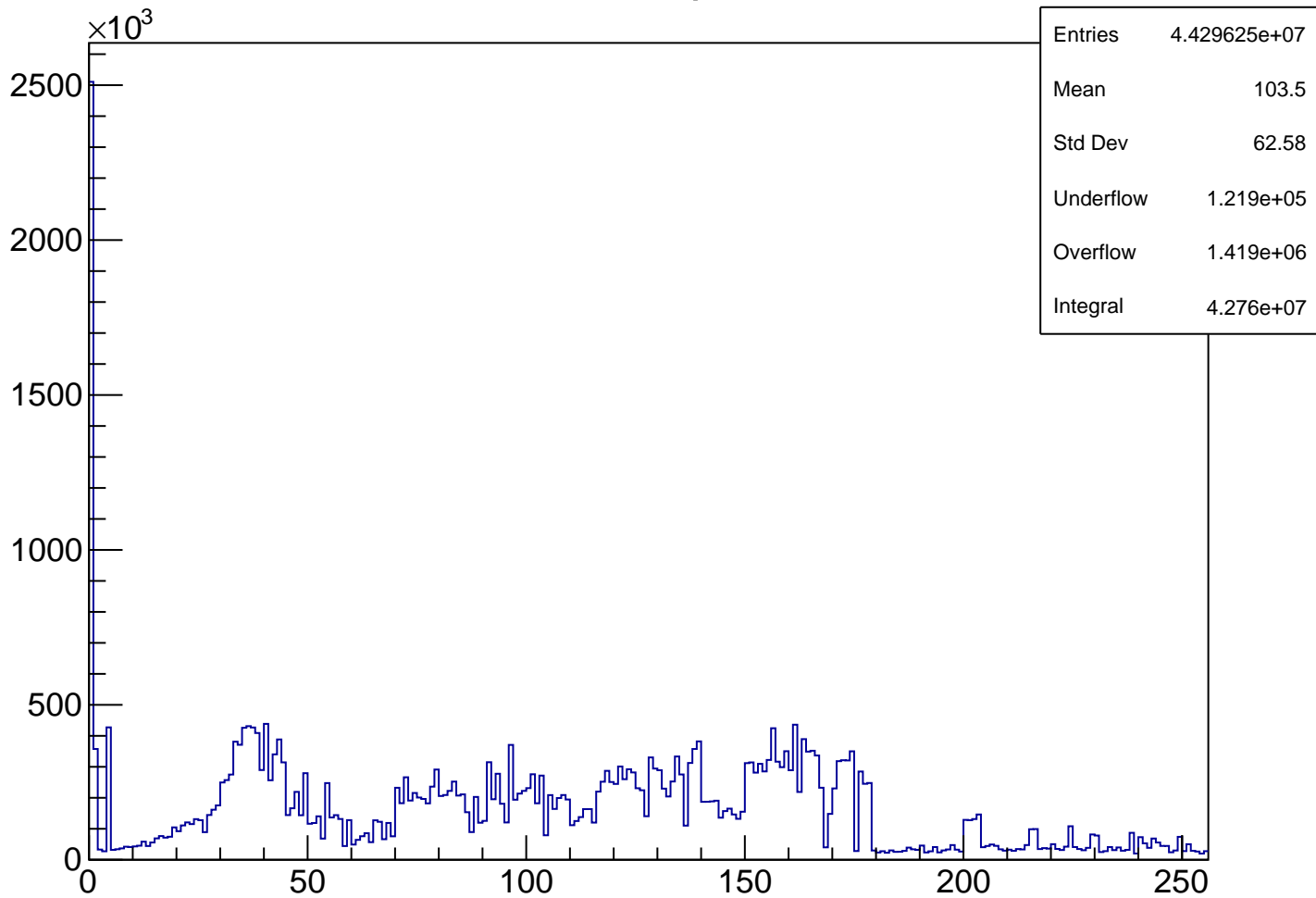
# SftX U Hitpat



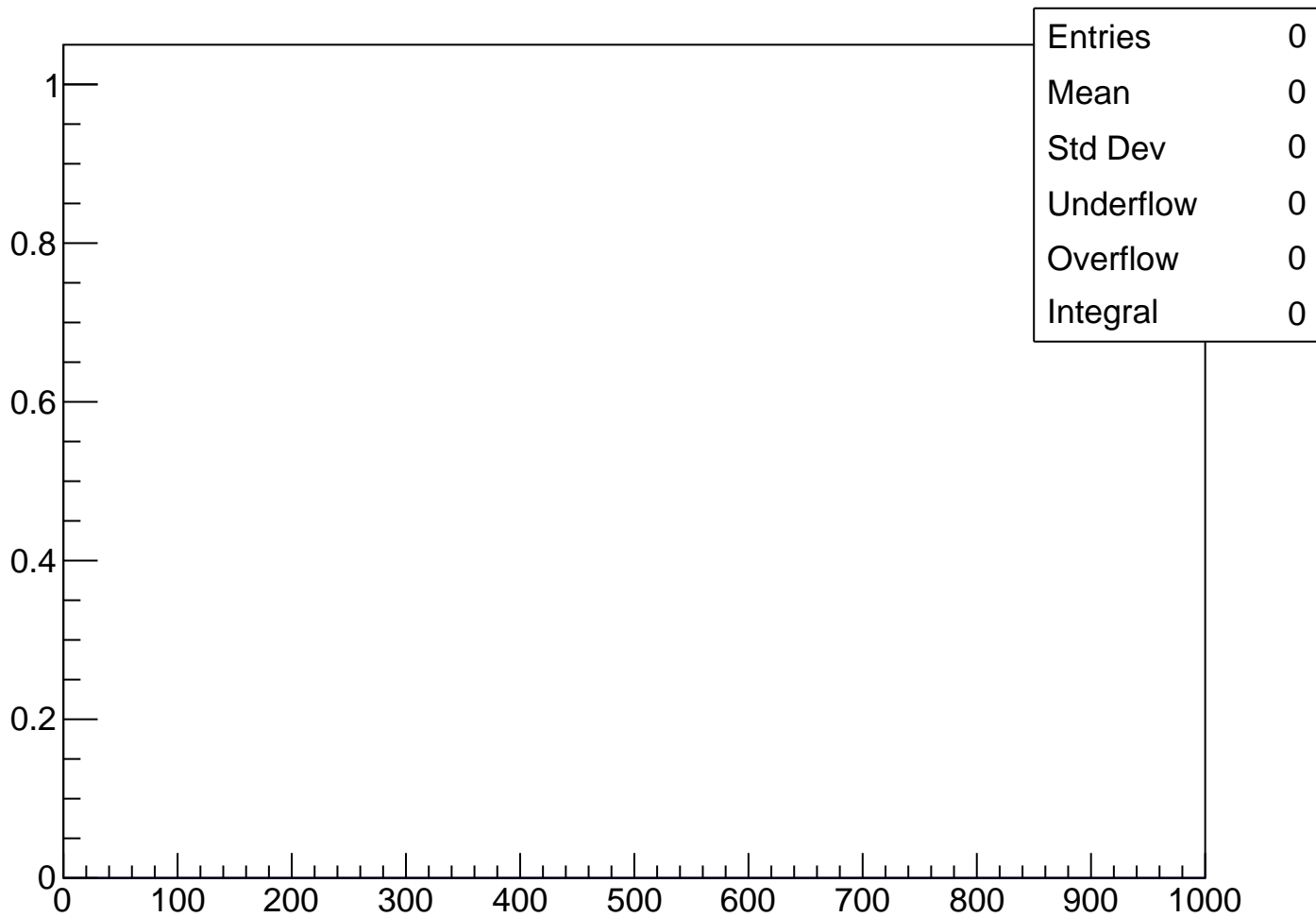
# SftX D Hitpat



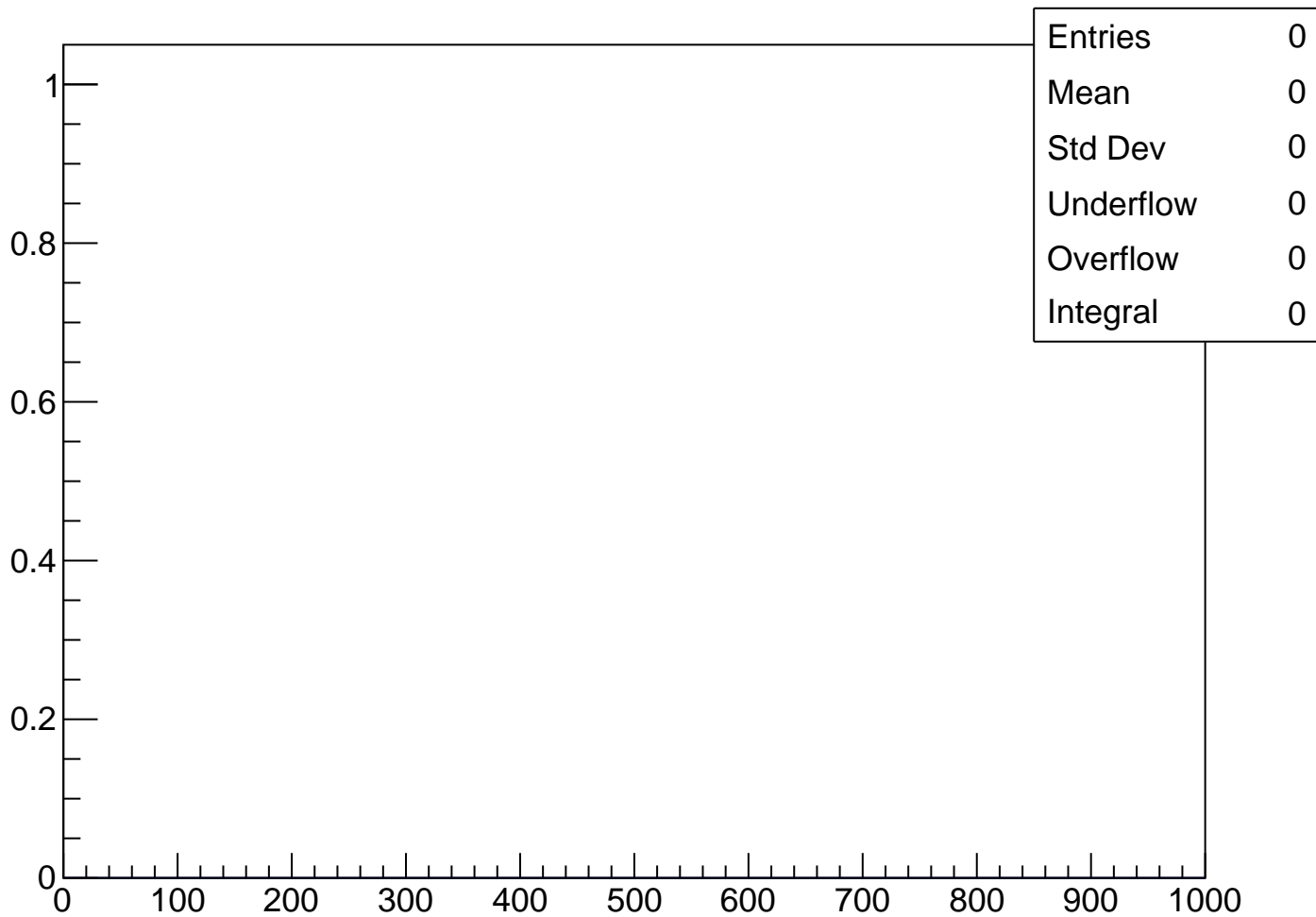
# SftXHitpat



# SftX U Tdc

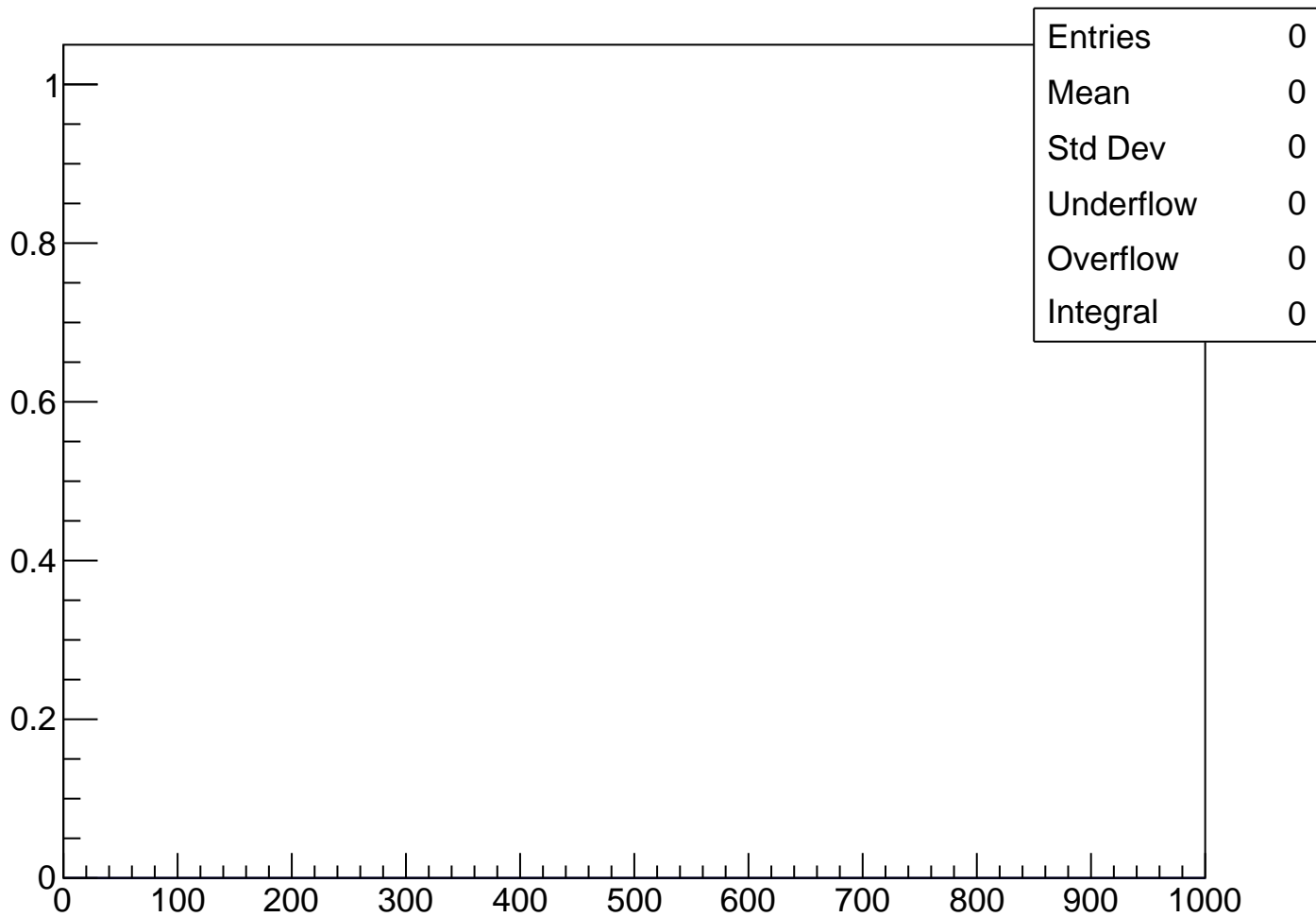


# SftX D Tdc

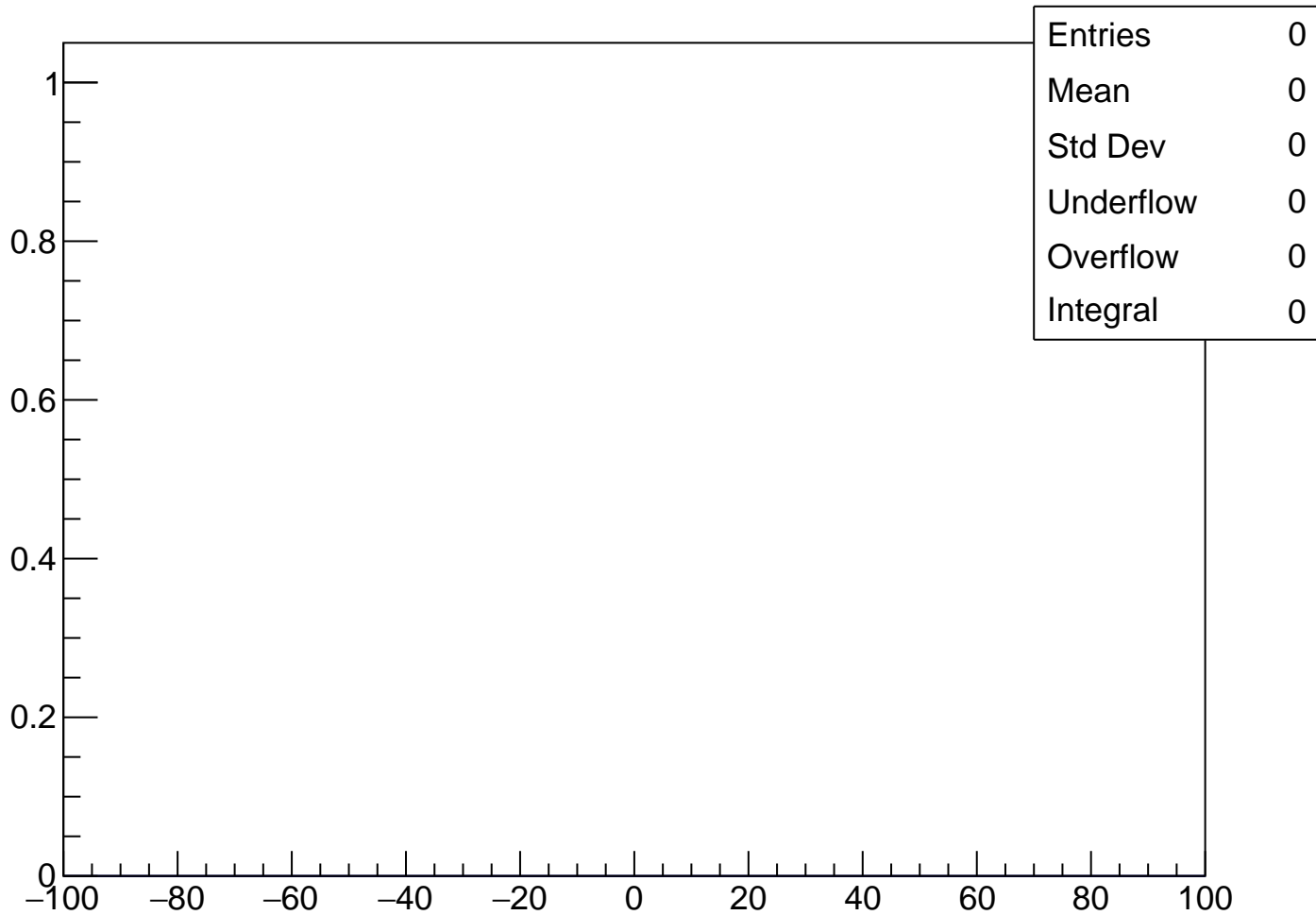




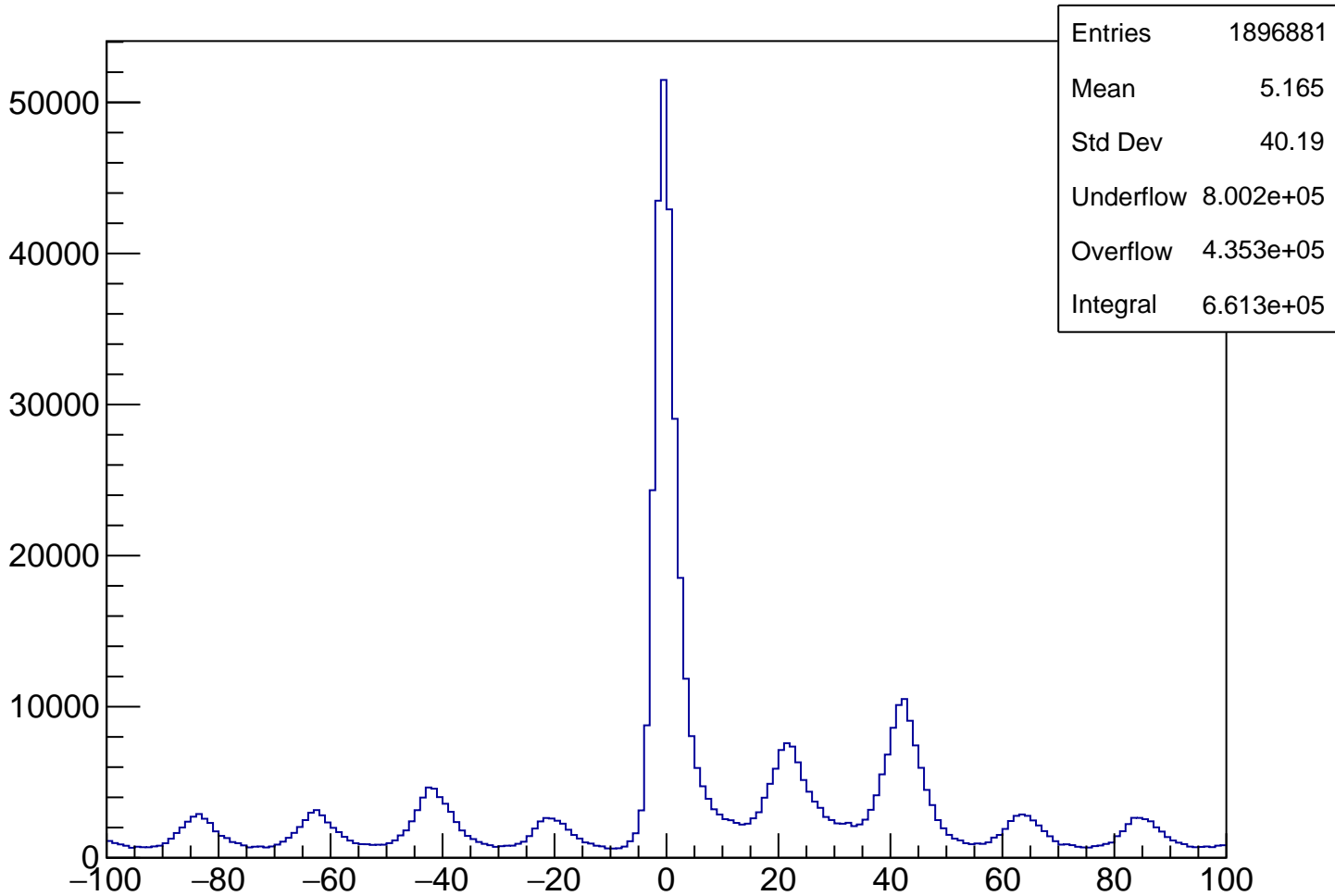
# SftXTdc



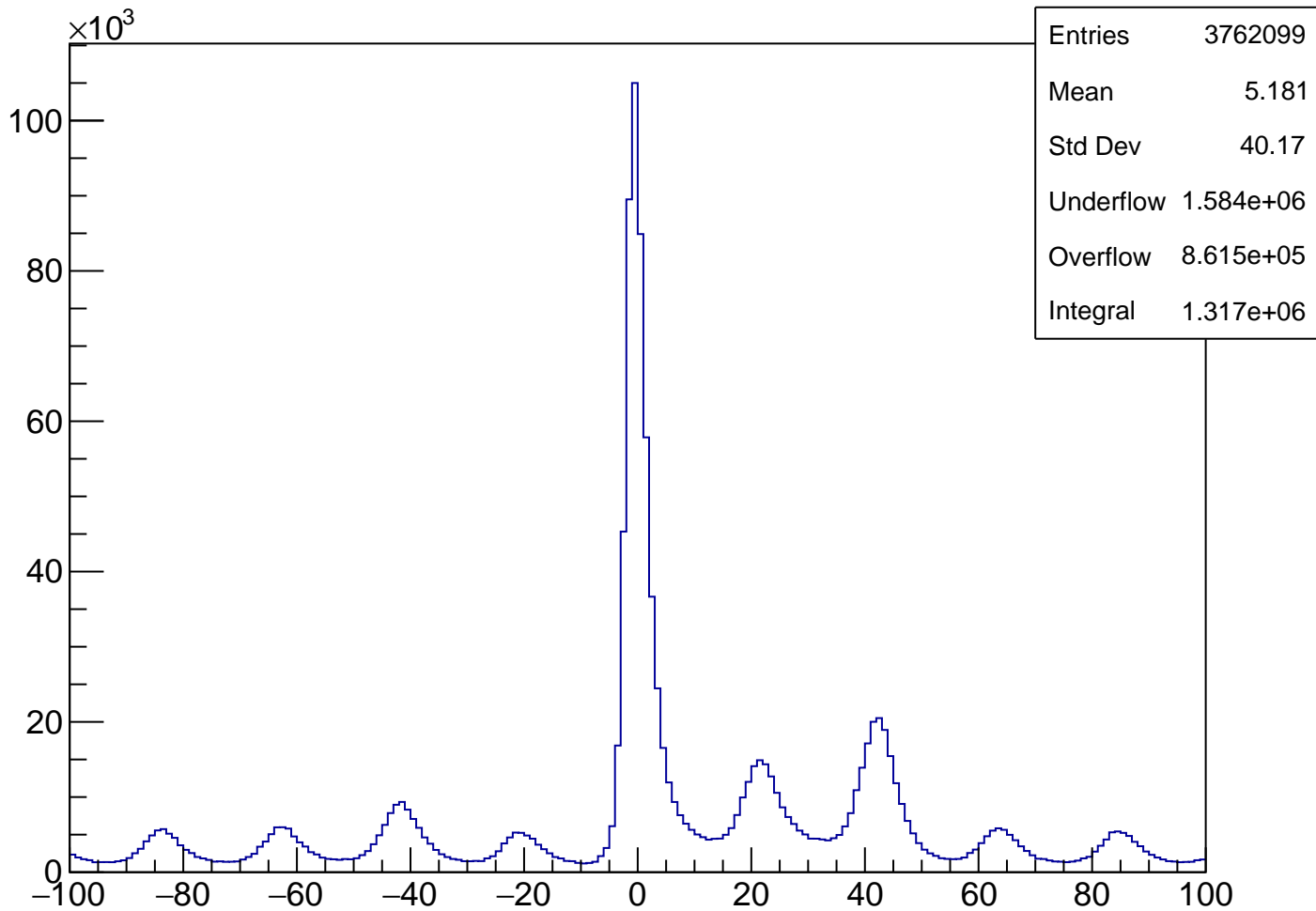
# SftX U Time



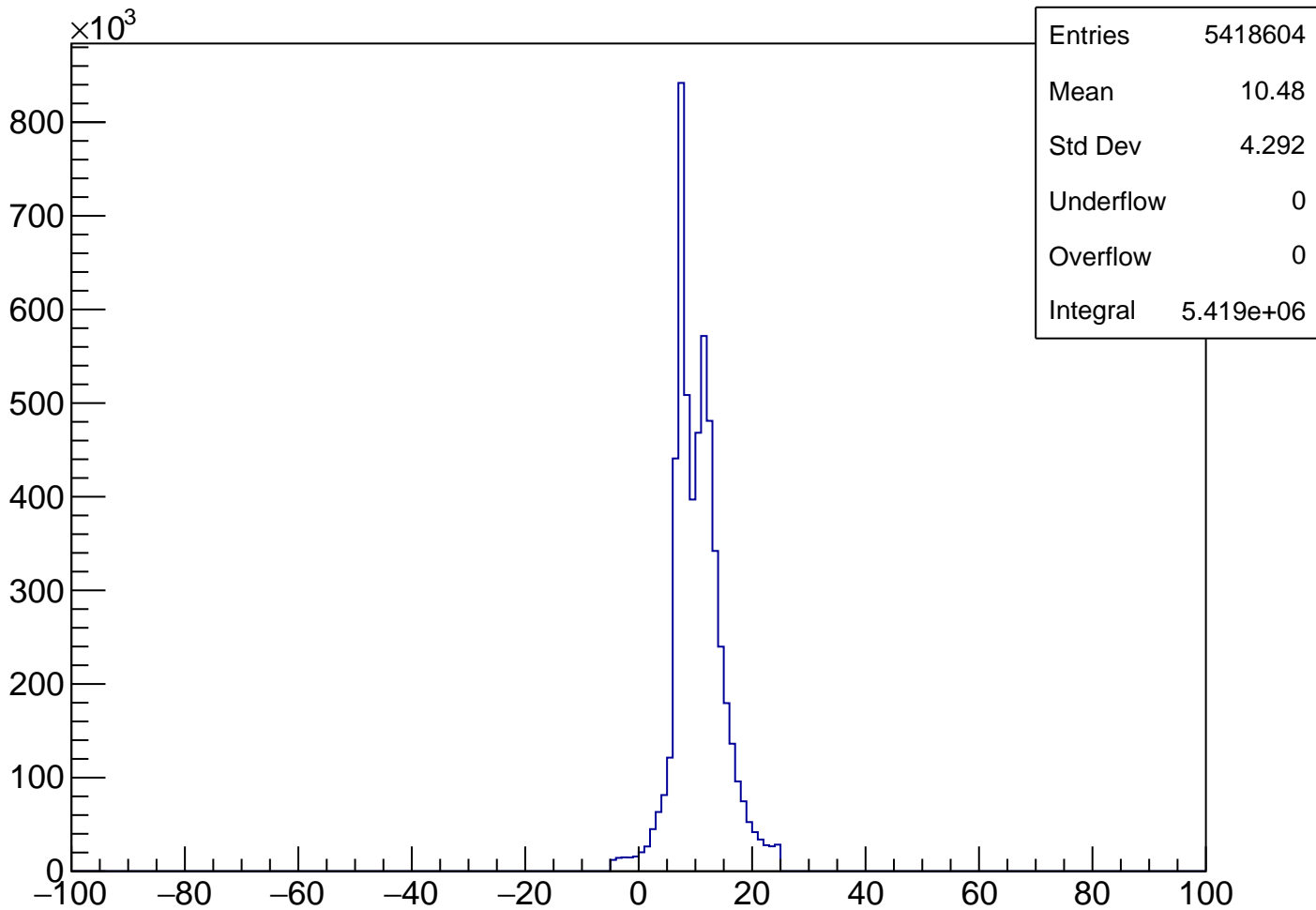
# SftX D Time



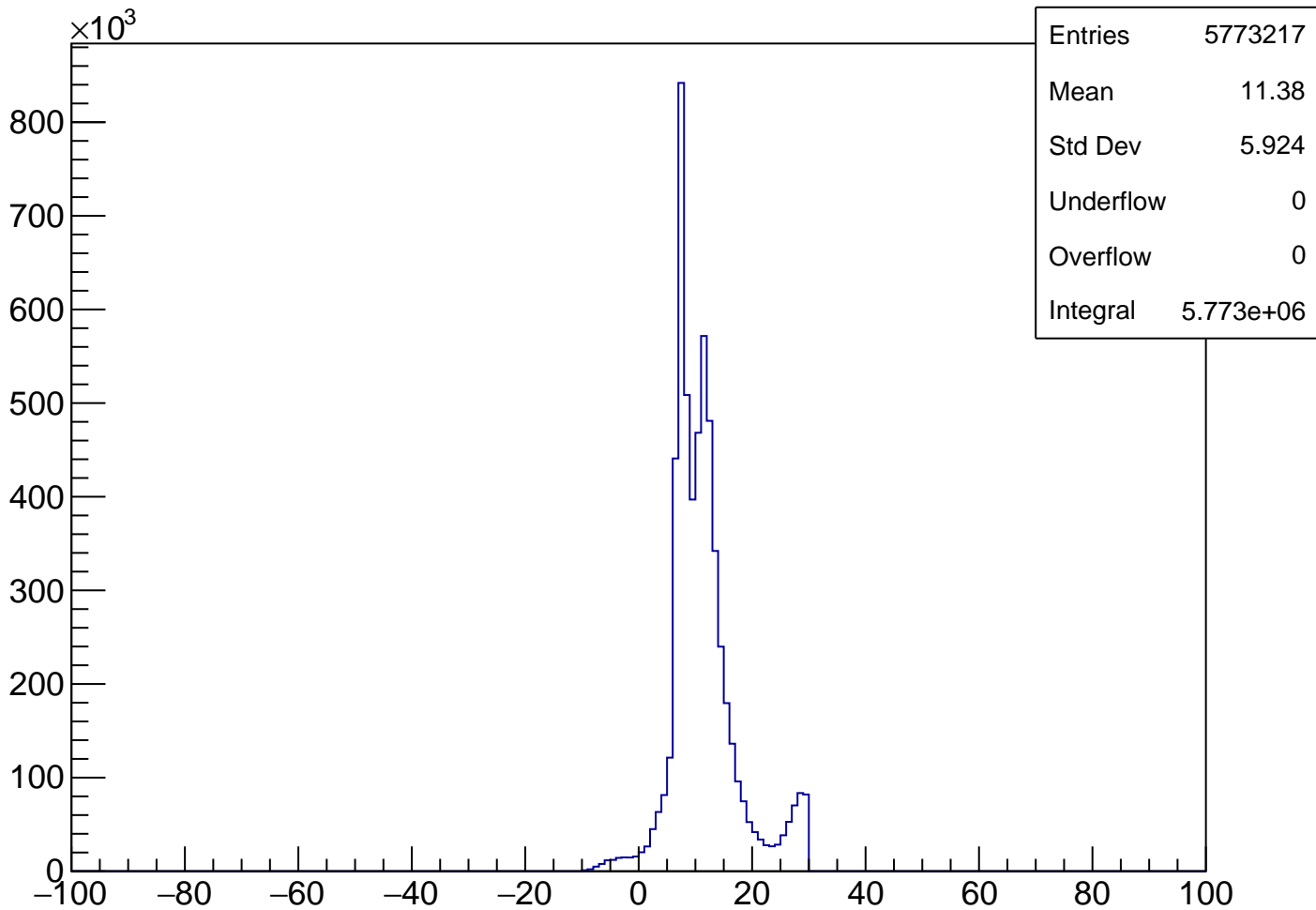
# SftXTime



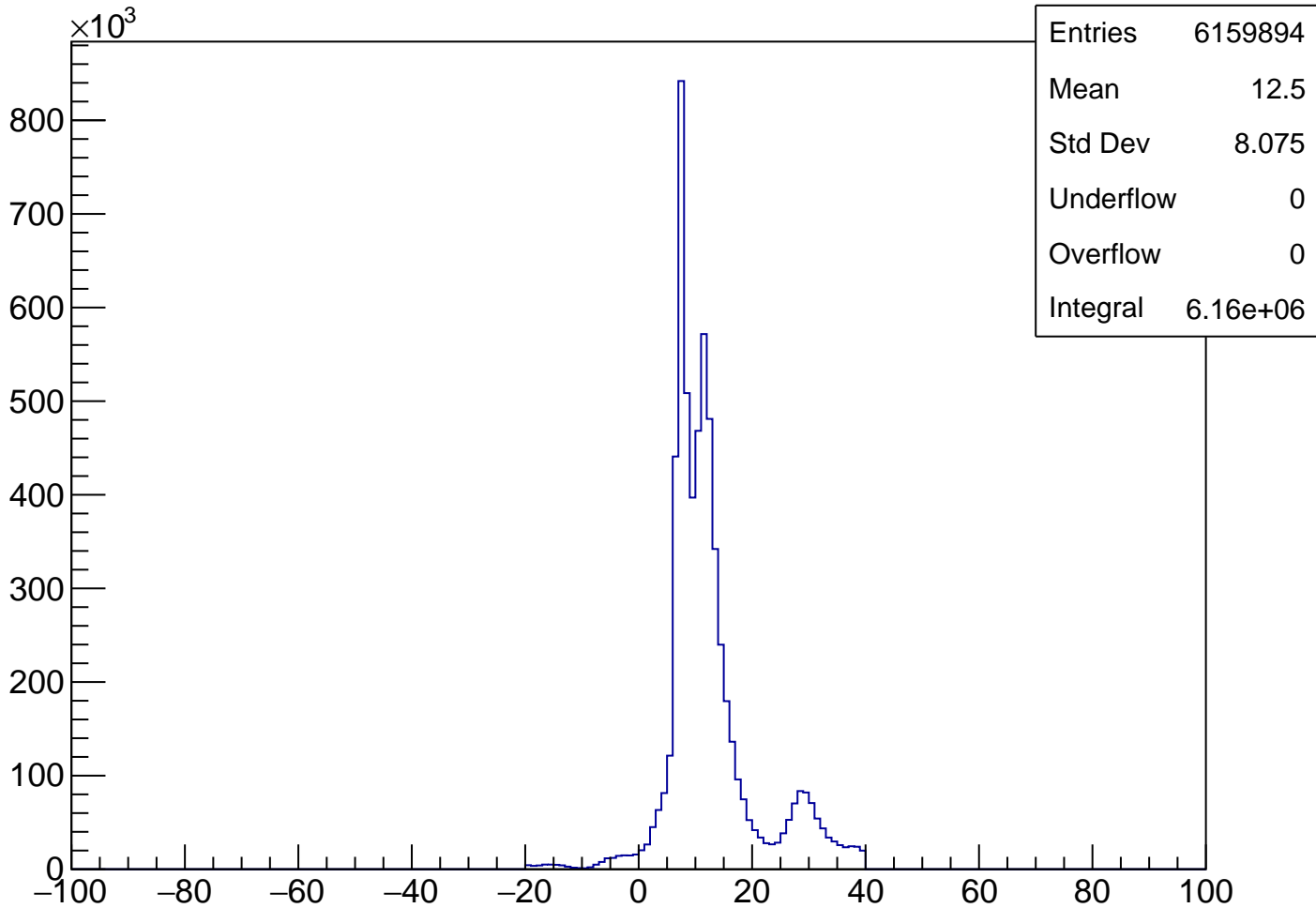
# TofMtOr Gate1



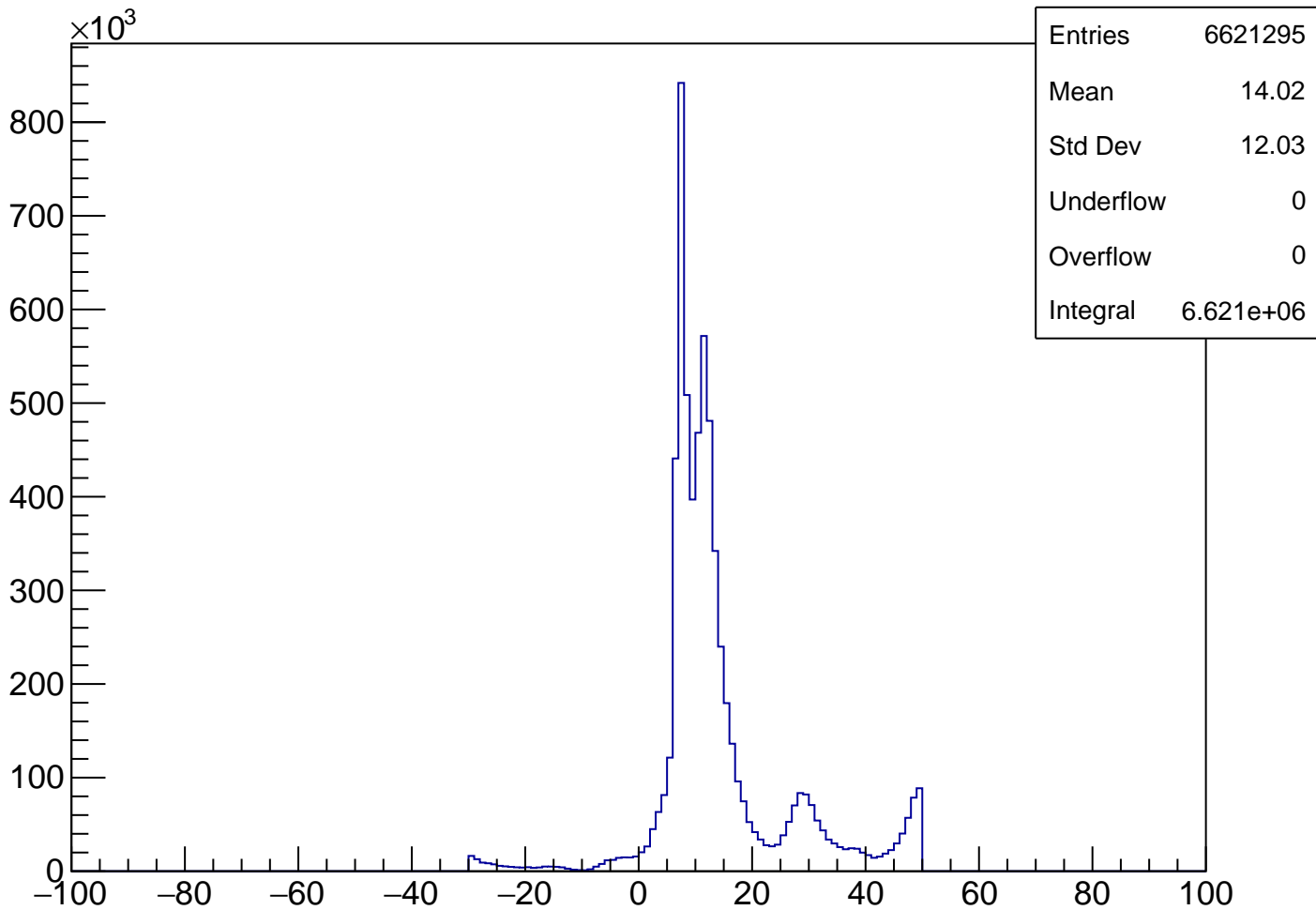
# TofMtOr Gate2



# TofMtOr Gate3

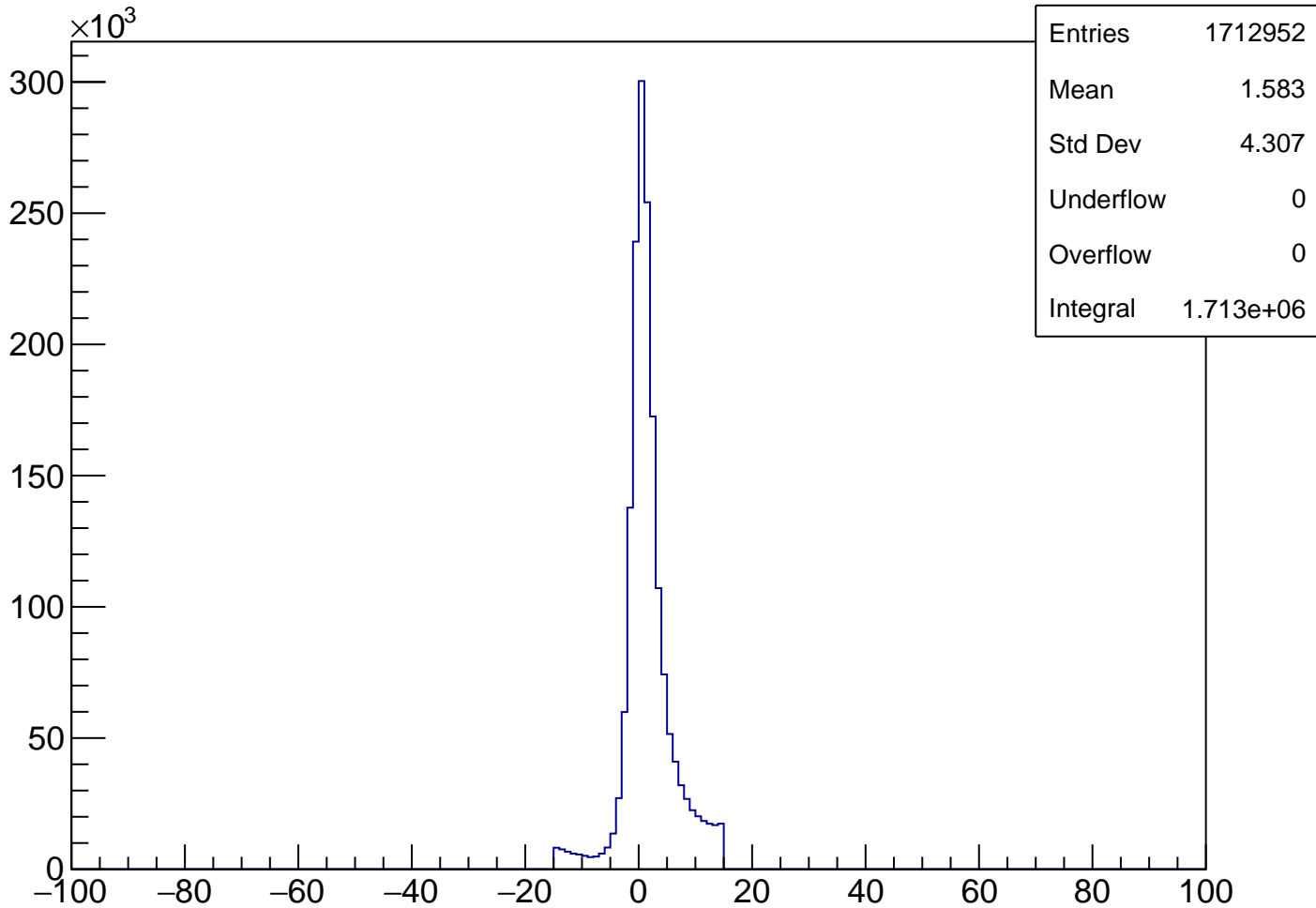


# TofMtOr Gate4

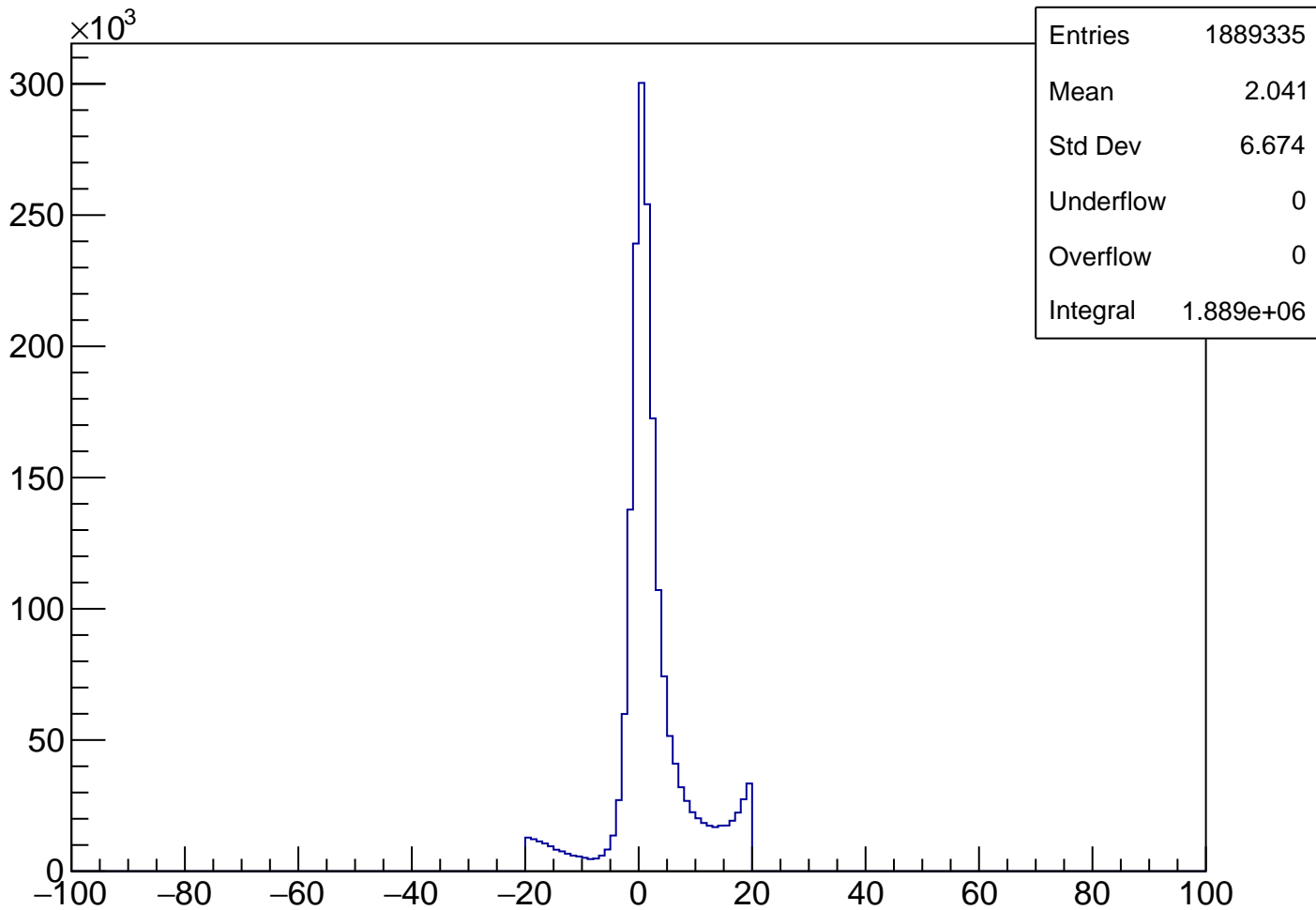




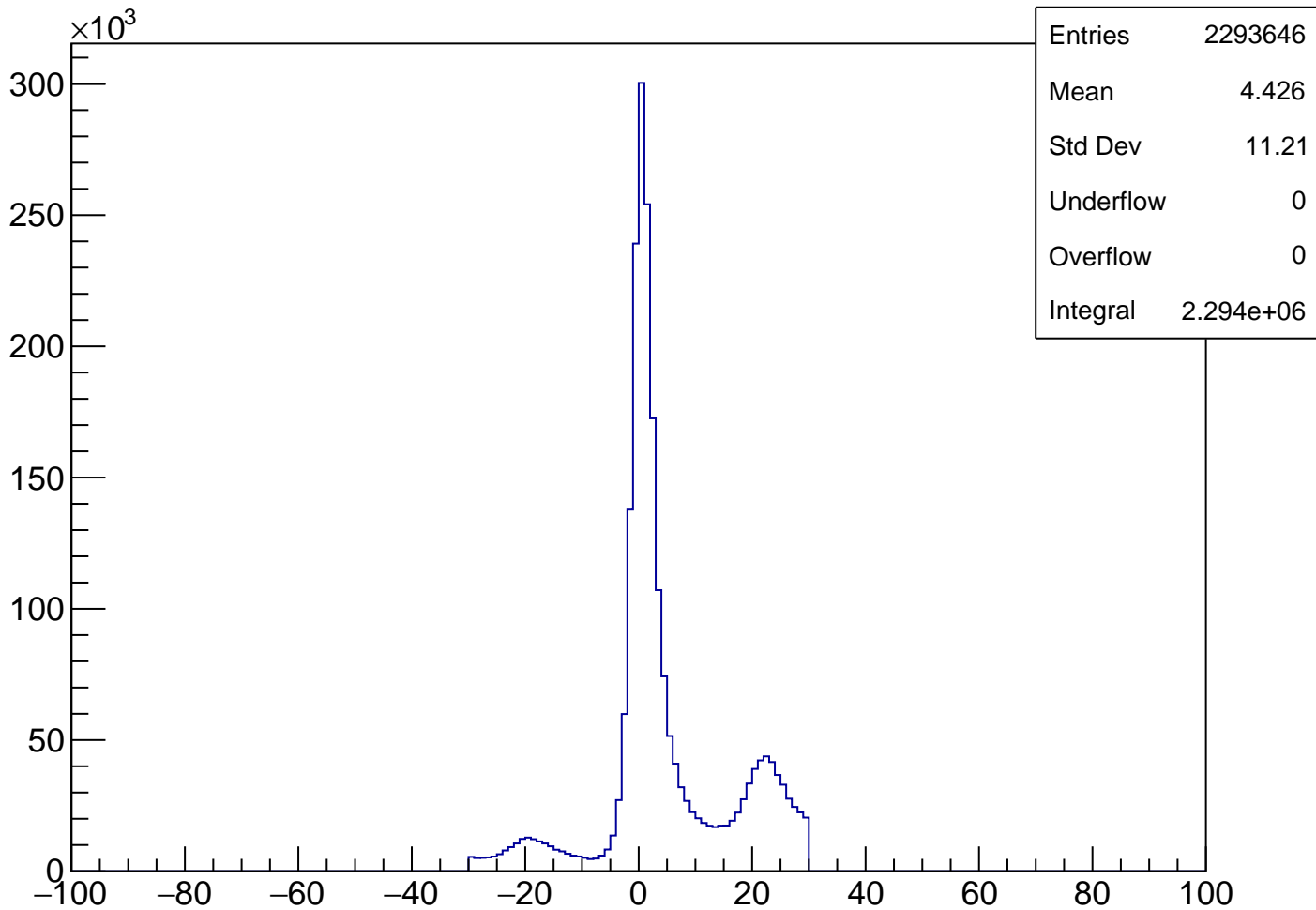
# Sch Time Gate1



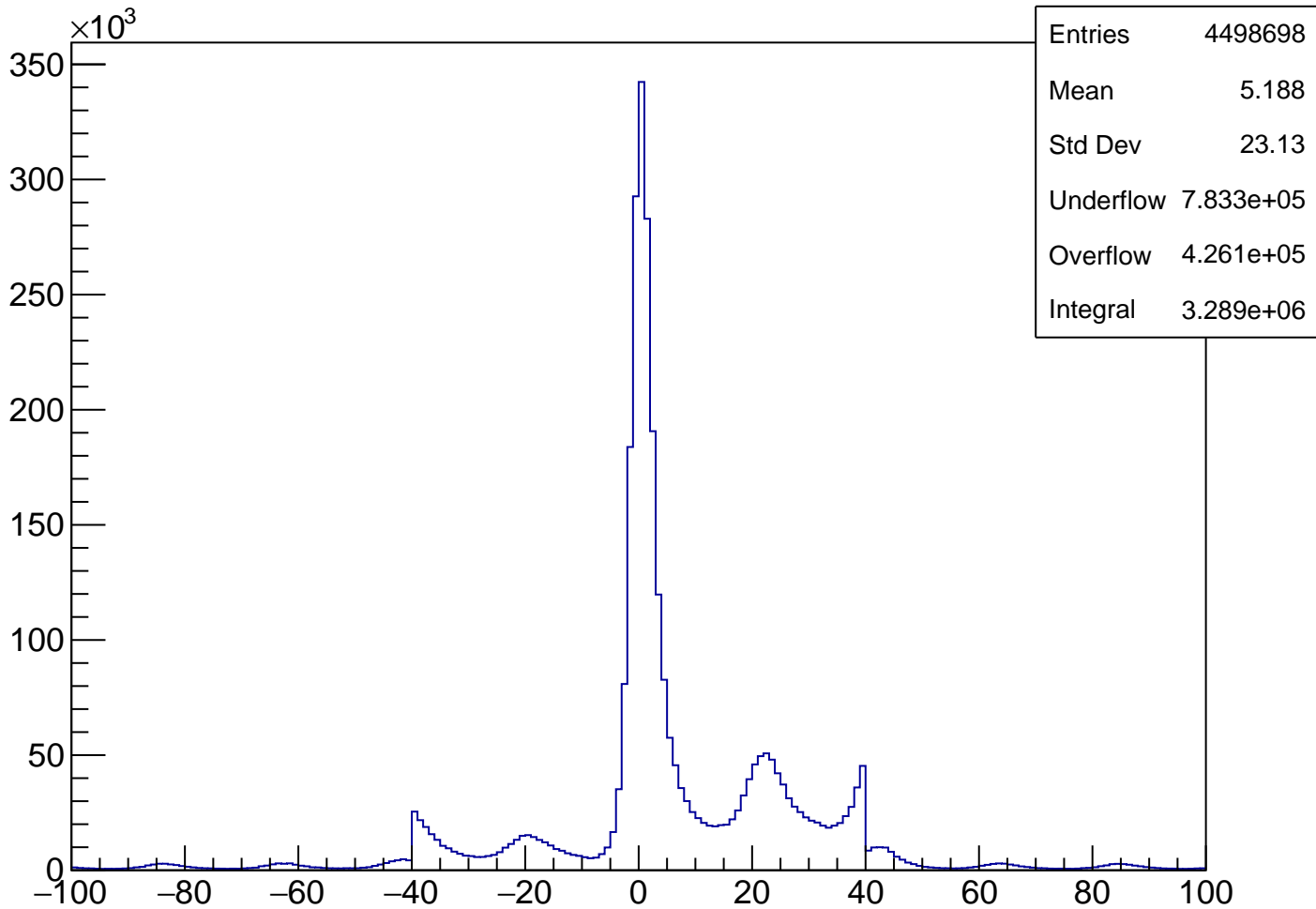
# Sch Time Gate2



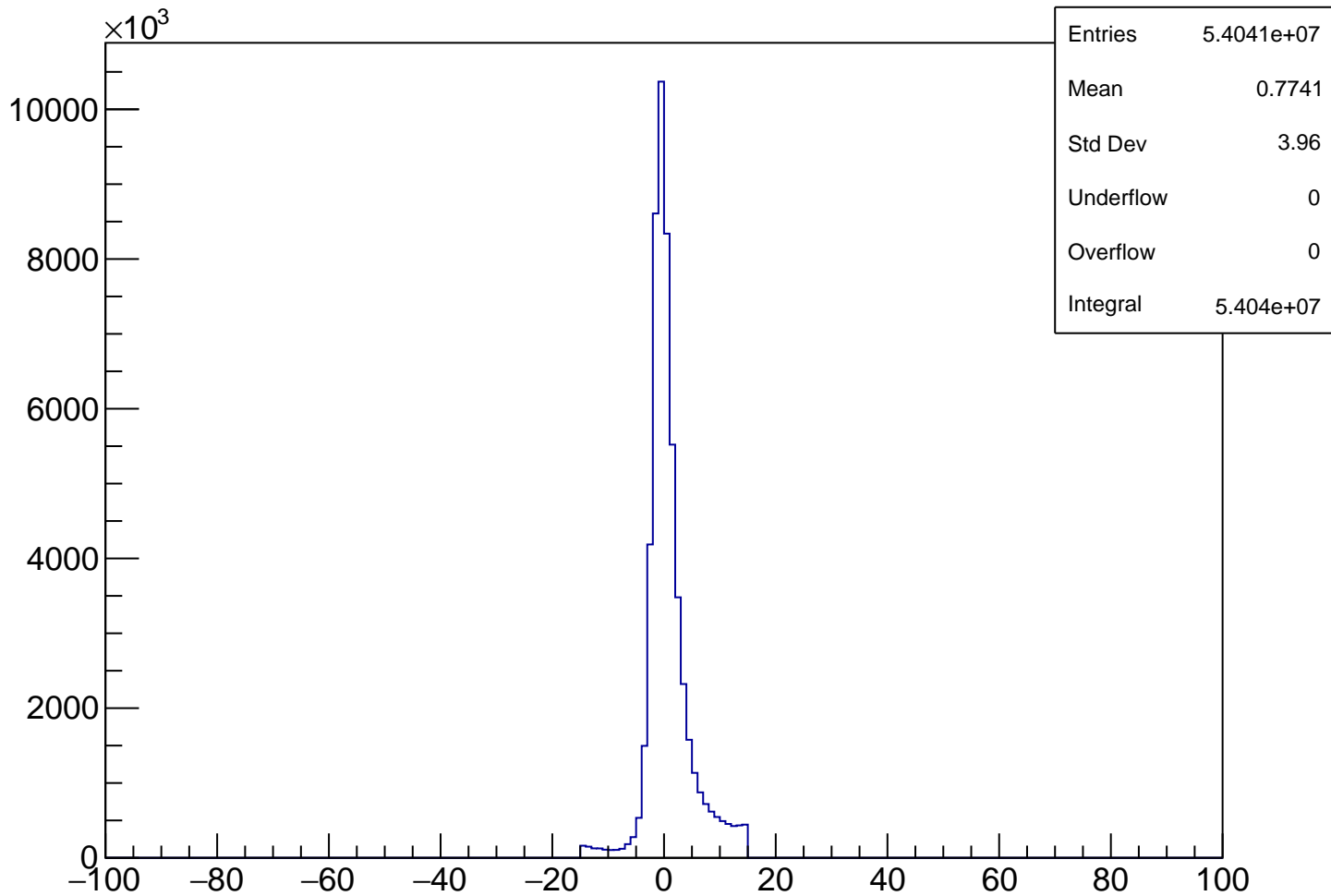
# Sch Time Gate3



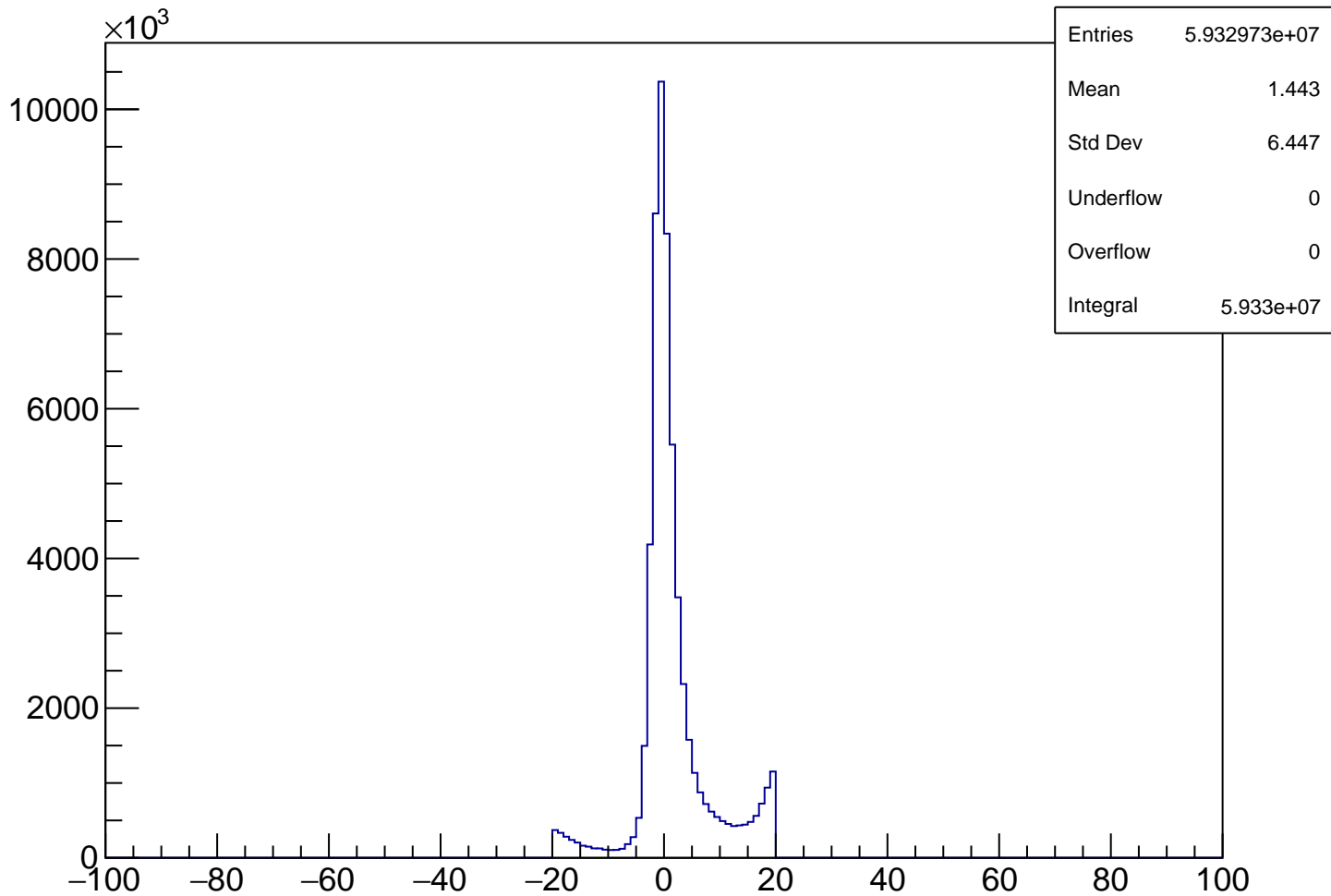
# Sch Time Gate4



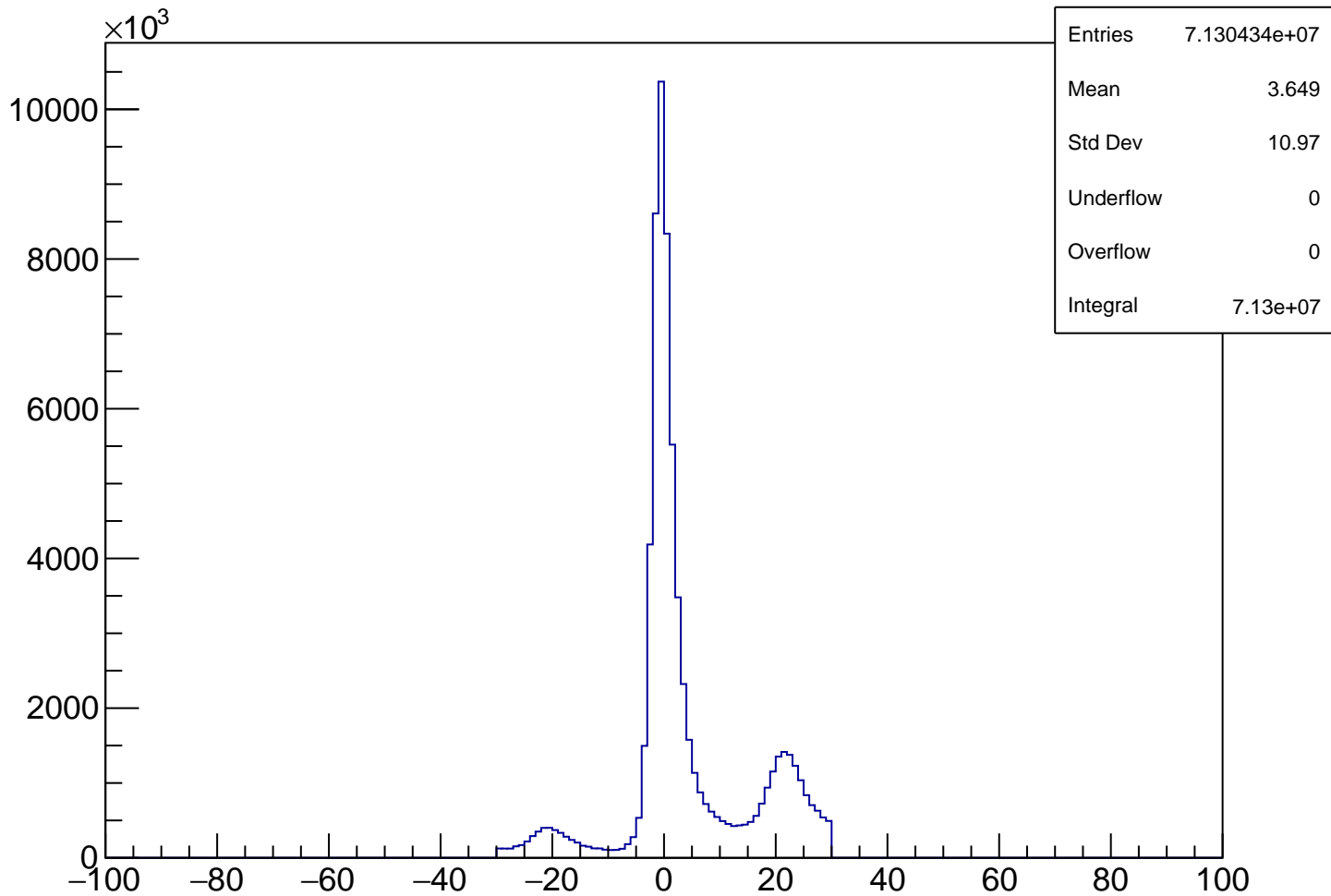
# SftX Time Gate1



# SftX Time Gate2



# SftX Time Gate3



# SftX Time Gate4

