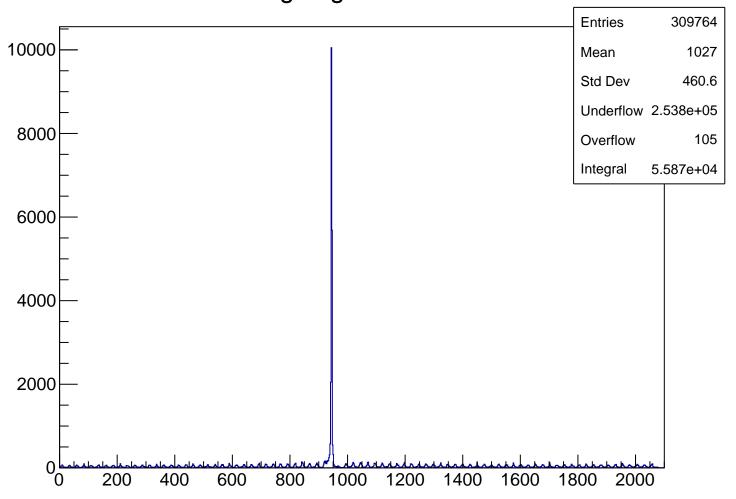
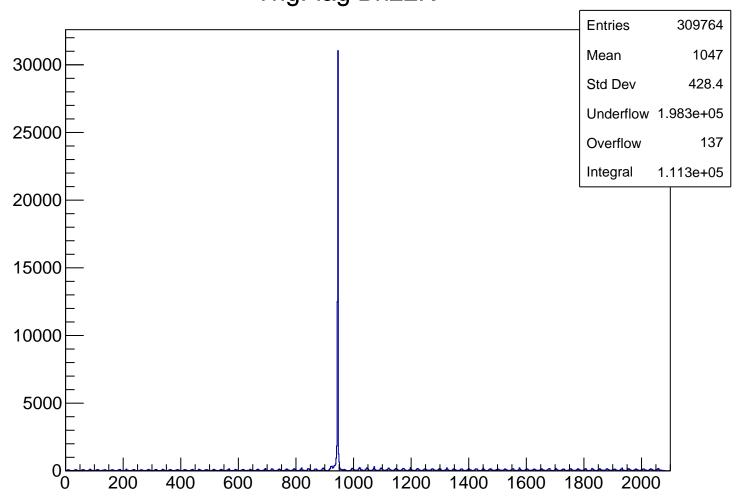
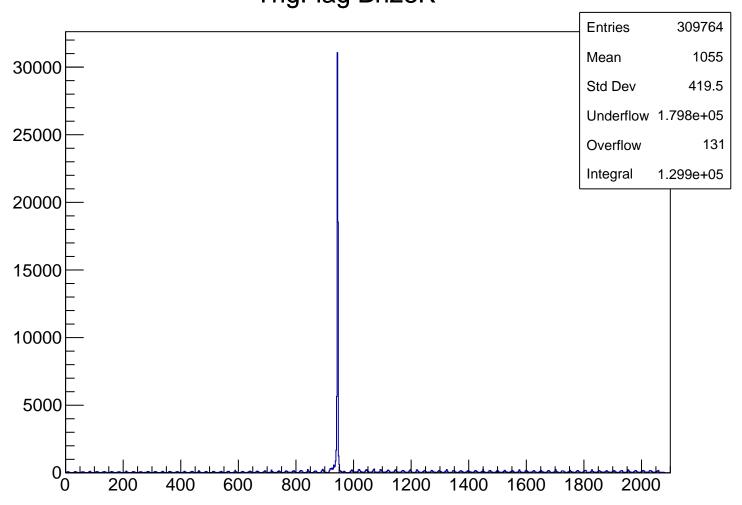
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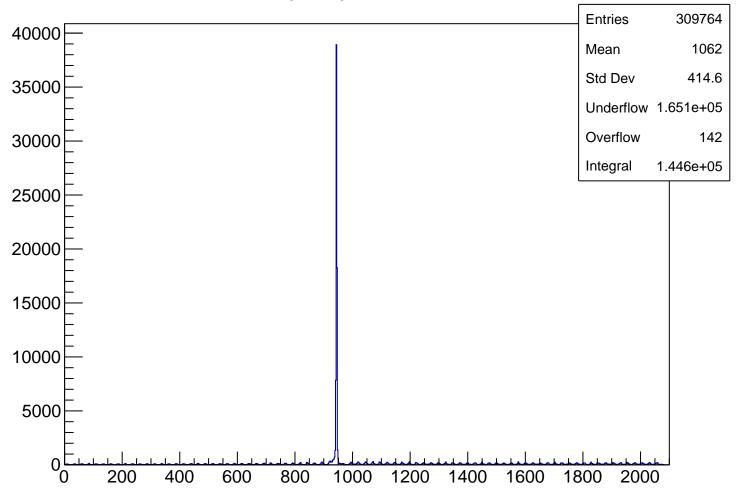
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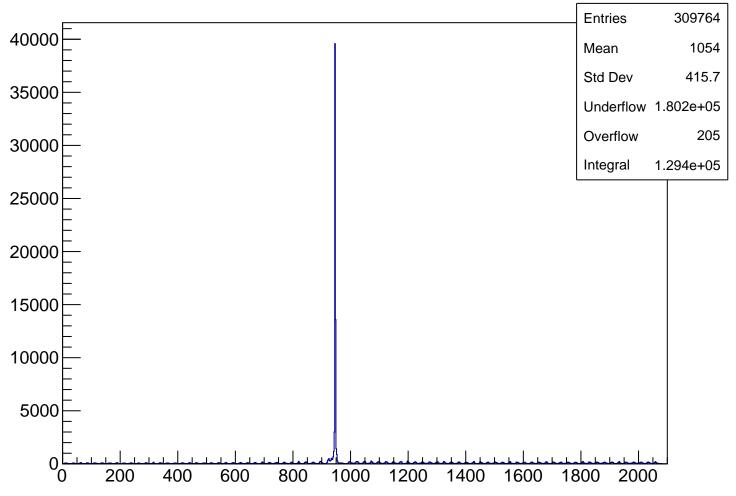
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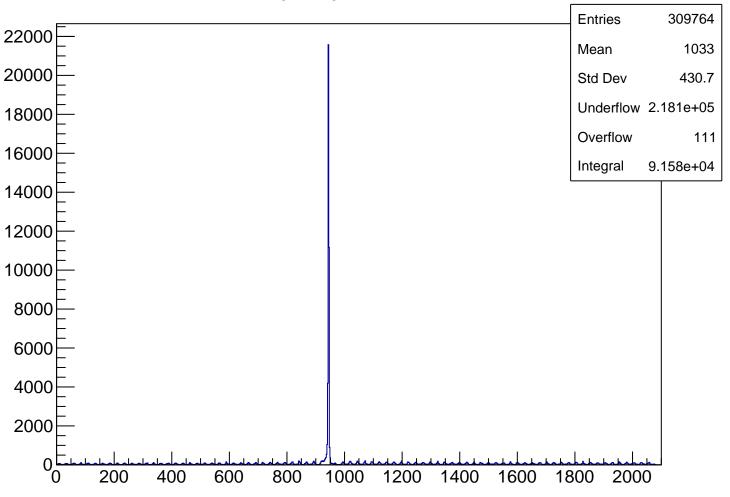
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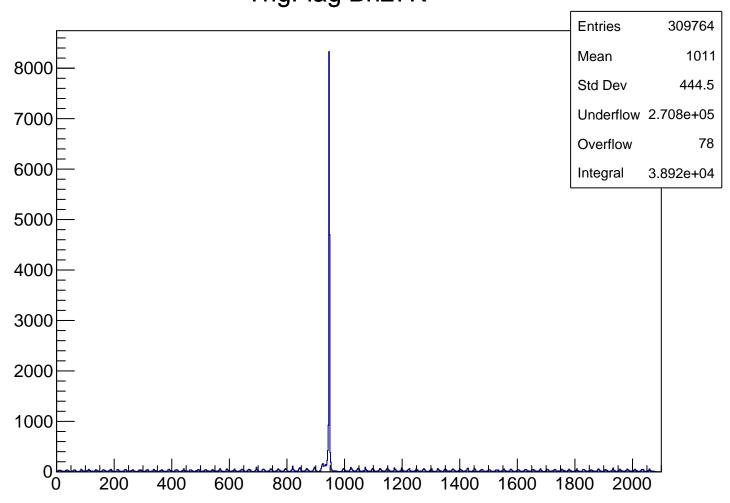
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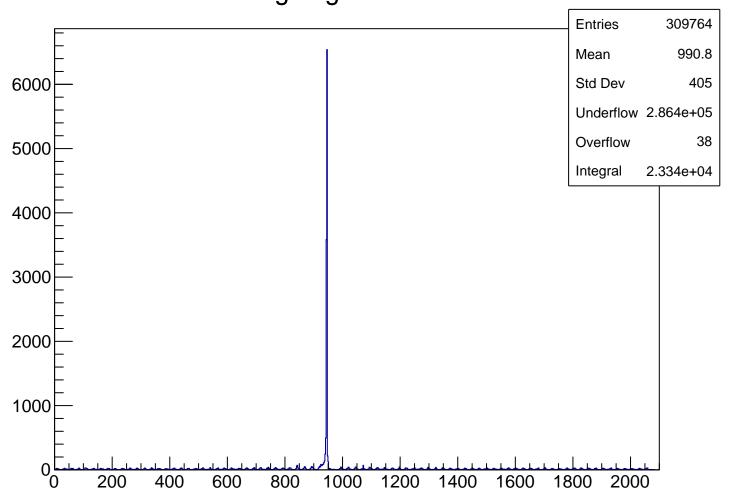
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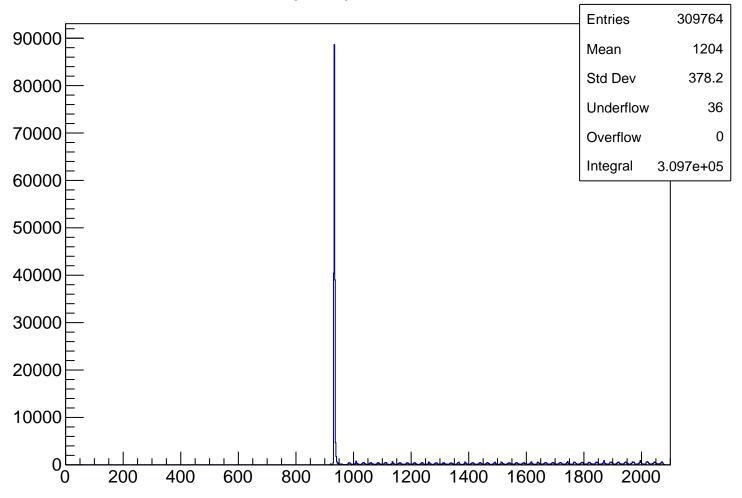
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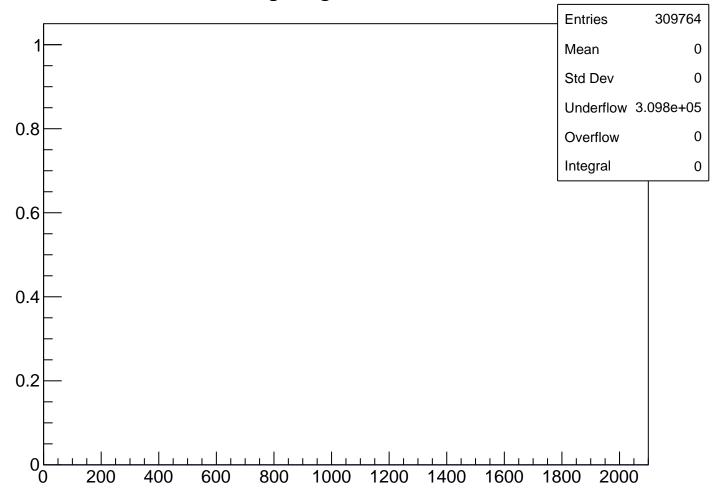
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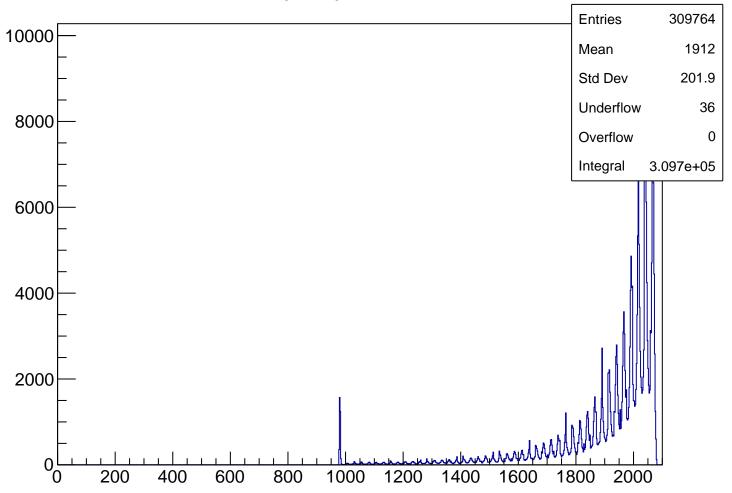
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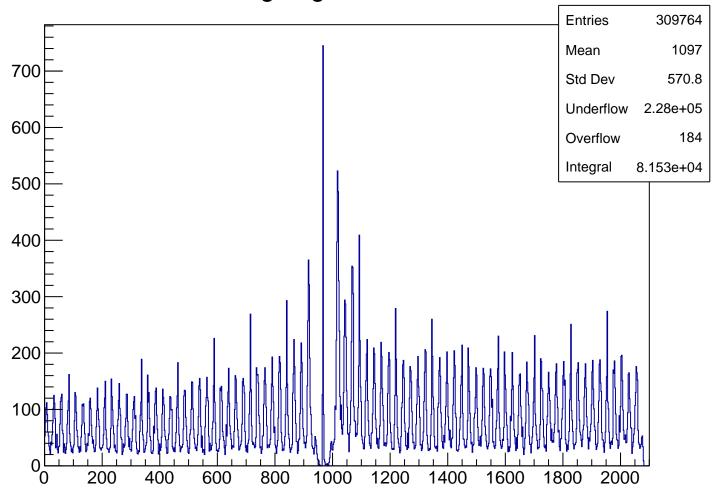
TrigFlag ElseOr



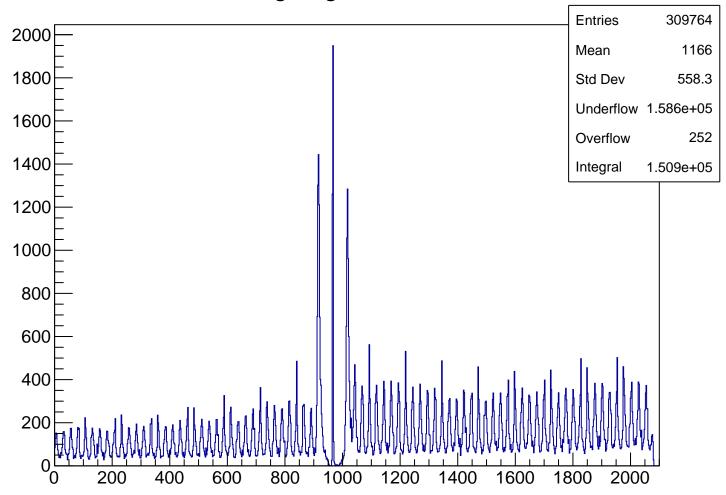
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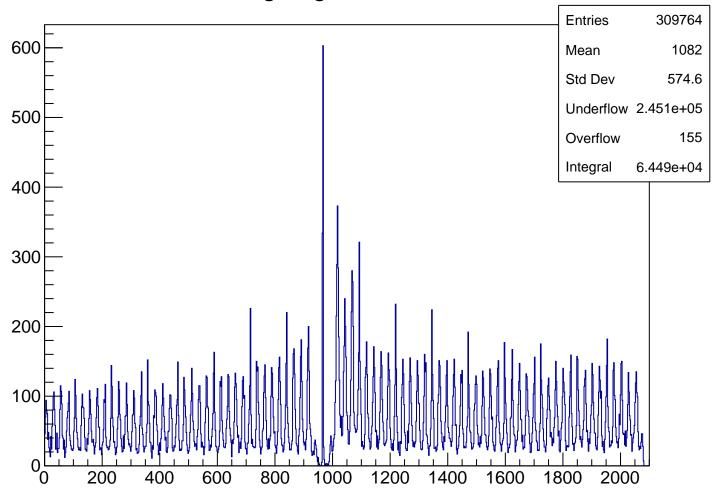
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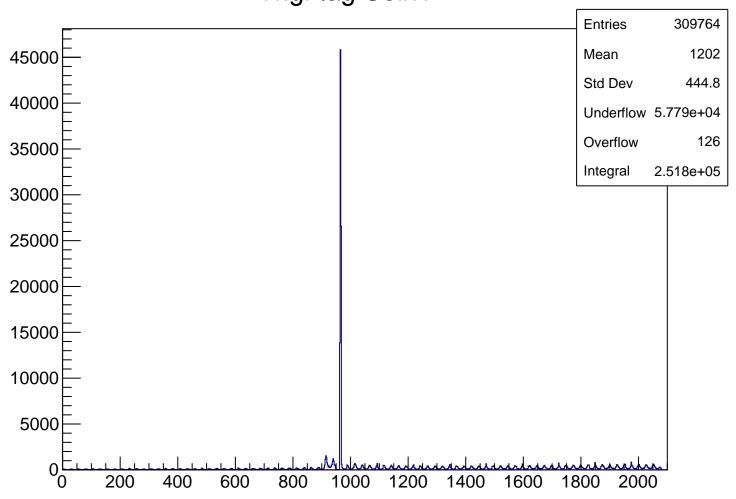
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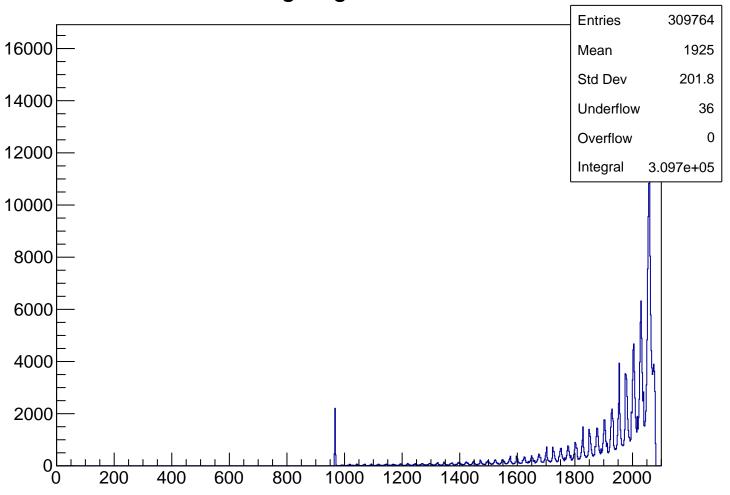
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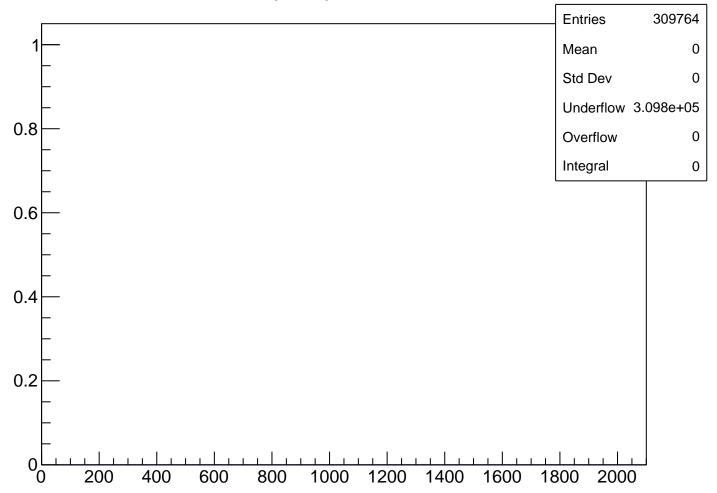
TrigFlag Coin1



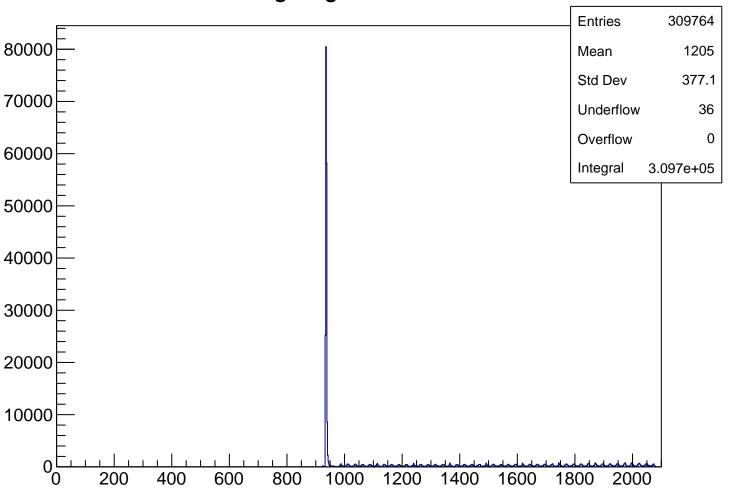
TrigFlag Coin2



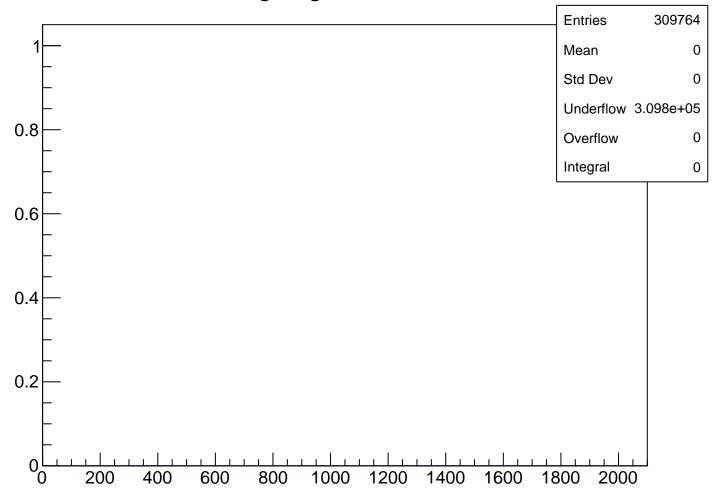
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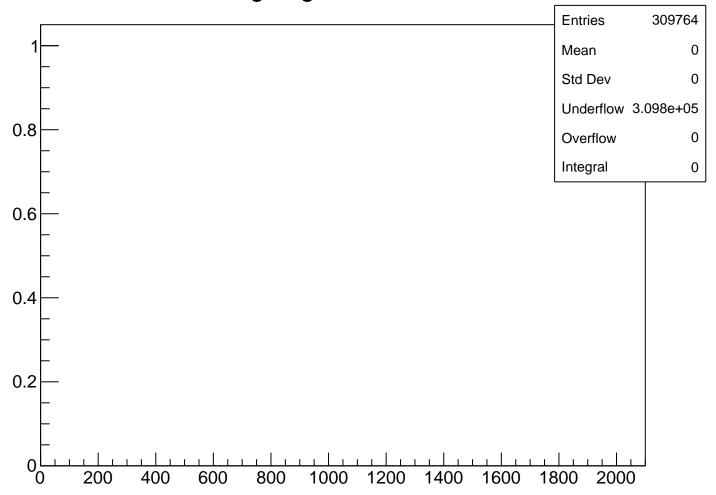
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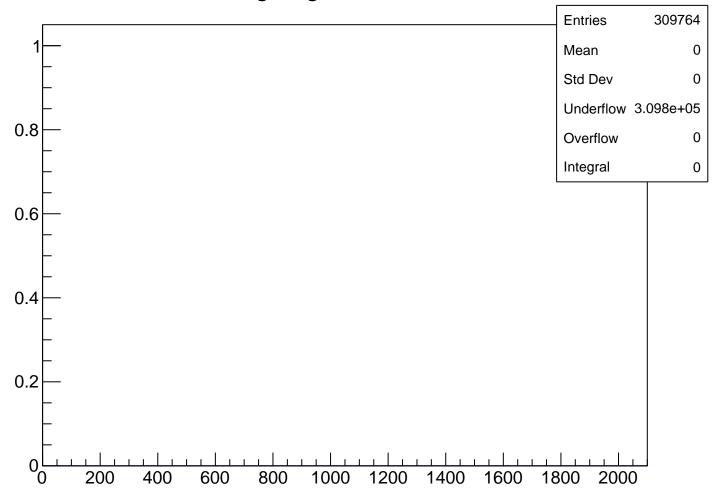
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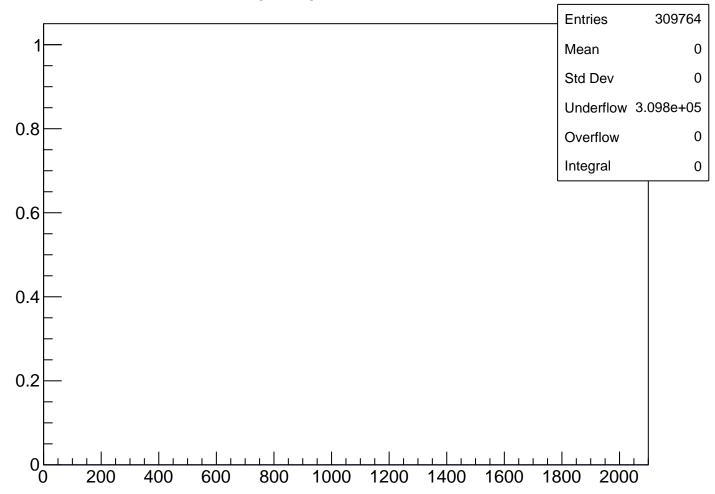
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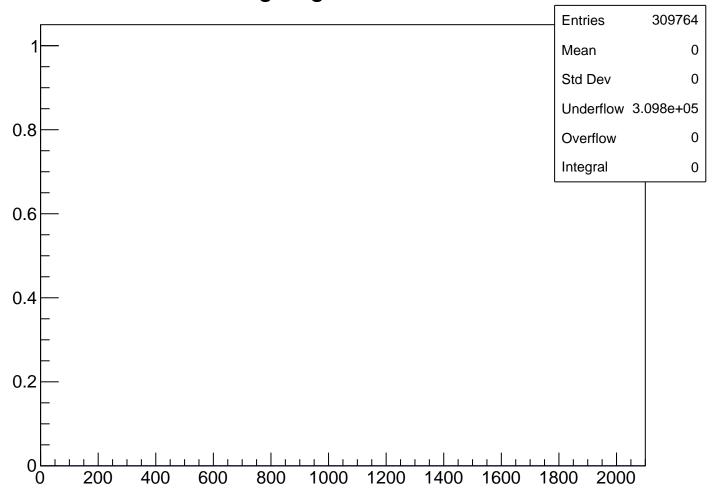
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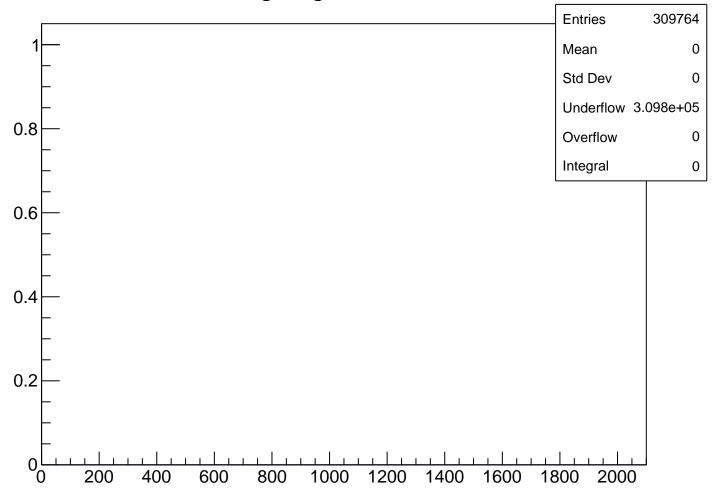
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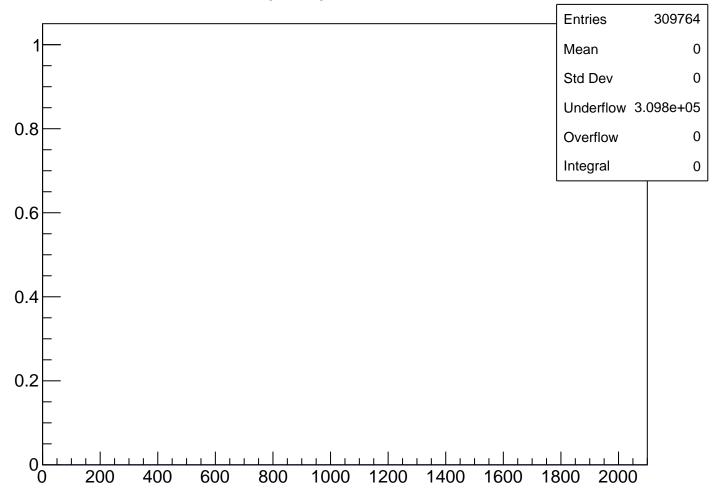
TrigFlag Coin1Ps



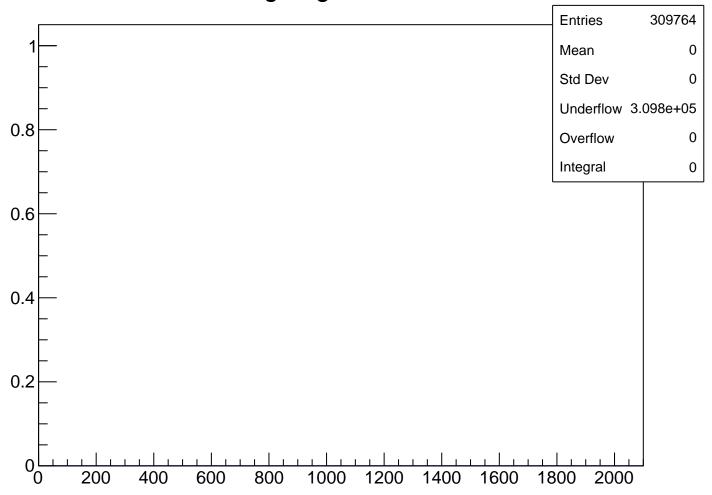
TrigFlag Coin2Ps



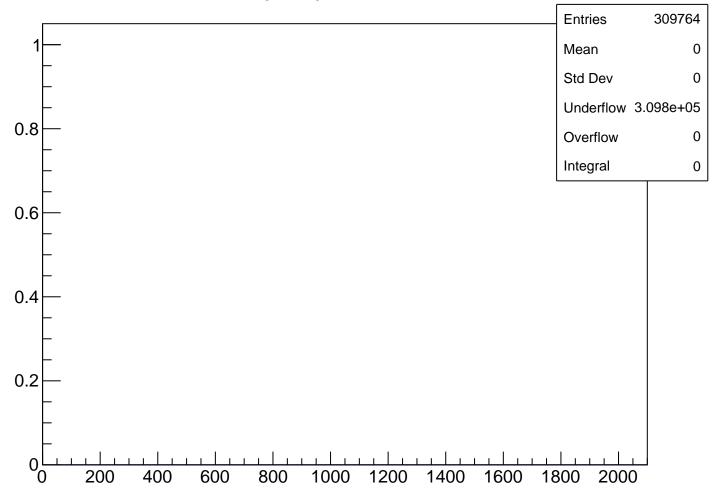
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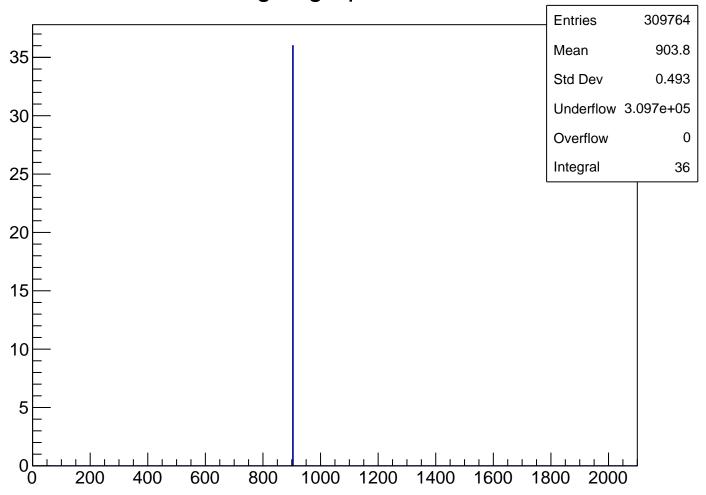
TrigFlag Clock



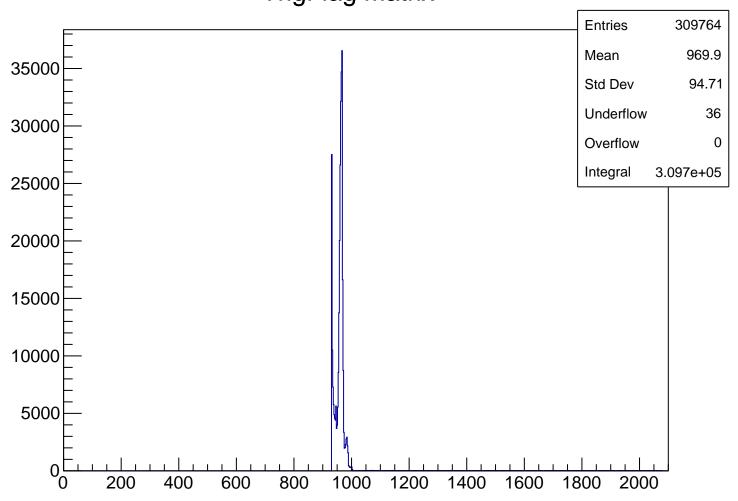
TrigFlag Reserve2



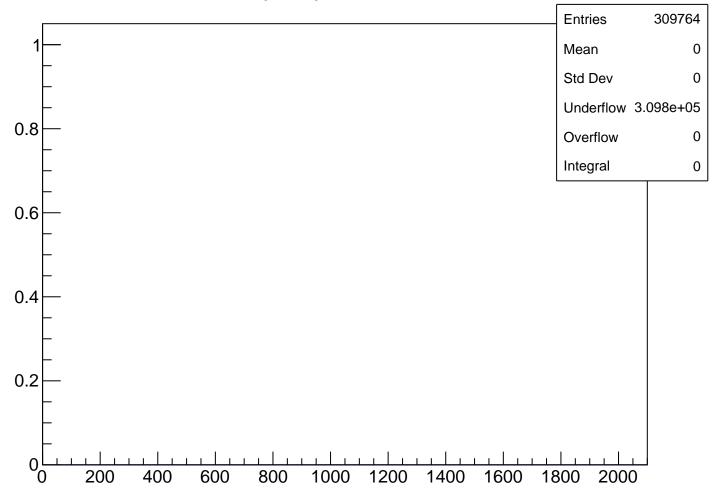
TrigFlag SpillEnd



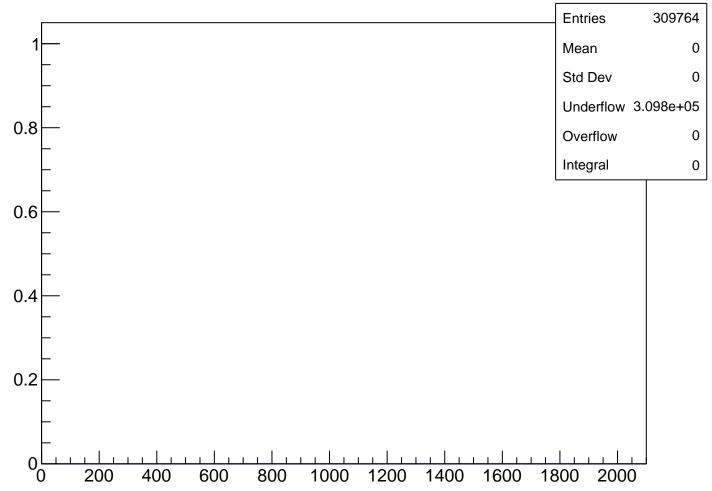
TrigFlag Matrix



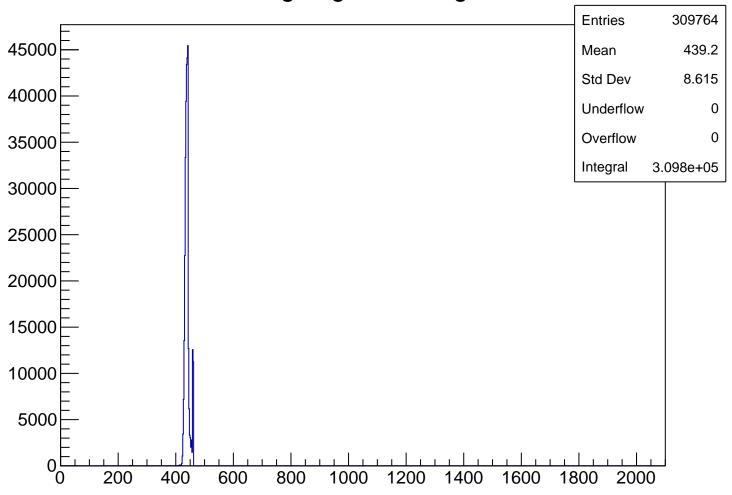
TrigFlag MstAccept



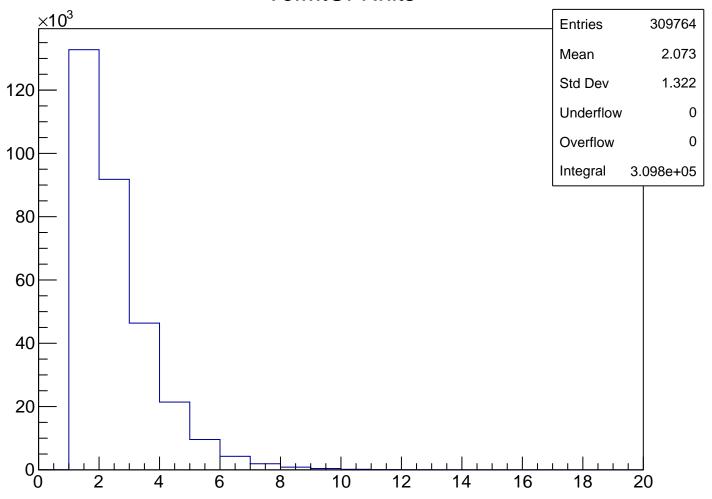
TrigFlag MstClear



TrigFlag TofTiming

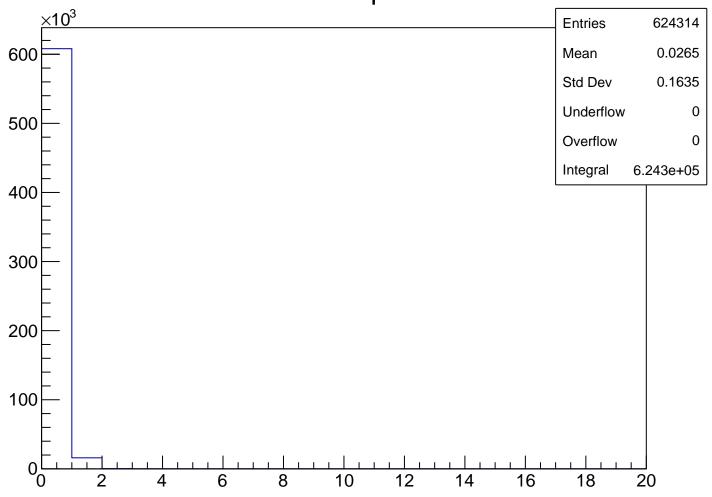


TofMtOr Nhits

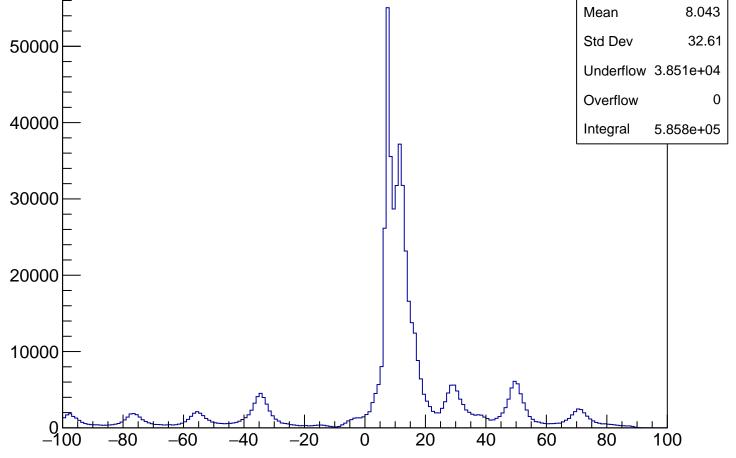


TofMtOr Hitpat **Entries** 11.68 Mean Std Dev 5.779 Underflow Overflow Integral 6.243e+05 0,

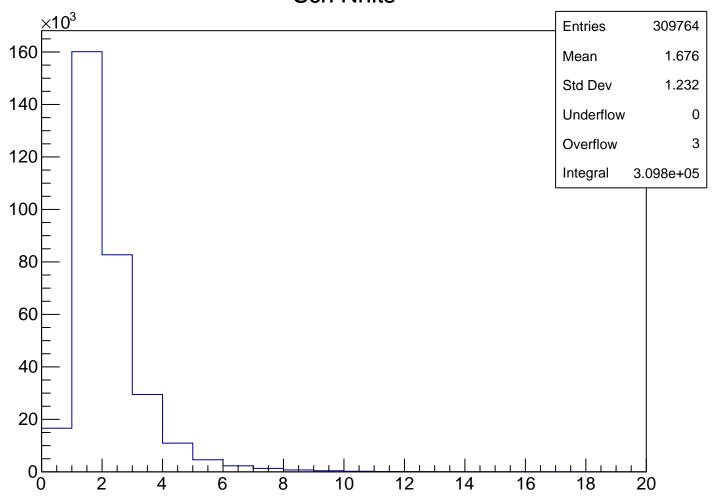
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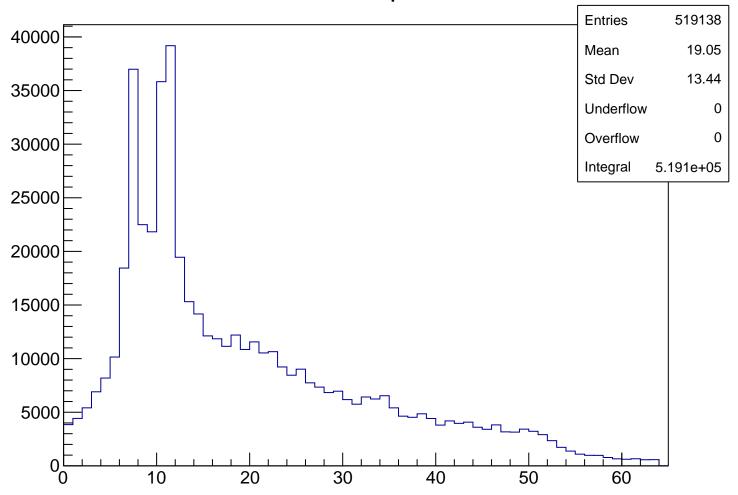
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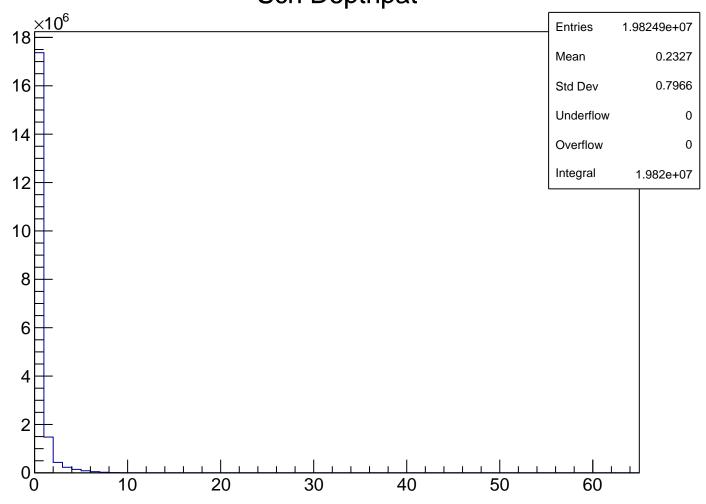
Sch Nhits



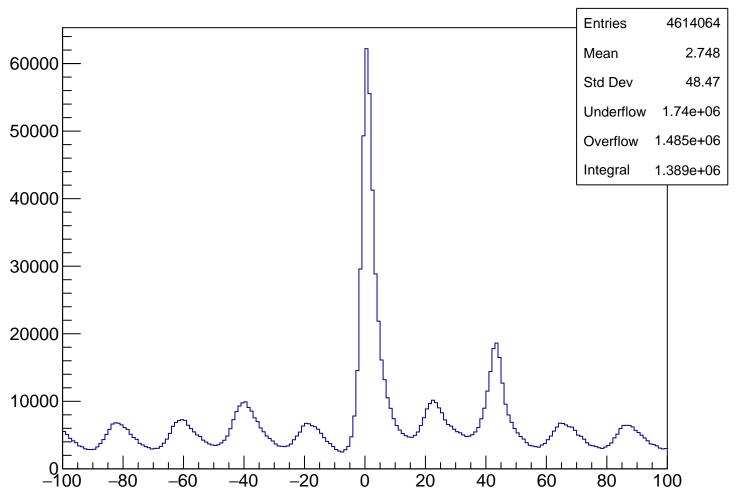
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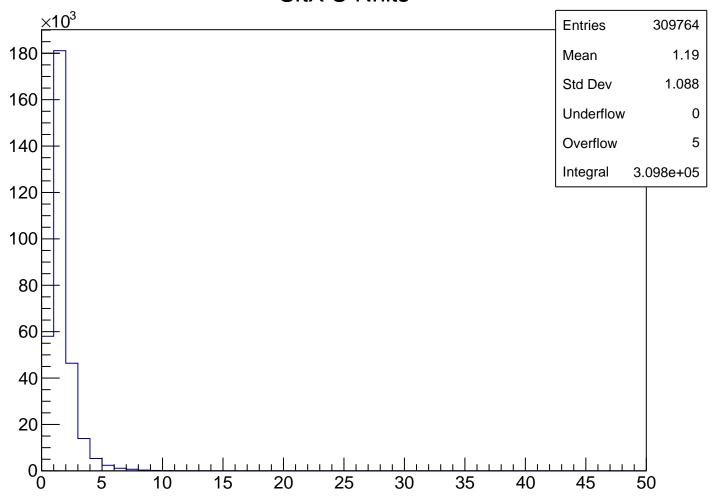
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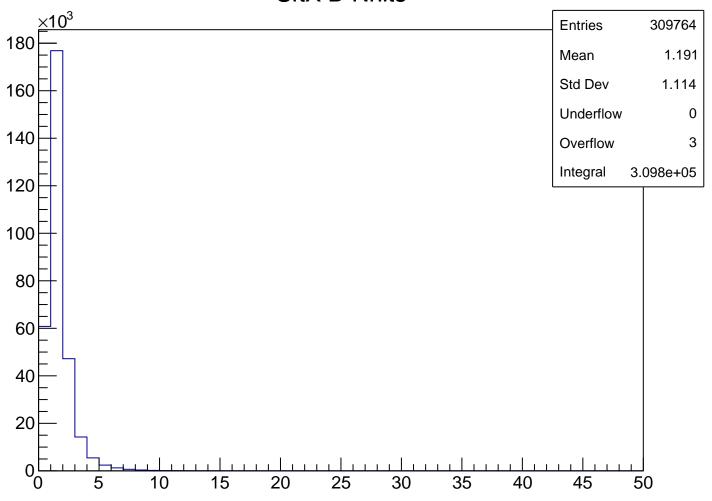
Sch Time



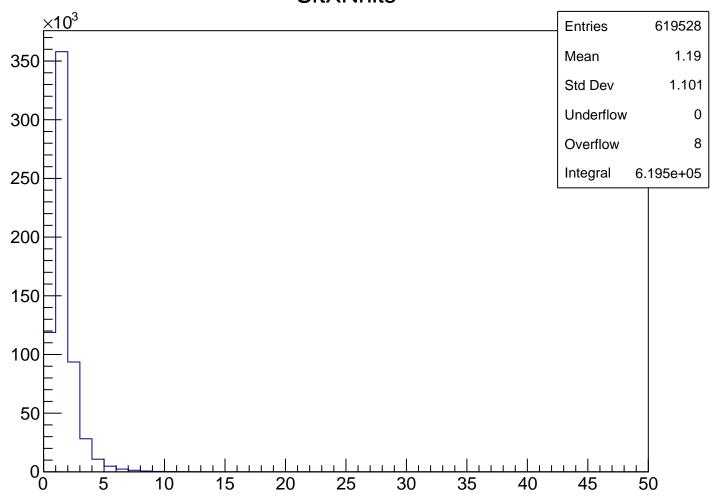
SftX U Nhits



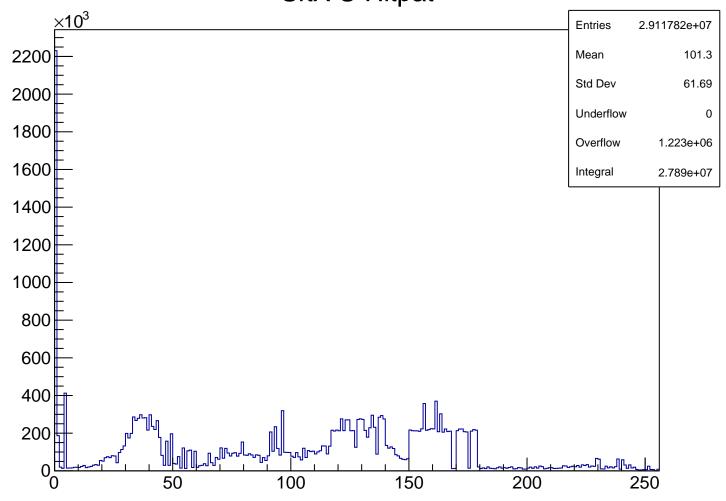
SftX D Nhits



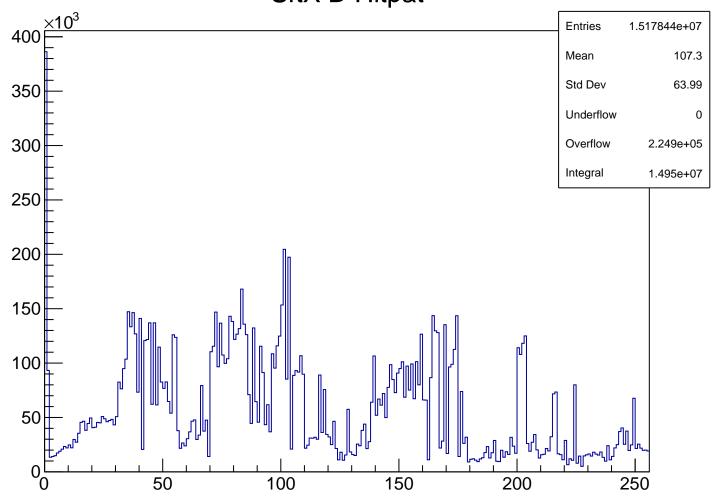
SftXNhits



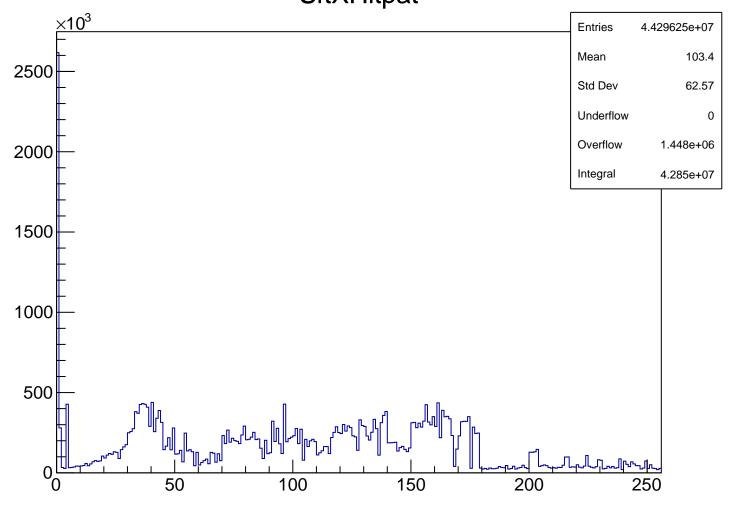
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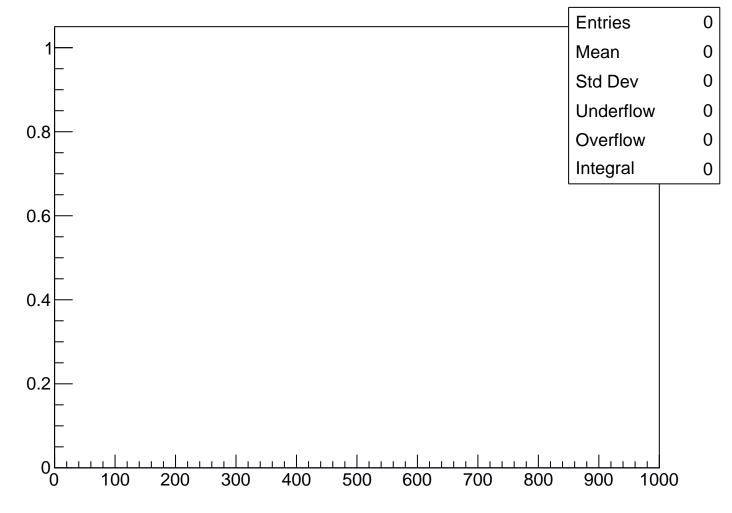
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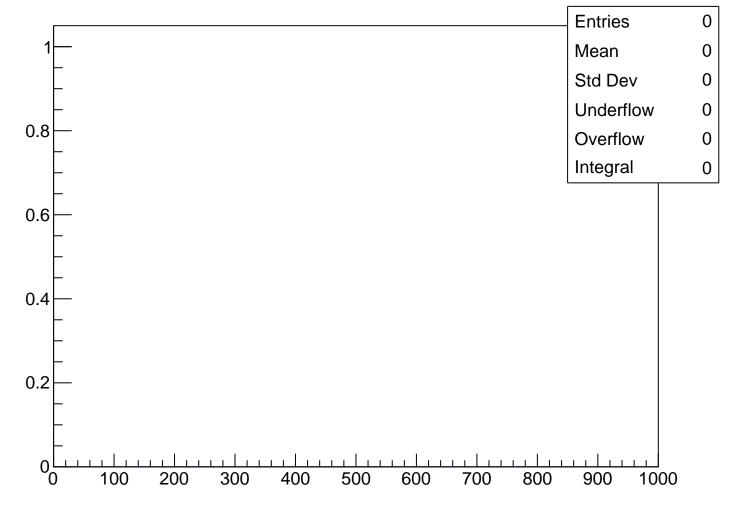
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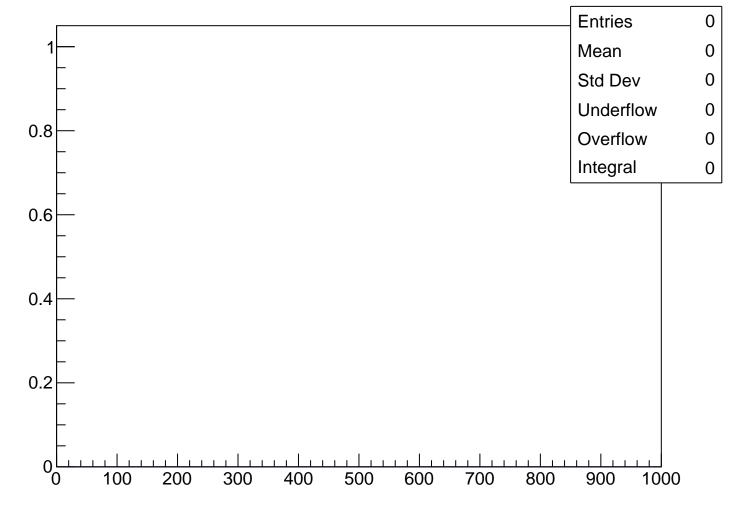
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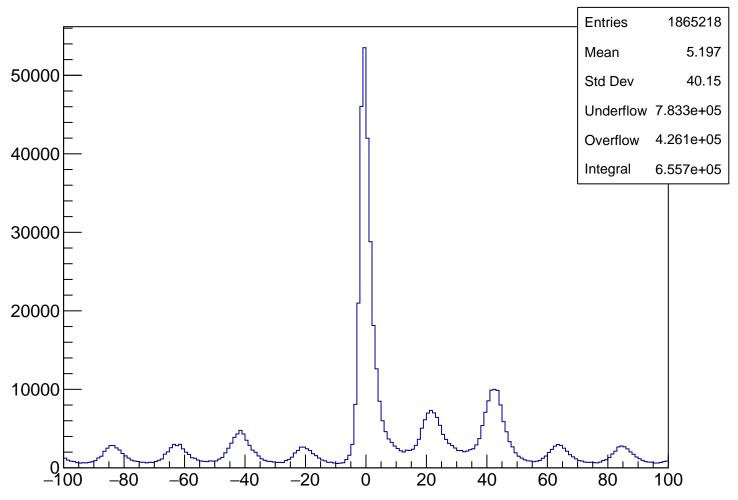
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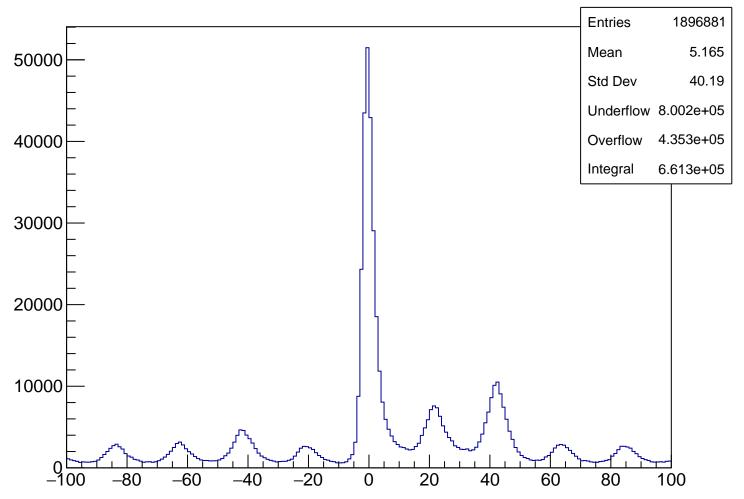
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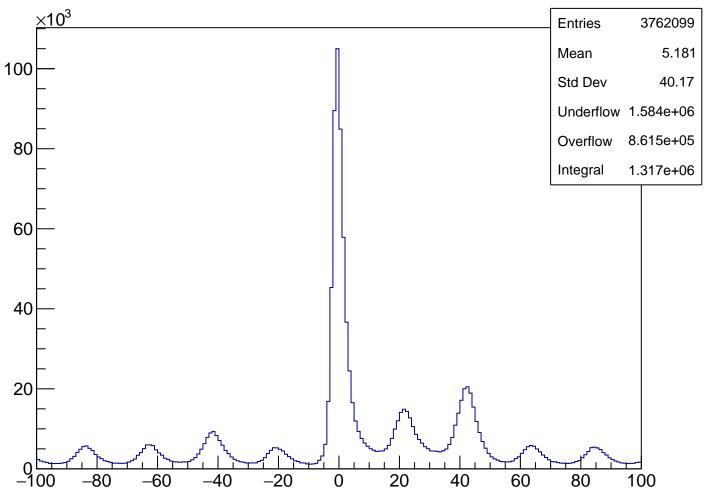
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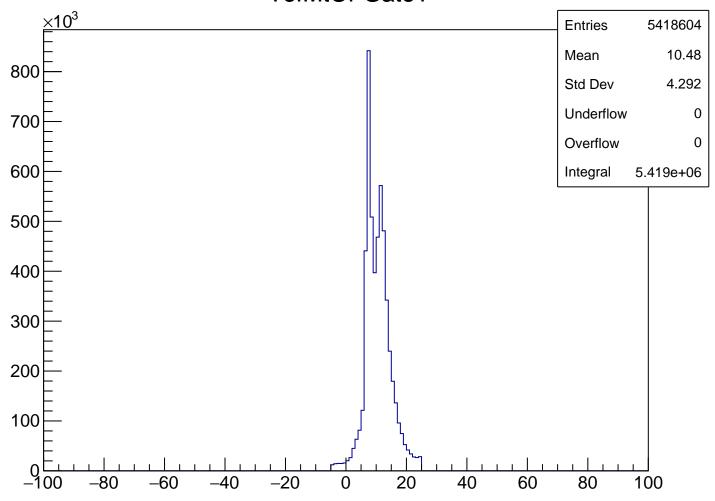
SftX D Time



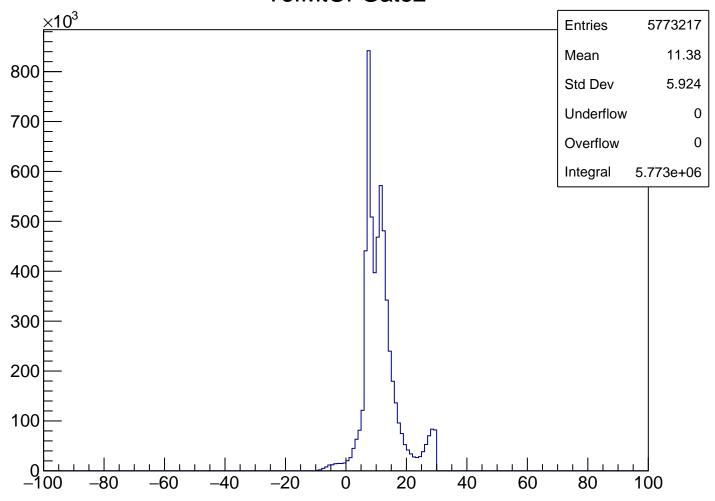
SftXTime



TofMtOr Gate1



TofMtOr Gate2



TofMtOr Gate3 ×10³ **Entries** 6159894 Mean 12.5 800 Std Dev 8.075 Underflow 0 700 Overflow 0 600 Integral 6.16e+06 500 400 300 200 100

20

40

60

80

100

0 -100

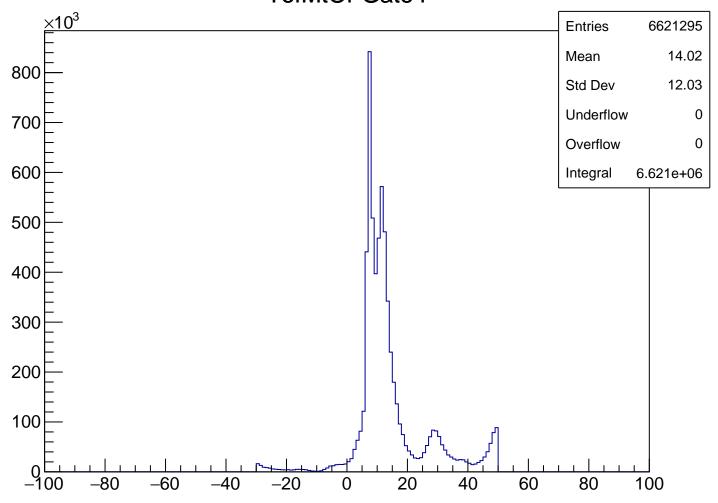
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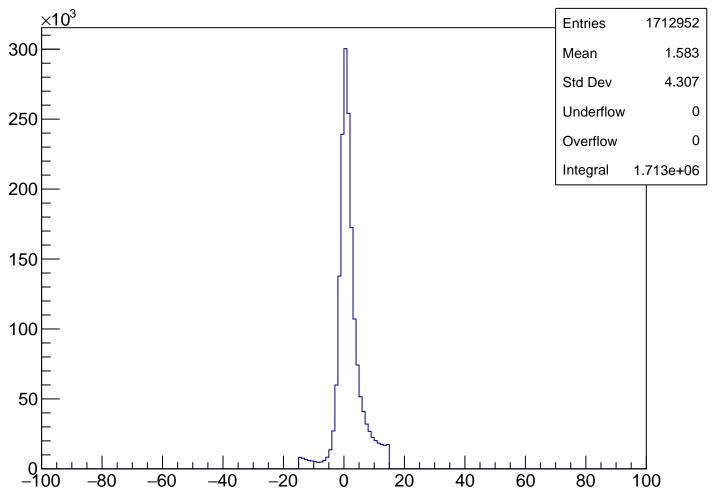
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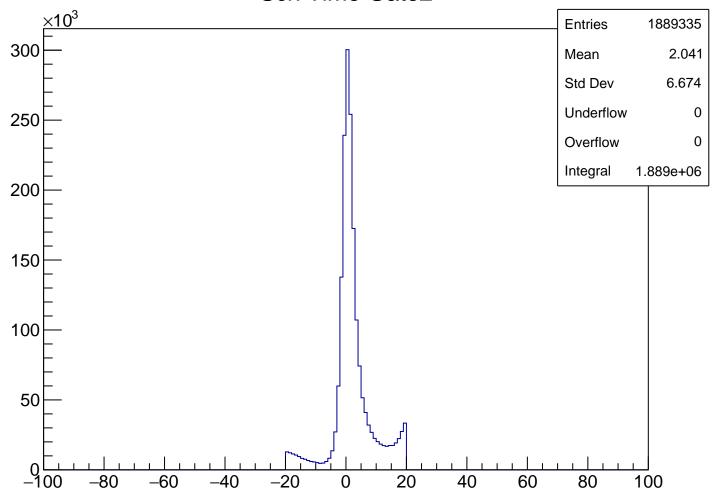
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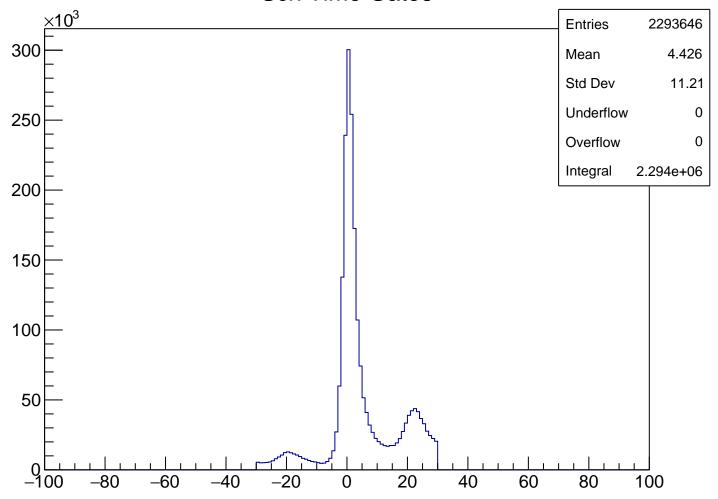
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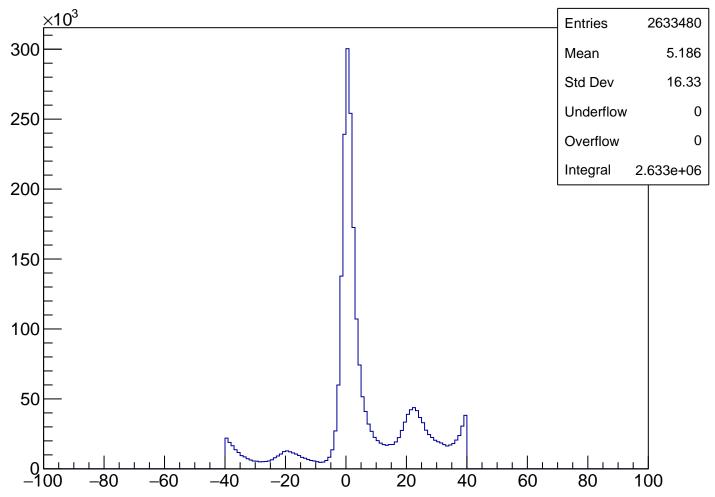
TofMtOr Gate4

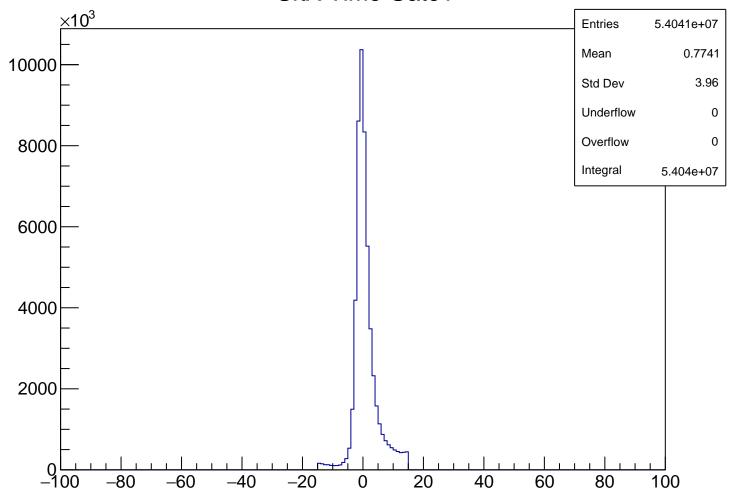


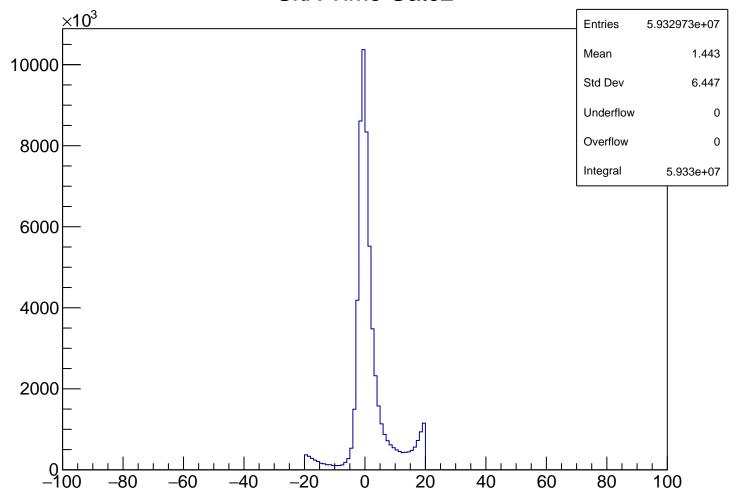


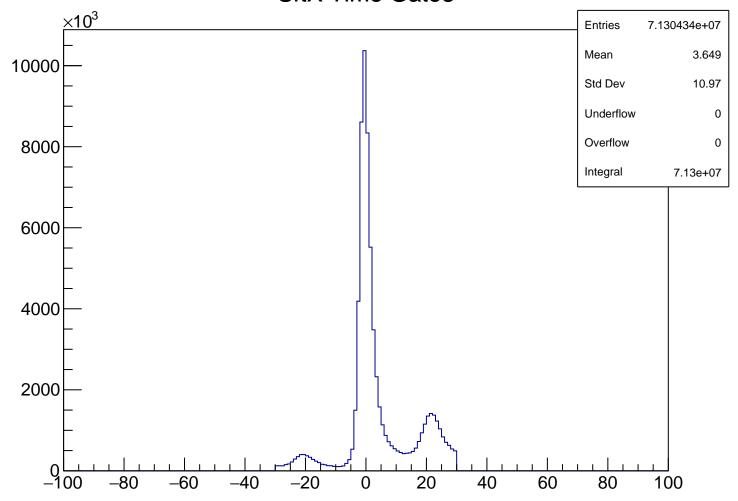


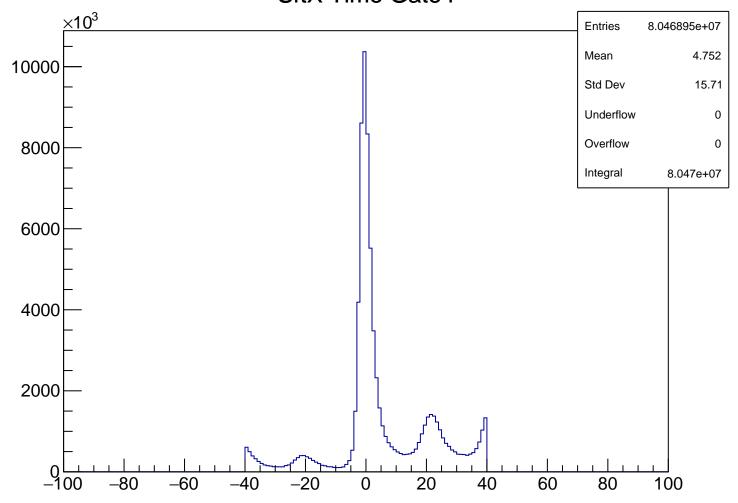




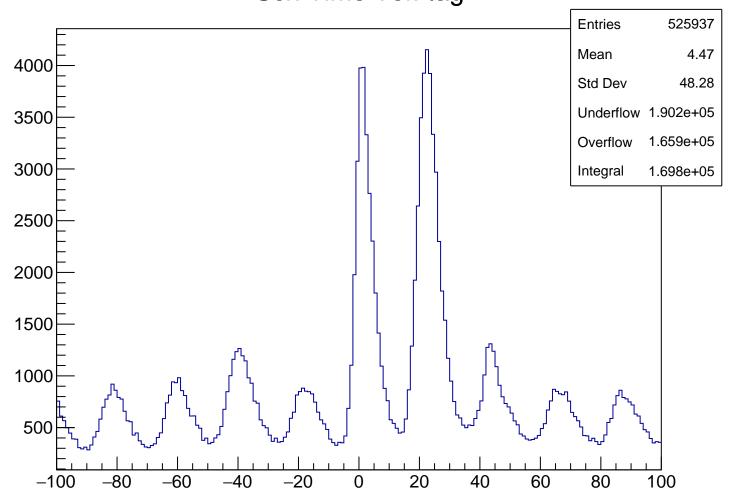








Sch Time TofFlag



SftX Time TofFlag

