ECEN 468/719   **Advanced Digital System Design**   Spring 2023

Homework 3   *Due 5:00 p.m., February 14, Tuesday*
Please submit through Canvas. *No late submission is accepted!*

(10 points) Consider the signal control example on page 2-5 of Lecture 4.
Please rewrite its SystemC code using dynamic processes for
`signals::sig_ctrl()` and `signals::red_sig()`.