1. Description

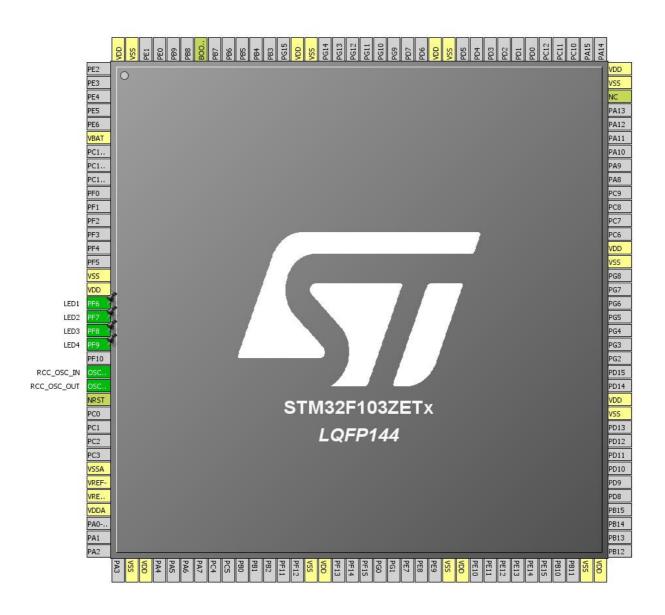
1.1. Project

Project Name	FreeRTOS_Timers
Board Name	FreeRTOS_Timers
Generated with:	STM32CubeMX 4.17.0
Date	12/08/2016

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103ZETx
MCU Package	LQFP144
MCU Pin number	144

2. Pinout Configuration

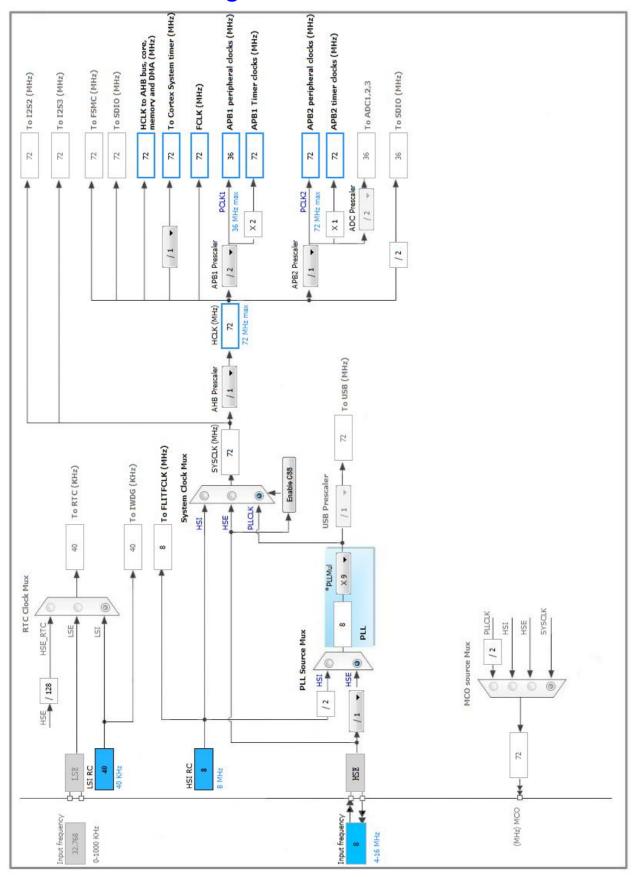


3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP144			Function(s)	
	reset)			
6	VBAT	Power		
16	VSS	Power		
17	VDD	Power		
18	PF6 *	I/O	GPIO_Output	LED1
19	PF7 *	I/O	GPIO_Output	LED2
20	PF8 *	I/O	GPIO_Output	LED3
21	PF9 *	I/O	GPIO_Output	LED4
23	OSC_IN	I/O	RCC_OSC_IN	
24	OSC_OUT	I/O	RCC_OSC_OUT	
25	NRST	Reset		
30	VSSA	Power		
31	VREF-	Power		
32	VREF+	Power		
33	VDDA	Power		
38	VSS	Power		
39	VDD	Power		
51	VSS	Power		
52	VDD	Power		
61	VSS	Power		
62	VDD	Power		
71	VSS	Power		
72	VDD	Power		
83	VSS	Power		
84	VDD	Power		
94	VSS	Power		
95	VDD	Power		
106	NC	NC		
107	VSS	Power		
108	VDD	Power		
120	VSS	Power		
121	VDD	Power		
130	VSS	Power		
131	VDD	Power		
138	BOOT0	Boot		
143	VSS	Power		
144	VDD	Power		

* The pin is affected with an I/O function		

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

5.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

5.2. SYS

Debug: No Debug

Timebase Source: TIM1

5.3. FREERTOS

mode: Enabled

5.3.1. Config parameters:

Versions:

CMSIS-RTOS version 1.02
FreeRTOS version 8.2.3

Kernel settings:

USE_PREEMPTION Enabled

CPU_CLOCK_HZ SystemCoreClock

 TICK_RATE_HZ
 1000

 MAX_PRIORITIES
 7

 MINIMAL_STACK_SIZE
 128

 MAX_TASK_NAME_LEN
 16

 USE_16_BIT_TICKS
 Disabled

IDLE_SHOULD_YIELD Enabled Enabled USE_MUTEXES USE_RECURSIVE_MUTEXES Disabled USE_COUNTING_SEMAPHORES Disabled QUEUE_REGISTRY_SIZE Disabled USE_APPLICATION_TASK_TAG 3072 TOTAL_HEAP_SIZE Memory Management scheme heap_4 Disabled USE_ALTERNATIVE_API Enabled ENABLE_BACKWARD_COMPATIBILITY Disabled USE_PORT_OPTIMISED_TASK_SELECTION Disabled USE_TICKLESS_IDLE Enabled USE_TASK_NOTIFICATIONS

Hook function related definitions:

USE_IDLE_HOOK Disabled
USE_TICK_HOOK Disabled
USE_MALLOC_FAILED_HOOK Disabled
CHECK_FOR_STACK_OVERFLOW Disabled

Run time and task stats gathering related definitions:

USE_TRACE_FACILITY Enabled
GENERATE_RUN_TIME_STATS Disabled

Co-routine related definitions:

USE_CO_ROUTINES Disabled MAX_CO_ROUTINE_PRIORITIES 2

Software timer definitions:

USE_TIMERS Enabled *
TIMER_TASK_PRIORITY 2

TIMER_QUEUE_LENGTH 10
TIMER_TASK_STACK_DEPTH 256

Interrupt nesting behaviour configuration:

LIBRARY_LOWEST_INTERRUPT_PRIORITY 15
LIBRARY_MAX_SYSCALL_INTERRUPT_PRIORITY 5

5.3.2. Include parameters:

Include definitions:

vTaskPrioritySet Enabled
uxTaskPriorityGet Enabled
vTaskDelete Enabled
vTaskCleanUpResources Disabled
vTaskSuspend Enabled

vTaskDelayUntil	Disabled
vTaskDelay	Enabled
xTaskGetSchedulerState	Enabled
xTaskResumeFromISR	Enabled
xQueueGetMutexHolder	Disabled
xSemaphoreGetMutexHolder	Disabled
pcTaskGetTaskName	Disabled
uxTaskGetStackHighWaterMark	Disabled
xTaskGetCurrentTaskHandle	Disabled
eTaskGetState	Disabled
xEventGroupSetBitFromISR	Disabled
xTimerPendFunctionCall	Disabled

^{*} User modified value

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max	User Label
				down	Speed	
RCC	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
GPIO	PF6	GPIO_Output	Output Push Pull	n/a	Low	LED1
	PF7	GPIO_Output	Output Push Pull	n/a	Low	LED2
	PF8	GPIO_Output	Output Push Pull	n/a	Low	LED3
	PF9	GPIO_Output	Output Push Pull	n/a	Low	LED4

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable Preenmption Priority SubPriority		SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true 0 0		0
System service call via SWI instruction	true 0 0		0
Debug monitor	true 0 0		0
Pendable request for system service	true 15 0		0
System tick timer	true 15 0		
TIM1 update interrupt	true 0 0		0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		

^{*} User modified value

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
мси	STM32F103ZETx
Datasheet	14611_Rev12

7.2. Parameter Selection

Temperature	25
Vdd	3.3

8. Software Project

8.1. Project Settings

Name	Value
Project Name	FreeRTOS_Timers
Project Folder	D:\Open103Z-Demo-
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F1 V1.4.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	