

Analog IC Lab #1

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1 Simulation Results

In this section, we present the results.

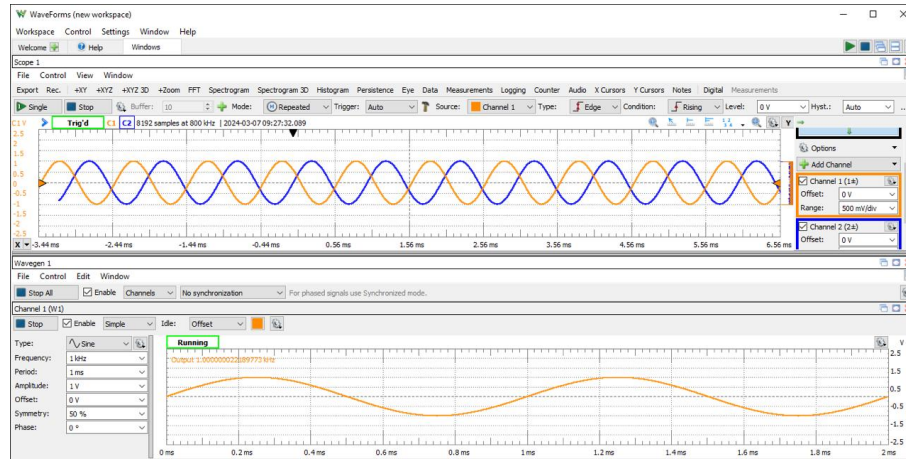


Figure 1: The simulation plot with unity gain

As shown in Figure 2, the data points are densely concentrated in the lower left corner of the plot.



Figure 2: The simulation plot with gain of 2

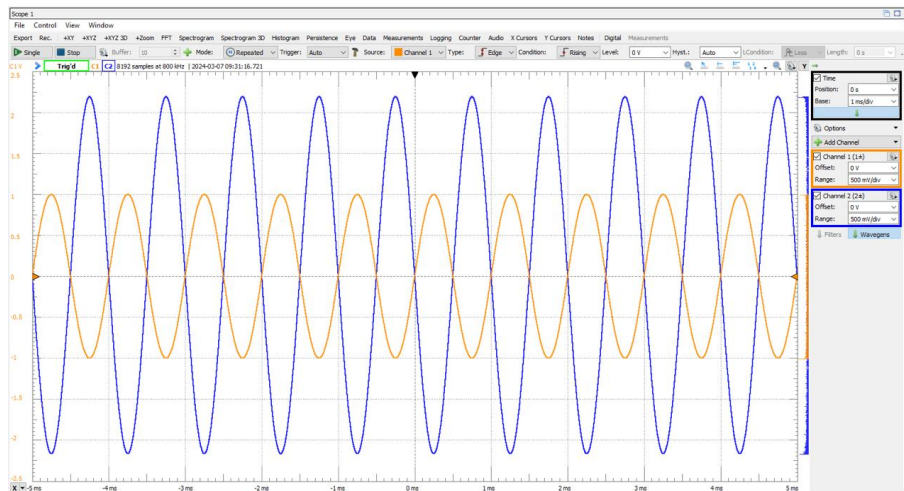


Figure 3: The simulation plot with gain of 2.4