



TRACEABILITY REPORT

Elevator System

Group 16

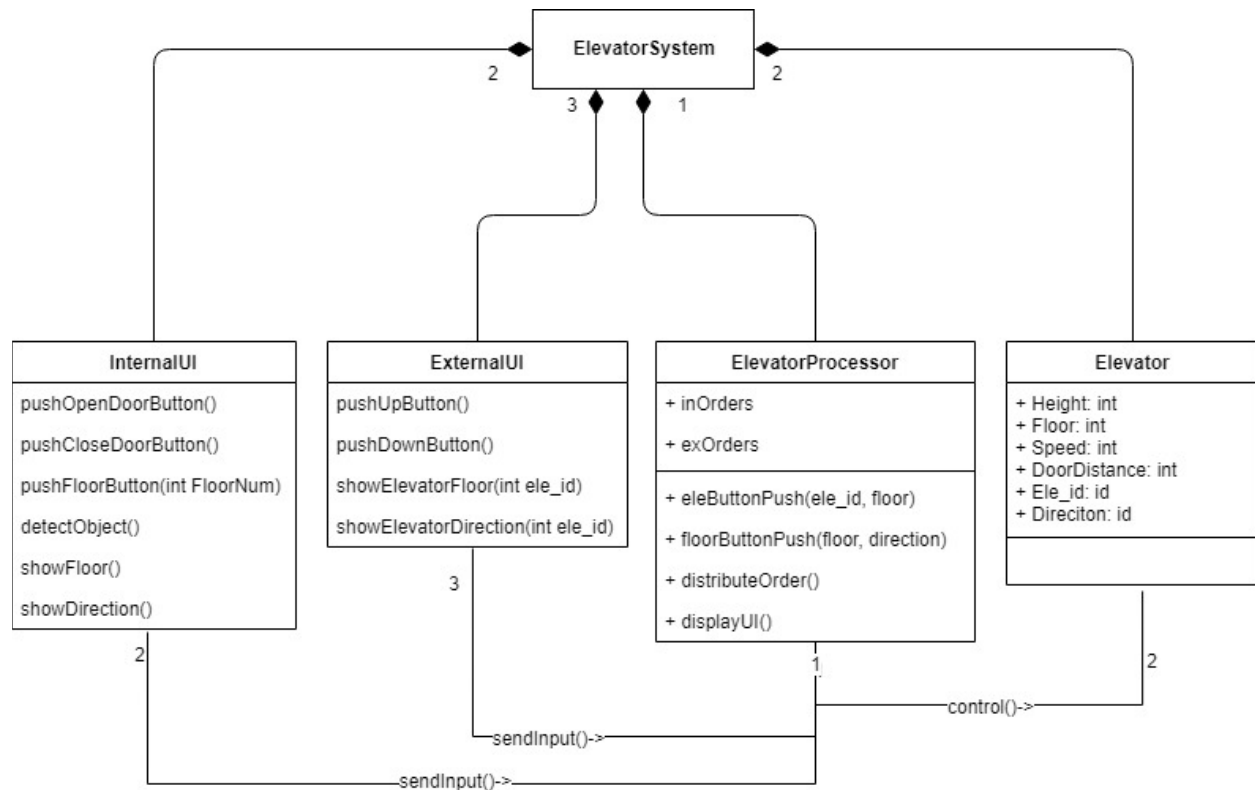
Author: Haoyi Wu

Table of Contents

System Architecture.....	2
Software Traceability	2

System Architecture

The system architecture is shown below:



Software Traceability

	Is implemented by	Is validated by
R1 (External UI)	S1.1.6, S2	T3.6
R1.1	S2.1	T3.6.4
R1.2	S2	T3.6.3
R1.3	S1.1.6	T3.6
R1.3.1	S1.1.6	T3.6.3
R1.3.2	S1.1.6	T3.6.5
R2 (Internal UI)	S1	T2.1, T3
R2.1	S1	T2.1, T3
R2.1.1	S1.1.1	T2.1.3, T3.4
R2.1.2	S1.2.1	T2.1.1, T3.1
R2.1.3	S1.2.2	T2.1.5, T3.2
R2.2	S1.1.2	T3.5
R2.3	S1	T3.6
R2.3.1	S1.1.6	T3.6.3
R2.3.2	S1.1.6	T3.6.5
R3 (Processor)	S1.1, S1.2, S2.1.2	T2, T3
R3.1	S1.1, S2.1.2	T2.2, T3

R3.1.1	S1.1.3, S1.1.4, S1.1.5, S2.1.2	T3.4, T3.7
R3.1.2	S2.1.2	T3.8.2
R3.1.3	S2.1.2	T3.8.1
R3.1.4	S2.1.2	T2.2
R3.2	S1.2	T3
R3.2.1	S1.2.1	T3.3
R3.2.2	S1.2	T3.2.1
R3.2.3	S1.2.2	T3.2.2