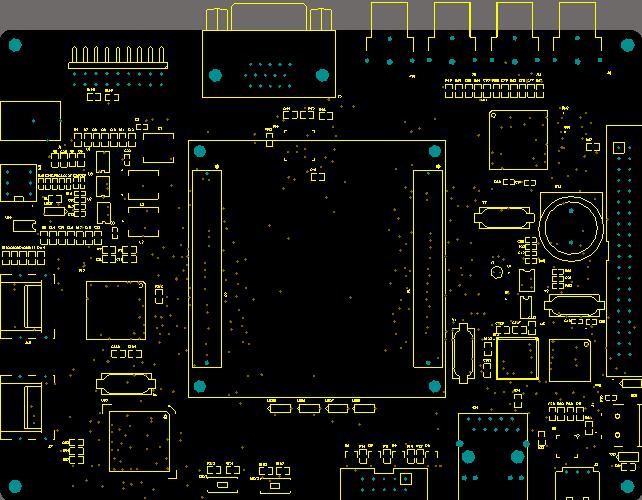
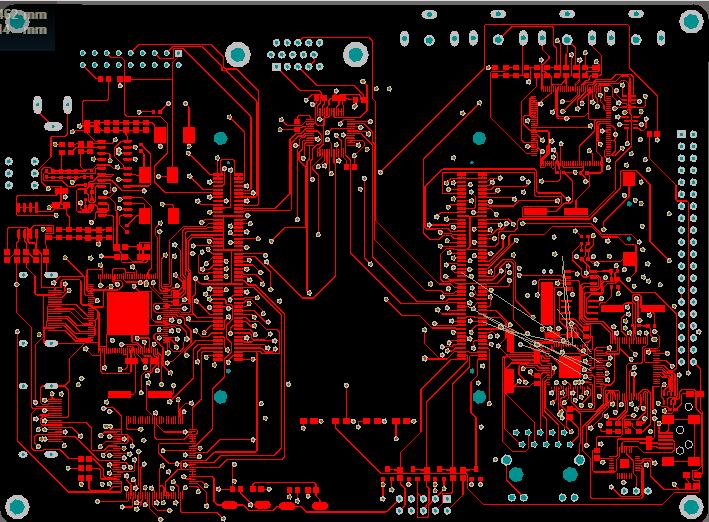
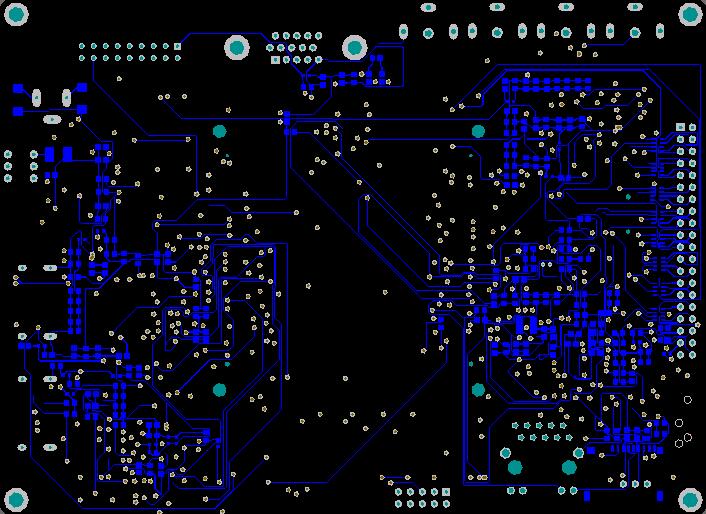
**附录A：原理图与PCB图**



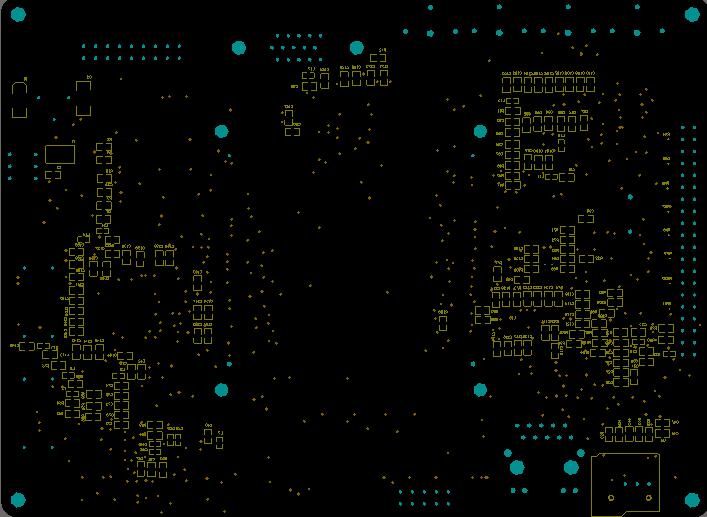
**图1 顶部器件层**



**图2 电源走线层**



**图3 地层**



**图4 底部器件层**