Advanced



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Tsinghua University
2015



Who?

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 - Interests:
 - Operating Sytems
- TA: Qixue Xiao, Junjie Mao,



- The Operating System (OS) I use has already been written, and I doubt it will be my job to write another one. For example, Windows, Linux.
- Haven't OS developers figured everything out already? What more is there to do?
- Why should I study this as a graduate student?



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- Why should I study this as a graduate student?

OS is cool!
OS is important!
OS is challenging!

You want to be involved!





Objectives

- Gain experience in doing OS research
 - Know how to read/write papers/reports



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 - Know current OS hot topics
 - Develop and test OS projects



Course Materials

- Lecture notes, Papers & Projects
- No required textbooks



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- No required textbooks

Reference Books

- Wolfgang Mauerer, Professional Linux Kernel Architecture
- Uresh Vahalia, UNIX Internals-- The New Frontiers
- Daniel P. Bovet, Marco Cesati, Understanding the Linux Kernel,
- Mark E. Russinovich, David A. Solomon, Microsoft Windows? Internals
- Tanenbaum, Modern Operating Systems
- Andrew S. Tanenbaum, Distributed Operating Systems



Week	Lectures (includes some invited talks, Interviews)				
1	Course overview + Speed+Security+Correct				
2	OS Architecture				
3	Virtual Machine				
4	Building Real OS—uCore, Linux				
5	Overview of OS Optimization for Multicore				
6	Paper&Project: The Scalable Commutativity Rule & Commuter				
7	Paper&Project: TCP/IP & FastSocket				
8	Midterm Exam				
9	Overview of OS Security				
10	Paper&Project: Integer Security for System & Kint				
11	Paper&Project: Lang, Library, Device Driver & RUST ,KLEE, PF-				
	Miner				
12	Overview of Correction on System				
13	Paper&Project: Verified OS & SeL4				
14	Paper&Project: Verified Component & JITK				
15-16	Final Exam				



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4	Building Real OS—uCore, Linux				



Week	Lectures (includes some invited talks, Interviews)				
	Overview of OS Optimization for				
5	Overview of OS Optimization for				
	Multicore				
6	Paper&Project: The Scalable				
	Commutativity Rule & Commuter				
7	Paper&Project: TCP/IP & FastSocket				
8	Midterm Exam				



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10	Paper&Project: Integer Security for			
	System & Kint			
11	Paper&Project: Lang, Library, Device			
	Driver & RUST ,KLEE, PF-Miner			



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14	Paper&Project: Verified Component &				
	JITK				
15-	Final Exam				
16					



Reading Grading

- 6 Papers Summaries and critiques 60%
 - include report doc(4+ or 4000 words inA4 pages) & presentation (15+ pages)
 - Research Areas
 - OS/VMM Architectures
 - Performance/Multicore
 - Security/Find Bug
 - Correction/Verification



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 - The best papers are from below conferences: SOSP,
 OSDI, EuroSys, RTSS, NSDI, FAST, USENIX ATC, VEE, MobiSys,
 PPoPP, SC, ICS, PLDI, SenSys, SIGMETRICS, ISCA, HPCA,
 HPDC, Micro, ASPLOS, IPDPS, SIGMOD, SIGCOMM, PerCom,
 WWW, Cluster, CCGrid, S&P, CCS, SECURITY, NDSS,...
 (1996~2015)



Reading Critiques

- Need to address the following:
 - Summary of major innovations
 - What the problems the paper mentioned?
 - How about the important related works/papers?
 - What are some intriguing aspects of the paper?



Reading Critiques

- Need to address the following:
 - Summary of major innovations
 - What the problems the paper mentioned?
 - How about the important related works/papers?
 - What are some intriguing aspects of the paper?
 - How to test/compare/analyze the results?
 - How can the research be improved?
 - If you write this paper, then how would you do?



Project Grading

- One Project 40%
 - didn't finish ucore/xv6/jos labs
 - Analyze/Testing/Improving ucore OS
 - https://github.com/chyyuu/ucore



Project Grading

- One Project 40%
 - didn't finish ucore/xv6/jos labs
 - Analyze/Testing/Improving ucore OS
 - https://github.com/chyyuu/ucore_lab
 - already finished ucore/xv6/jos labs
 - Do one OS related project and write the final report
 - research areas
 - There are a project lists
 - Discuss with me



Project Proposal

- Due on the 2nd~3rd week
- 2-page written proposal
 - team members (<=3)</pre>
 - Motivation
 - The state-of-the-art Methodology
 - Expected results
 - Timeline
 - Division of labor among teams
 - Some references



Project Midterm/Final Exam

- Needed
 - 10-15 minutes Presentation
 - Midterm
 - 3~10 page written paper/report (double column, single-space, 10-pt font)
 - Final
 - 5~15 page written paper/ project report (double column, single-space, 10-pt font)



Other Choose

- I can not understand the paper, my coding ability is poor
 - Need to talk with me ASAP



OS Overview



History of OS: Change!

		1980	2009	Factor
Speed	CPU	1 MIPS	77,000 MIPS	7.7 x 10 ⁵
	Memory	500 ns	0.9 ns	1.8 x 10 ³
	Disk	18 ms	4 ms	2.2 x 10 ¹
	Network	300 b/sec	10 Gb/sec	3.3 x 10 ⁷
Capacity	Memory	64 KB	8 GB	1.3 x 10 ⁵
	Disk	1 MB	2 TB	2.0 x 10 ⁶
Cost	Per MIP	\$100K/MIP	\$.007899	1.0 x 10 ⁷
Other	Address bits	8	64	8
	Users/machine	10s	0.1	1.0 x 10 ⁻²



Changing Roles of the OS

- What OS does depends on
 - -available hardware and software
 - -changing uses of machines
 - -changing expectations of users



OS Concepts

- Single-Machine OS/VMM
 - Memory management
 - -Process management
 - Synchronization
 - -File systems and device support
- Security
- Correction
- Distributed System



Single-Machine OS/VMM

Purposes

- Clean virtual machine
- Hardware independence
- Resource sharing and management
- Persistent DataStorage
- Protection
- Real time support
- Parallelism



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Strategy

- •How do we organize the OS effectively for development, evolution, performance, and security?
- •How do we use multiprocessor machines effectively?



Memory Management

Purposes

- Virtual memory: provides the illusion of infinite physical memory
- Swapping: moves processes to disk as necessary
- Paging: allows processes to run with only the active pages in memory
- Buffer Cache: speedup the IO access



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Strategy

- How do we coordinate machines to share memory?
- How can we simplify memory management as memory becomes abundant?



Process Management

- Thread:
- Address space:
- Process:



Process Management

- Thread: A sequential execution stream
- Address space: Chunks of memory and everything needed to run a program
- Process: An address space + thread(s)



Process Management

Purposes

- Thread: A sequential execution stream
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Strategy

- How do processes communicate and share states efficiently and securely on the same machine?
- How do we improve the computing process model?



Process Scheduling

Purposes

- Provides the illusion of multiple processes running at the same time on a single processor
- Context switching: changing the attention of the processor
 - Involves saving and restoring states
 - Necessary to cross kernel boundary



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Strategy

- How do we achieve fairness, high throughput, and responsiveness at the same time?
- How do we reduce or avoid the cost of context switching?



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Synchronization

Purposes

 Provides correct execution or coordinating threads in the face of arbitrary context switching



Synchronization

Purposes

 Provides correct execution or coordinating threads in the face of arbitrary context switching

Strategy

- Atomic actions: all or nothing
- Mutual exclusion: one thread in the critical section at a time
- Semaphores: atomic, counter-based locks
- Avoid Deadlock: circular waiting on resources



File Systems

- Purposes
- File: data + attributes
- File system services:
 - Organization
 - Naming
 - Access
 - Synchronization
 - Protection and security



File Systems

Purposes

- File: data + attributes
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Strategy

- How do we make different file systems work together, even across machines?
- How do we provide consistency, availability, and reliability to copies of a file across multiple machines?
- How do we handle very large data sets?



I/O Device

Purposes

- I/O devices tend to be a lot slower than memory speed
- Caching: stores
 extra data in
 memory in hope of
 near-term reuse



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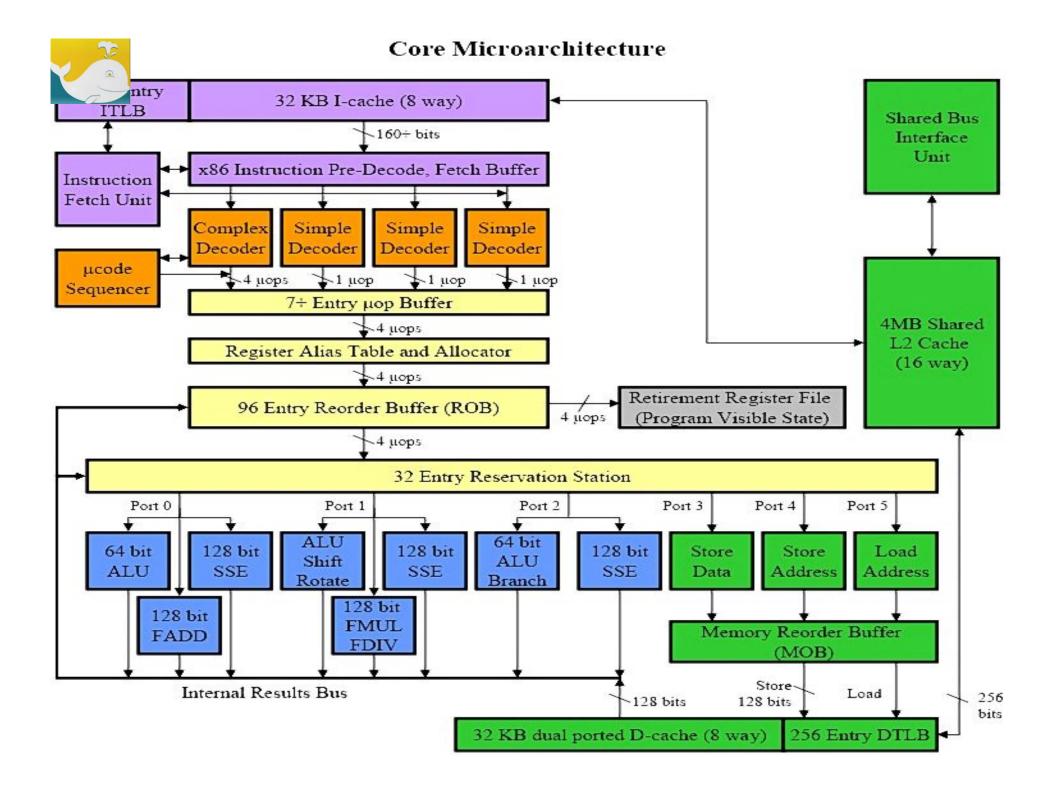
Strategy

- How do we coordinate the memory resources across machines to enhance performance?
- How do we handle new devices with new characteristics?



Tendency

- MultiCore
- Security
- Correction





Multi-Core Challenges

Today's Commodity Multi-Cores

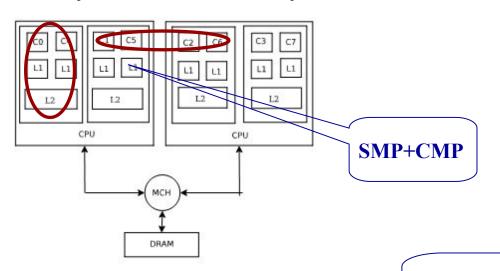


Fig. 1. The architecture of Intel's multi-core platform used in experiments. Squares with C tag represent cores, squares with LI tag represent L1 instruction cache and L1 data cache, rectangles with L2 represent L2 unified cache, rectangle with DRAM represents memory, circle with MCH represents memory controller hub, lines with arrow represent directly accessible relationship and rectangles with CPU represent chips. Each cache line is 64 bytes, both L1 instruction cache and L1 data cache are 32K bytes 8-way set associative, and the L2 cache is 4M bytes 16-way set associative.

Memory References

In the second seco

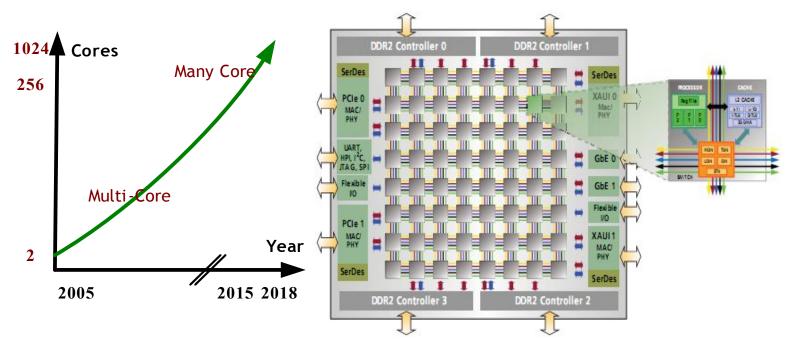
Applications should avoid the memory wall

Cache-to-cache transfer should be avoided



Multi-Core Challenges

- CMP V.S. SMP
 - More cores can be integrated
 - SMP: Low(2CPU), Middle(4~8CPU), High(>16CPU)
 - CMP: 4~8 cores systems, 1000+ cores (<10years) E.g. Intel's 80 cores chip & Tilera's 64 core chip





Multi-Core Challenge

• However, can operating systems and applications use these cores effectively?

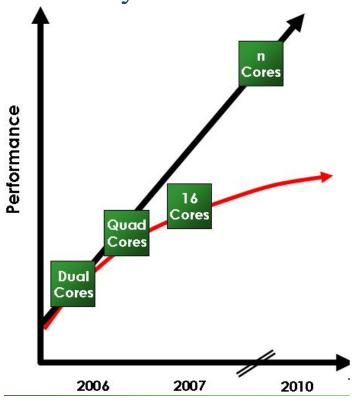


Fig 2. Performance bound by bottlenecks



OS supporting Multicore

- Linux
- Solaris
- FreeBSD
- Windows
- VxWorks

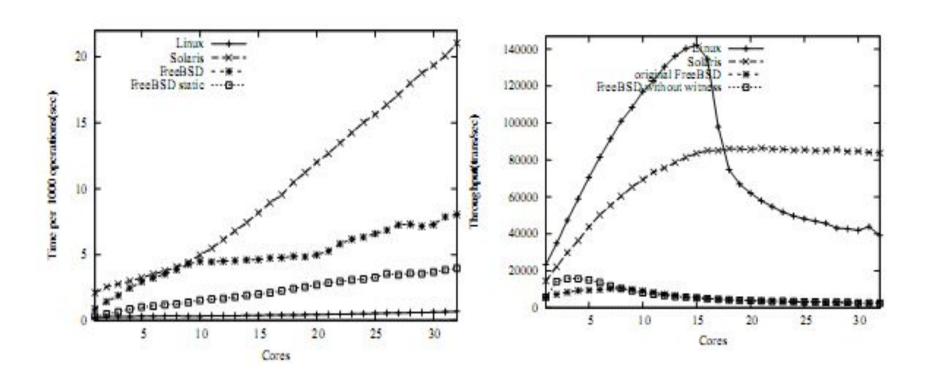








Benchmarks





Some Conclusions

- No system scales clearly better than another in all aspects for micro-benchmark test
- Linux and Solaris are competitive in application benchmark test, FreeBSD loses both in performance and scalability
- Kernel synchronizations protecting the shared data structure are the main bottlenecks on multicore platform



Tendency

- MultiCore
- Security
- Correction



History of Security Problem

- Originally, there was no security problem
- Later, there was a problem, but nobody cared
- Now, there are increasing problems, and people are beginning to care
 - Automation
 - Action at a distance
 - Technique propagation



Threat Analysis

- What are we trying to protect? (and why?)
- What are the vulnerabilities of those assets?
- Who might exploit a vulnerability?
 - Either on purpose or by accident



Threat Analysis

- What are we trying to protect? (and why?)
- What are the vulnerabilities of those assets?
- Who might exploit a vulnerability?
 - Either on purpose or by accident
- How can we prevent a specific threat?
- How much is it worth to us to prevent it?
- How much will it cost to prevent it?



The Core Technical Problem

- Controlling access to machine and data resources
- Controlling the way access rights are passed from holder to holder
 - person to person
 - program to program
- Preventing maliciousness and errors from subverting the controls



Access Rights

- In general case, need triplet for every possible combination of *right*, protected *asset*, and potential user
 U (U, A, R)
- ...and some entity must be responsible for checking and enforcing any limitation...
- The 3-D matrix is hard to manage...We need a simpler approach!



ACL&Capabilities

	file1	file 2	file 3	device	domain
User/Domain 1	r	rx	rwx	_	enter
User/Domain 2	r	X	rx	rwx	_
User/Domain 3	rw	_	_	_	_
•••					

- Columns are access control lists (ACLs)
 - Associated with each object
- Rows are capabilities
 - Associated with each user or each domain



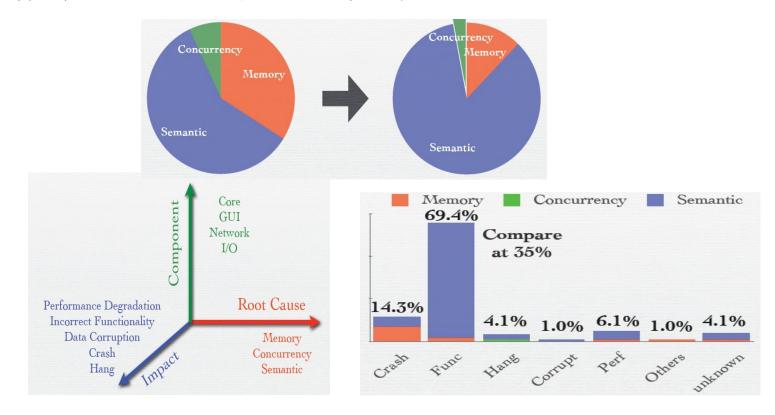
What about efficiency?

- At run-time...
 - What does the OS know about the user?
 - What does the OS know about the resources?
- What is the cost of checking and enforcing?
 - Access to the data
 - Cost of searching for a match



Current Status (1)

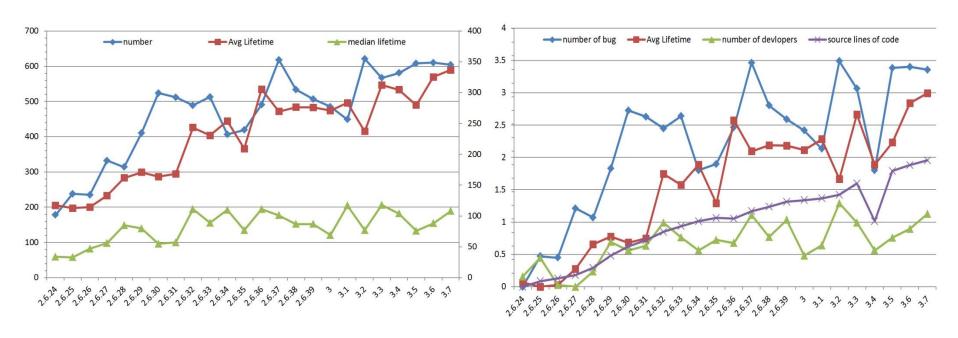
- 对当前Android漏洞的理解
 - Sematic Vulnerability 越来越多
 - 数据泄漏漏洞的威胁越来越大





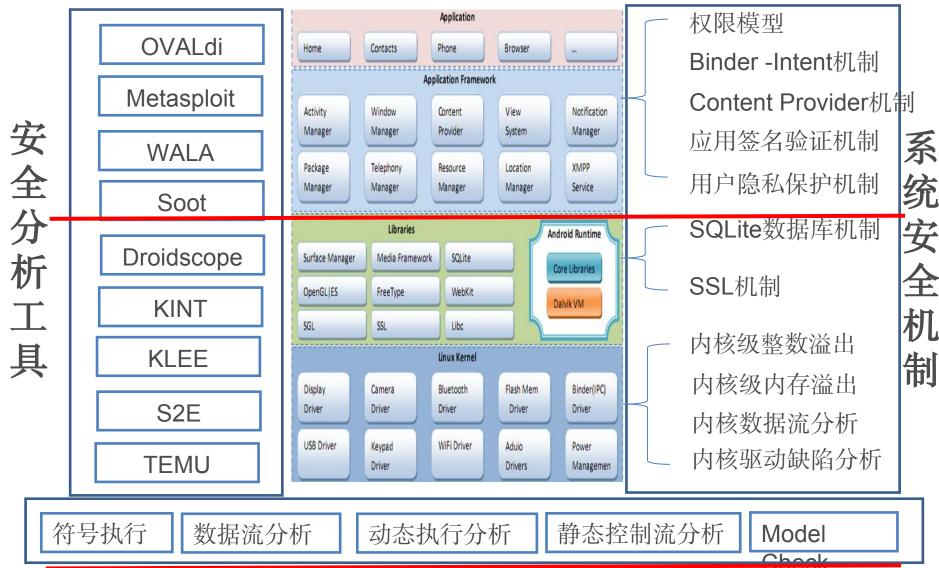
Current Status (2)

- 对当前Linux Kernel漏洞的理解
 - Linux漏洞有扩大化的趋势
 - 但发现Linux漏洞难度加大





System Security Technology



核心分析技术



Tendency

- MultiCore
- Security
- Correction



Standard Motivating Slide for Verification



Ariane 5



Mars Polar Lander



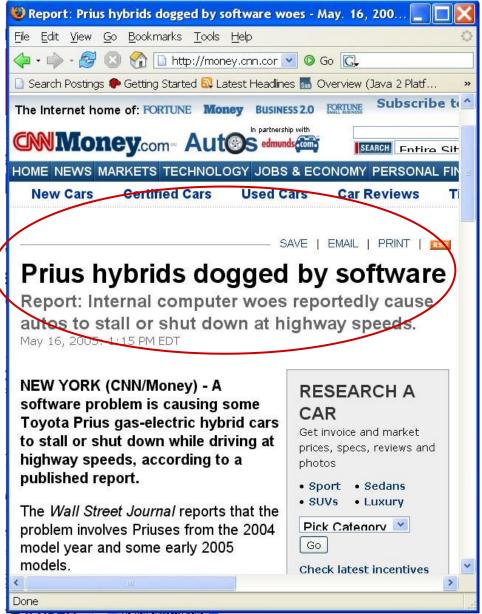
Mars climate orbiter





Photo from edmunds.com

Toyota recalled its 160,000 Prius cars in Oct 2005, because of bugs in the software controlling the hybrid gas-electric engine system...

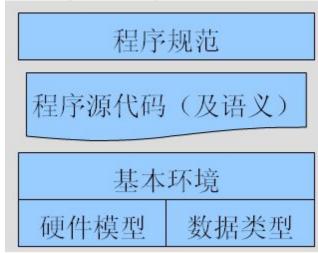




Basic Idea

Certified Software: Problem Definition

- Hardware
 - processors, memory, storage, devices,
- Software
 - bootloader, device drivers, OS, runtime, applications, ...
- Need a mathematical proof showing that as long as the hardware works, the software always work according to its specification





Challenges

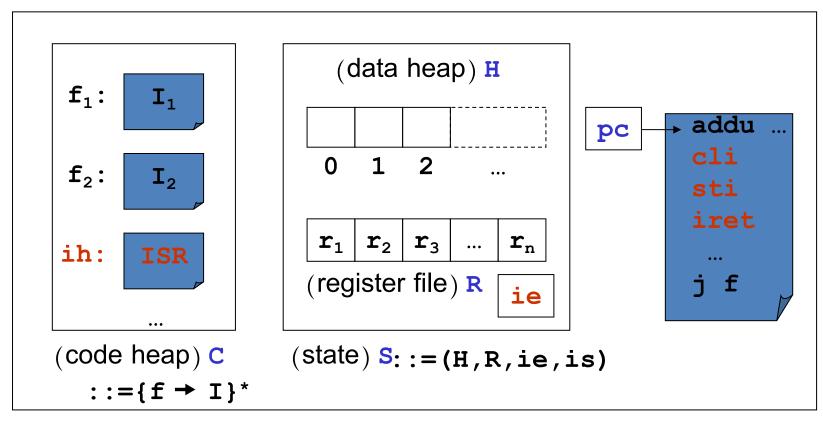
- Components come from different sources
 - Manually written assembly
 - -C/C++
 - Type safe languages
 - Java, C# ...

- Many different features
 - Code loading
 - Control abstractions
 - jmp (goto)
 - functions
 - exceptions
 - threads
 - interrupts



AIM: The Machine

Abstract Interrupt Machine



(program) P ::=(C,S,pc)



Challenges

- Components come from different sources
 - Manually written assembly
 - -C/C++
 - Type safe languages
 - Java, C# ...

- Many different features
 - Memory update
 - type-preserving update
 - type-changing update
 - pointer arithmetic
 - Device drivers and I/O

— ...



Some Conclusions

- AIM machine
 - low-level
 - can implement interrupt handlers and thread libraries
- A program logic
 - following local reasoning in separation logic
 - modeling cli/sti, switch, block/unblock in terms of memory ownership transfer
 - can certify different implementation of locks and C.V.s
- mini-OS proof
 - Interrupt handler, scheduler and others



Thanks

- Some materials are from Andy Wang
- CS-502 Operating Systems from WPI
- Compiler/Program Research Group in TH
-