

Program		All Implementation Messages - Errors, Warnings, and Infos	New
xst	ERROR	HDLCompiler:1660 - "counter_line_error_here.v" Line 103: Procedural assignment to a non-register error is not permitted, left-hand side should be reg/integer/time/genvar	
xst	ERROR	HDLCompiler:1660 - "counter_line_error_here.v" Line 103: Procedural assignment to a non-register error is not permitted, left-hand side should be reg/integer/time/genvar	
xst	ERROR	HDLCompiler:1660 - "error_1.v" Line 30: Procedural assignment to a non-register error1 is not permitted, left-hand side should be reg/integer/time/genvar	
xst	ERROR	HDLCompiler:1660 - "error_1.v" Line 30: Procedural assignment to a non-register error1 is not permitted, left-hand side should be reg/integer/time/genvar	
xst	ERROR	HDLCompiler:598 - "G:\FPGA\Book\Codes\line\counter_line.v" Line 19: Module <counter_line> ignored due to previous errors.	