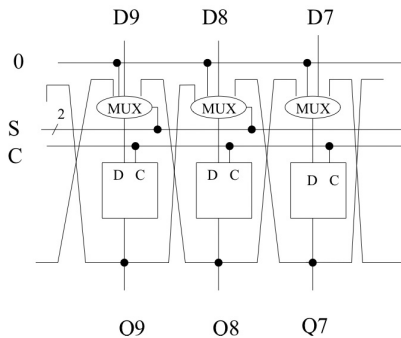


1. for every multiplexor ! !  
0 !



I'm basically building mine based on this model.

For //, Circular shift right, I make sure the output of the D flip-flop on the right goes right in the left (most left) one as input.

Also when a relatively left D flip-flop has its output sent into the ~~D flip-flop~~ as input, it is a right-shift.

When the output of a D flip-flop is sent into the D flip-flop on its left as input, it is a left-shift.

And to manage the input and output, we can add 2 additional mux, to make sure that the input signal is shifted in to the most left D flip-flop when  $S_1, S_0$  is 10, and the input is shifted into the most right D flip-flop when  $S_1, S_0$  is 01.