Time	10 ns	20 ns	30 lr	1S 40 ns	50 ns	60 hs	70 hs	80 hs	90 hs	100 hs	110	ns	120 hs 130 i	is 140 hs	
addr[15:0]	(xxxx )(0000		X0001		X0002		X0003		X0004		X0005		X0006		X
clk					Ĥ		ì				- î		i î		Ť
cout															
cur_ins[15:0]	xxxx (0000	χ0040		X0281		X04D1	_	X071A		xxxx					
	xxxx FFFF			XFFFE				XFFFF		xxxx					
	xxxx FFFF			,				(0001		xxxx					
	(xxxx )(0040		X0281		X04D1		X071A		xxxx						
load_ir															一
op[1:0]	(xx )(00			X01		X10		X11		xx					
pc_inc															$\equiv$
rd_addr_a[2:0]	xxx (000			X001				X010		xxx					
rd_addr_b[2:0]						X010		X011		(xxx					
reset															
wr_addr[2:0]	xxx (000	χ001		X010		X011		<b>X</b> 100		(xxx					
wr_reg															