[ 9.211177] pcie-tegra.c:nvidia,max-speed is 1 | NOTE: 1: Gen1, 2: Gen2, 3: Gen3, 4: Gen4

[ 9.211378] tegra-pcie-dw 141a0000.pcie: PCIe max-speed check

[ 9.212828] tegra-pcie-dw 141a0000.pcie: Setting init speed to max speed

[ 9.215861] OF: PCI: host bridge /pcie@141a0000 ranges:

[ 9.217313] OF: PCI: IO 0x3a100000..0x3a1fffff -> 0x3a100000

[ 9.218737] OF: PCI: MEM 0x1f40000000..0x1fffffffff -> 0x40000000

[ 9.222065] OF: PCI: MEM 0x1c00000000..0x1f3fffffff -> 0x1c00000000

[ 9.339529] tegra-pcie-dw 141a0000.pcie: link is up

[ 9.341208] tegra-pcie-dw 141a0000.pcie: PCI host bridge to bus 0005:00

[ 9.343869] pci\_bus 0005:00: root bus resource [bus 00-ff]

[ 9.345251] pci\_bus 0005:00: root bus resource [io 0x300000-0x3fffff] (bus address [0x3a100000-0x3a1fffff])

[ 9.346776] pci\_bus 0005:00: root bus resource [mem 0x1f40000000-0x1fffffffff] (bus address [0x40000000-0xffffffff])

[ 9.348276] pci\_bus 0005:00: root bus resource [mem 0x1c00000000-0x1f3fffffff pref]

[ 9.349703] pci 0005:00:00.0: [10de:1ad0] type 01 class 0x060400

[ 9.349931] pci 0005:00:00.0: PME# supported from D0 D3hot D3cold

[ 9.350133] iommu: Adding device 0005:00:00.0 to group 63

[ 9.351822] pci 0005:01:00.0: [1172:e003] type 00 class 0xff0000

[ 9.351967] pci 0005:01:00.0: reg 0x10: [mem 0x00000000-0x000001ff 64bit pref]

[ 9.352162] pci 0005:01:00.0: reg 0x20: [mem 0x00000000-0x0001ffff 64bit pref]

[ 9.353048] iommu: Adding device 0005:01:00.0 to group 64

[ 9.364310] pci 0005:00:00.0: BAR 15: assigned [mem 0x1c00000000-0x1c000fffff 64bit pref]

[ 9.365742] pci 0005:01:00.0: BAR 4: assigned [mem 0x1c00000000-0x1c0001ffff 64bit pref]

[ 9.367228] pci 0005:01:00.0: BAR 0: assigned [mem 0x1c00020000-0x1c000201ff 64bit pref]

[ 9.368747] pci 0005:00:00.0: PCI bridge to [bus 01-ff]

[ 9.370096] pci 0005:00:00.0: bridge window [mem 0x1c00000000-0x1c000fffff 64bit pref]

[ 9.371574] pci 0005:00:00.0: Max Payload Size set to 256/ 256 (was 256), Max Read Rq 512

[ 9.373046] pci 0005:01:00.0: Max Payload Size set to 256/ 256 (was 128), Max Read Rq 512

[ 9.374831] pcieport 0005:00:00.0: Signaling PME through PCIe PME interrupt

[ 9.376269] pci 0005:01:00.0: Signaling PME through PCIe PME interrupt

[ 9.377657] pcie\_pme 0005:00:00.0:pcie001: service driver pcie\_pme loaded

….

0005:01:00.0 Unassigned class [ff00]: Altera Corporation Device e003 (rev 01)

Subsystem: Device 4e1b:4e0c

Control: I/O- Mem- BusMaster- SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR- FastB2B- DisINTx-

Status: Cap+ 66MHz- UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR- INTx-

Interrupt: pin A routed to IRQ 0

Region 0: Memory at 1c00020000 (64-bit, prefetchable) [disabled] [size=512]

Region 4: Memory at 1c00000000 (64-bit, prefetchable) [disabled] [size=128K]

Capabilities: [50] MSI: Enable- Count=1/4 Maskable- 64bit+

Address: 0000000000000000 Data: 0000

Capabilities: [78] Power Management version 3

Flags: PMEClk- DSI- D1- D2- AuxCurrent=0mA PME(D0-,D1-,D2-,D3hot-,D3cold-)

Status: D0 NoSoftRst- PME-Enable- DSel=0 DScale=0 PME-

Capabilities: [80] Express (v2) Endpoint, MSI 00

DevCap: MaxPayload 256 bytes, PhantFunc 0, Latency L0s <64ns, L1 <1us

ExtTag- AttnBtn- AttnInd- PwrInd- RBE+ FLReset- SlotPowerLimit 0.000W

DevCtl: Report errors: Correctable- Non-Fatal- Fatal- Unsupported-

RlxdOrd+ ExtTag- PhantFunc- AuxPwr- NoSnoop+

MaxPayload 256 bytes, MaxReadReq 512 bytes

DevSta: CorrErr+ UncorrErr- FatalErr- UnsuppReq- AuxPwr- TransPend-

LnkCap: Port #1, Speed 8GT/s, Width x8, ASPM not supported, Exit Latency L0s <4us, L1 <1us

ClockPM- Surprise- LLActRep- BwNot- ASPMOptComp+

LnkCtl: ASPM Disabled; RCB 64 bytes Disabled- CommClk+

ExtSynch- ClockPM- AutWidDis- BWInt- AutBWInt-

LnkSta: Speed 2.5GT/s, Width x8, TrErr- Train- SlotClk+ DLActive- BWMgmt- ABWMgmt-

DevCap2: Completion Timeout: Not Supported, TimeoutDis+, LTR-, OBFF Not Supported

DevCtl2: Completion Timeout: 50us to 50ms, TimeoutDis-, LTR-, OBFF Disabled

LnkCtl2: Target Link Speed: 8GT/s, EnterCompliance- SpeedDis-

Transmit Margin: Normal Operating Range, EnterModifiedCompliance- ComplianceSOS-

Compliance De-emphasis: -6dB

LnkSta2: Current De-emphasis Level: -3.5dB, EqualizationComplete-, EqualizationPhase1-

EqualizationPhase2-, EqualizationPhase3-, LinkEqualizationRequest-

Capabilities: [100 v1] Virtual Channel

Caps: LPEVC=0 RefClk=100ns PATEntryBits=1

Arb: Fixed- WRR32- WRR64- WRR128-

Ctrl: ArbSelect=Fixed

Status: InProgress-

VC0: Caps: PATOffset=00 MaxTimeSlots=1 RejSnoopTrans-

Arb: Fixed- WRR32- WRR64- WRR128- TWRR128- WRR256-

Ctrl: Enable+ ID=0 ArbSelect=Fixed TC/VC=ff

Status: NegoPending- InProgress-

Capabilities: [200 v1] Vendor Specific Information: ID=1172 Rev=0 Len=044 <?>

Capabilities: [300 v1] #19

Capabilities: [800 v1] Advanced Error Reporting

UESta: DLP- SDES- TLP- FCP- CmpltTO- CmpltAbrt- UnxCmplt- RxOF- MalfTLP- ECRC- UnsupReq- ACSViol-

UEMsk: DLP- SDES- TLP- FCP- CmpltTO- CmpltAbrt- UnxCmplt- RxOF- MalfTLP- ECRC- UnsupReq- ACSViol-

UESvrt: DLP+ SDES+ TLP- FCP+ CmpltTO- CmpltAbrt- UnxCmplt- RxOF+ MalfTLP+ ECRC- UnsupReq- ACSViol-

CESta: RxErr- BadTLP- BadDLLP- Rollover- Timeout- NonFatalErr-

CEMsk: RxErr- BadTLP- BadDLLP- Rollover- Timeout- NonFatalErr+

AERCap: First Error Pointer: 00, GenCap- CGenEn- ChkCap- ChkEn-

[ 408.532540] altera\_dma: loading out-of-tree module taints kernel.

[ 408.533725] Altera DMA: altera\_dma\_init(), Aug 13 2019 17:09:06

[ 408.534289] Altera DMA 0005:01:00.0: enabling device (0000 -> 0002)

[ 408.534336] Altera DMA 0005:01:00.0: pci\_enable\_device() successful

[ 408.534629] Altera DMA 0005:01:00.0: pci\_enable\_msi() successful

[ 408.534669] Altera DMA 0005:01:00.0: using a 64-bit irq mask

[ 408.534675] Altera DMA 0005:01:00.0: irq pin: 1

[ 408.534682] Altera DMA 0005:01:00.0: irq line: 0

[ 408.534687] Altera DMA 0005:01:00.0: irq: 819

[ 408.534696] Altera DMA 0005:01:00.0: request irq: 0

[ 408.534709] Altera DMA 0005:01:00.0: BAR[0] 0x1c00020000-0x1c000201ff flags 0x0014220c, length 512

[ 408.534718] Altera DMA 0005:01:00.0: BAR[1] 0x00000000-0x00000000 flags 0x00000000, length 0

[ 408.534724] Altera DMA 0005:01:00.0: BAR[2] 0x00000000-0x00000000 flags 0x00000000, length 0

[ 408.534730] Altera DMA 0005:01:00.0: BAR[3] 0x00000000-0x00000000 flags 0x00000000, length 0

[ 408.534737] Altera DMA 0005:01:00.0: BAR[4] 0x1c00000000-0x1c0001ffff flags 0x0014220c, length 131072

[ 408.534743] Altera DMA 0005:01:00.0: BAR[5] 0x00000000-0x00000000 flags 0x00000000, length 0

[ 408.535124] Altera DMA 0005:01:00.0: BAR[0] mapped to 0xffffff800fd3b000, length 512

[ 408.535388] Altera DMA 0005:01:00.0: BAR[4] mapped to 0xffffff8046e40000, length 131072

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0005:01:00.0 Unassigned class [ff00]: Altera Corporation Device e003 (rev 01)

Subsystem: Device 4e1b:4e0c

Control: I/O- Mem+ BusMaster+ SpecCycle- MemWINV- VGASnoop- ParErr- Stepping- SERR- FastB2B- DisINTx+

Status: Cap+ 66MHz- UDF- FastB2B- ParErr- DEVSEL=fast >TAbort- <TAbort- <MAbort- >SERR- <PERR- INTx-

Latency: 0

Interrupt: pin A routed to IRQ 819

Region 0: Memory at 1c00020000 (64-bit, prefetchable) [size=512]

Region 4: Memory at 1c00000000 (64-bit, prefetchable) [size=128K]

Capabilities: [50] MSI: Enable+ Count=1/4 Maskable- 64bit+

Address: 00000000fffff000 Data: 0000

Capabilities: [78] Power Management version 3

Flags: PMEClk- DSI- D1- D2- AuxCurrent=0mA PME(D0-,D1-,D2-,D3hot-,D3cold-)

Status: D0 NoSoftRst- PME-Enable- DSel=0 DScale=0 PME-

Capabilities: [80] Express (v2) Endpoint, MSI 00

DevCap: MaxPayload 256 bytes, PhantFunc 0, Latency L0s <64ns, L1 <1us

ExtTag- AttnBtn- AttnInd- PwrInd- RBE+ FLReset- SlotPowerLimit 0.000W

DevCtl: Report errors: Correctable- Non-Fatal- Fatal- Unsupported-

RlxdOrd+ ExtTag- PhantFunc- AuxPwr- NoSnoop+

MaxPayload 256 bytes, MaxReadReq 512 bytes

DevSta: CorrErr+ UncorrErr- FatalErr- UnsuppReq- AuxPwr- TransPend-

LnkCap: Port #1, Speed 8GT/s, Width x8, ASPM not supported, Exit Latency L0s <4us, L1 <1us

ClockPM- Surprise- LLActRep- BwNot- ASPMOptComp+

LnkCtl: ASPM Disabled; RCB 64 bytes Disabled- CommClk+

ExtSynch- ClockPM- AutWidDis- BWInt- AutBWInt-

LnkSta: Speed 2.5GT/s, Width x8, TrErr- Train- SlotClk+ DLActive- BWMgmt- ABWMgmt-

DevCap2: Completion Timeout: Not Supported, TimeoutDis+, LTR-, OBFF Not Supported

DevCtl2: Completion Timeout: 50us to 50ms, TimeoutDis-, LTR-, OBFF Disabled

LnkCtl2: Target Link Speed: 8GT/s, EnterCompliance- SpeedDis-

Transmit Margin: Normal Operating Range, EnterModifiedCompliance- ComplianceSOS-

Compliance De-emphasis: -6dB

LnkSta2: Current De-emphasis Level: -3.5dB, EqualizationComplete-, EqualizationPhase1-

EqualizationPhase2-, EqualizationPhase3-, LinkEqualizationRequest-

Capabilities: [100 v1] Virtual Channel

Caps: LPEVC=0 RefClk=100ns PATEntryBits=1

Arb: Fixed- WRR32- WRR64- WRR128-

Ctrl: ArbSelect=Fixed

Status: InProgress-

VC0: Caps: PATOffset=00 MaxTimeSlots=1 RejSnoopTrans-

Arb: Fixed- WRR32- WRR64- WRR128- TWRR128- WRR256-

Ctrl: Enable+ ID=0 ArbSelect=Fixed TC/VC=ff

Status: NegoPending- InProgress-

Capabilities: [200 v1] Vendor Specific Information: ID=1172 Rev=0 Len=044 <?>

Capabilities: [300 v1] #19

Capabilities: [800 v1] Advanced Error Reporting

UESta: DLP- SDES- TLP- FCP- CmpltTO- CmpltAbrt- UnxCmplt- RxOF- MalfTLP- ECRC- UnsupReq- ACSViol-

UEMsk: DLP- SDES- TLP- FCP- CmpltTO- CmpltAbrt- UnxCmplt- RxOF- MalfTLP- ECRC- UnsupReq- ACSViol-

UESvrt: DLP+ SDES+ TLP- FCP+ CmpltTO- CmpltAbrt- UnxCmplt- RxOF+ MalfTLP+ ECRC- UnsupReq- ACSViol-

CESta: RxErr- BadTLP- BadDLLP- Rollover- Timeout- NonFatalErr-

CEMsk: RxErr- BadTLP- BadDLLP- Rollover- Timeout- NonFatalErr+

AERCap: First Error Pointer: 00, GenCap- CGenEn- ChkCap- ChkEn-

Kernel driver in use: Altera DMA