HACKADAY.IO

Adapter Back to overview

LCD/OLED screens with

MIPI DSI Display Shield/HDMI

Step 1

- Solder it!

your screen.

Step 2

Clone the repository:

from Xilinx website.

Build the software:

\$ cd software

panel type you wish to use.

Step 3

\$ make

Step 4

rev1_top.bit.

through JTAG.

Step 5

DISCUSSIONS

comment

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programmer?

it.

Build the FPGA bitstream:

declaration for the values.

Click Generate Programming File.

After a while, you'll get the bitstream:

Program the SPI flash with the bitstream

Power-cycle the board. A test screen with the

should appear. If it doesn't... well, something's

wrong. Otherwise, connect the HDMI input. If

display resolution and automatically configure

toferzim wrote 05/27/2018 at 22:17

Can I flash it with Arduino as a

Djon wrote 12/17/2016 at 16:59

somebody did it? Thank you!

Djon wrote 12/16/2016 at 16:05

Freezingcold wrote 02/01/2016 at 03:37

I need to buy one of these ready built and

that would be amazing. But not as amazing

I need to build a portable 1080p monitor 5"

as building the thing in the first place.

preferably. E980 looks perfect to use.

Think I need help don't I need hot air

soldering iron for these size of

ready rev1_top.bit file?

Hello! Where can I get the PCB design?

everything is OK, the PC should detect the

panel info and "No HDMI signal" message

Build the hardware:

should be mounted.

MIPI DSI interface.

Arduino shield format, HDMI-to-DSI adapter &

built-in framebuffer. <u>twl</u>

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I can not generate rev1.xise firmware,

Hello everybody! Does anyone have any

penny.lane.mini wrote 09/01/2014 at 23:14

- Make/order the DSI Shield PCB. - Check the BOM, as not all components

- No smoke indicates probable success.

git clone https://github.com/twl

Install an LM32 toolchain. You may find one

for Linux (IA32) available here.

on Lattice website. There's a pre-built version

Download and install Xilinx ISE 14.7 Webpack

- edit software/rev1/panels.h and set the

This will produce the boot.ram file that the

bitstream. The file contains both the panel

initialization/HDMI handling code and the

bootloader, so the LM32 application can be

Open hdl/top/rev1_top.vhd and edit the PLL

configuration genrics to match your LCD

panel. See Table 1 above the VHDL entity

cd hdl/syn/rev1; ise rev1.xise

synthesizer will embed in the FPGA

re-loaded at any time via USB UART.

 Make/order/design an adapter board for - Connect the two boards together with the LCD. Power the system through USB.

5ydc wrote 04/13/2015 at 10:59 How can i order full project hardware? Cuz my circumstance does not allow me to build it!

Gonzalo wrote 03/08/2015 at 15:28

I love your project. I want to know more

ask more. I want to take this project as

reference about how to implement FPGA

real application interface, you should work

easy at all. So it's really a study case. Great

I would love to interface the same way with

a mobile camera module. I'm reading that

Raspberry PI has a DSI and CSI2 (Camera

Serial Interface), so it should be possible to

connect it to a Display without using HDMI,

at hardware because what you did is not

about this project so I want to know if I can

components?

also a camera Module to the CSI2 directly. Do you think it's feasible? Maybe using the spartan chip can be done both with your card. Doing slight modifications. How much time did you take to make this

Hi just wondering if the board supports

I was hoping to use a mobile screen for an

touch as well?

odroid

I mean directly with the ribbon cable. And project?

work!