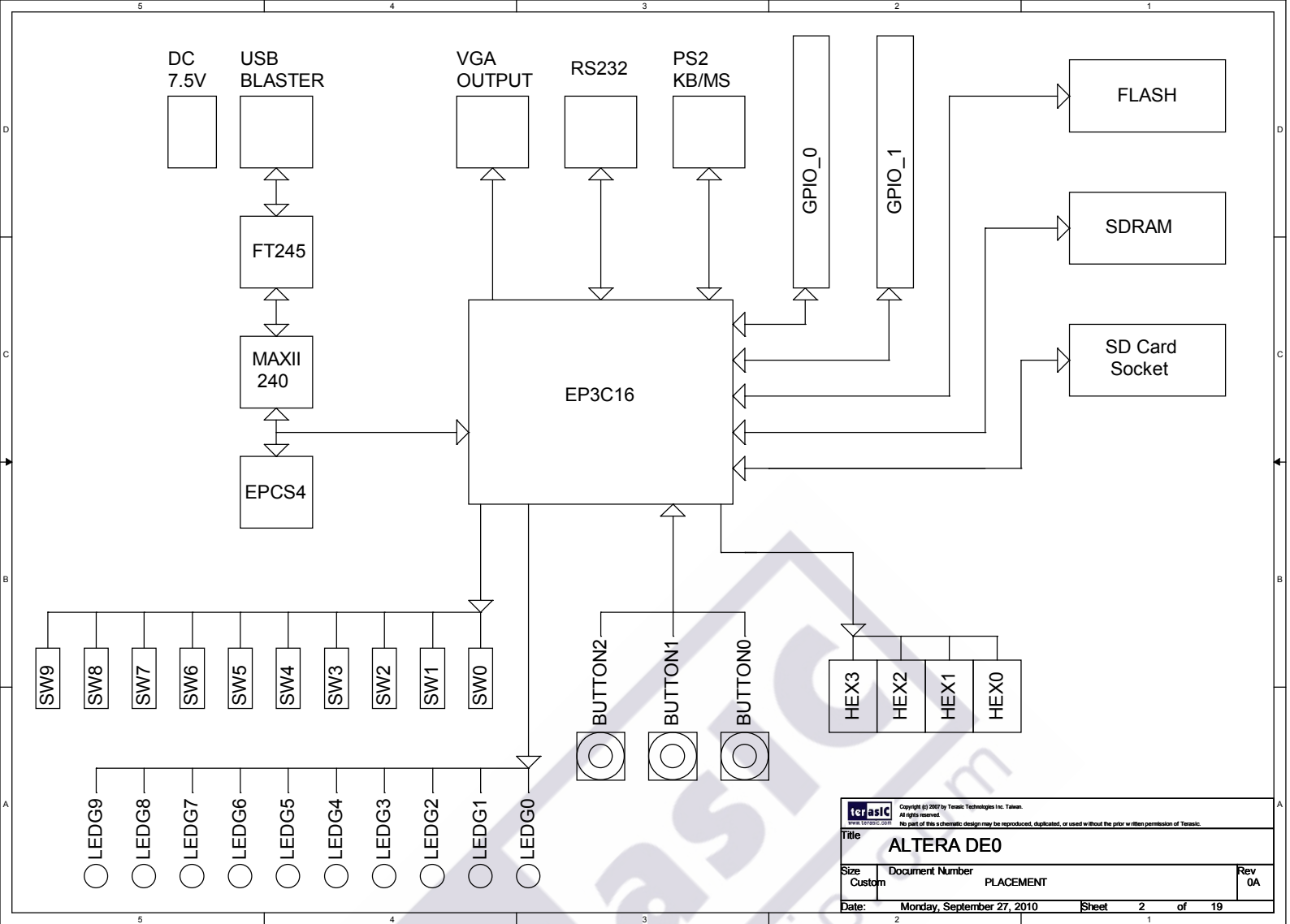
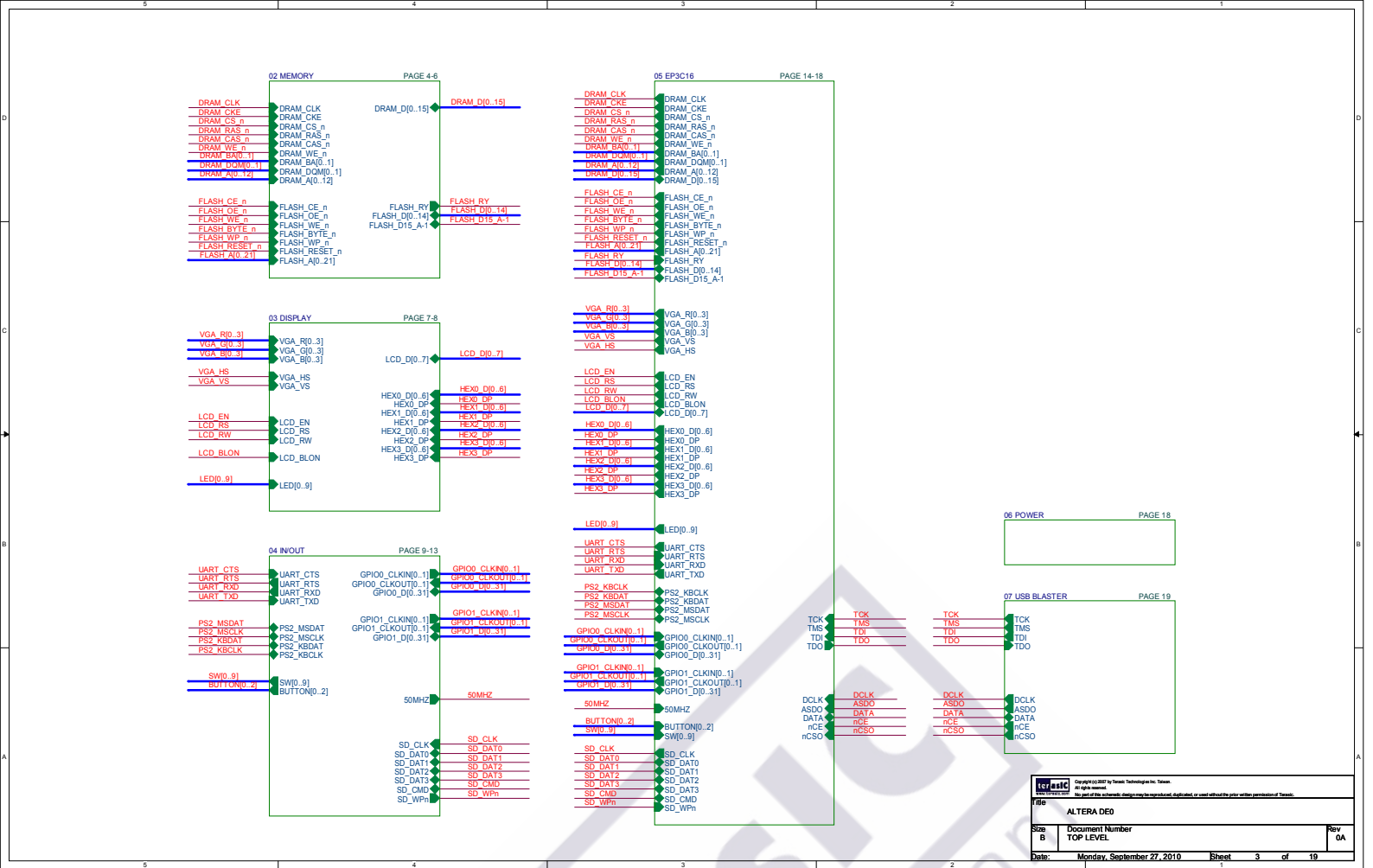


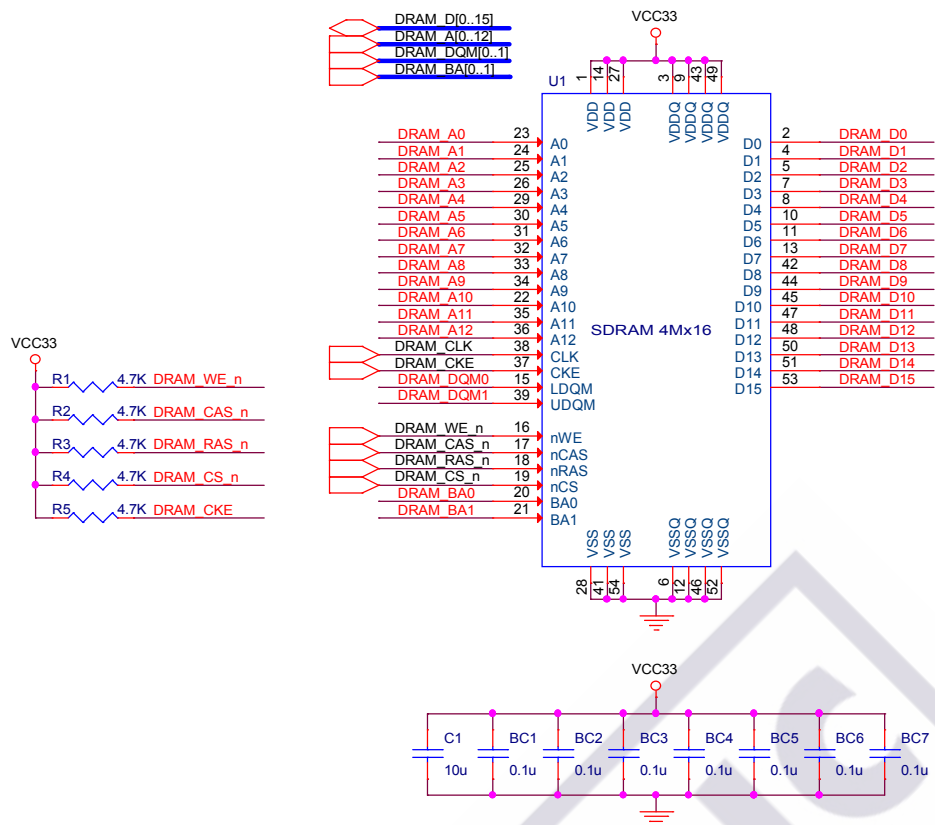
ALTERA CYCLONE III EP3C16 Development & Education BOARD


SCHEMATIC	CONTENT	PAGE
TOP	COVER PAGE, TOP	01 ~ 03
MEMORY	SDRAM,FLASH	04 ~ 05
DISPLAY	VGA, LED, 7SEGMENT	06 ~ 07
IN/OUT	CLOCK, PS2, RS232, BUTTON, SWITCH, CONNECTOR,SD CARD	08 ~ 12
FPGA	Cyclone II EP3C16 BANK1..BANK8 , POWER , CONFIG	13 ~ 17
POWER	POWER 1.2V,2.5V, 3.3V, 5V	18 ~ 18
USB BLASTER	USB BLASTER	19 ~ 19

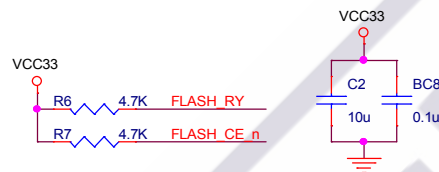
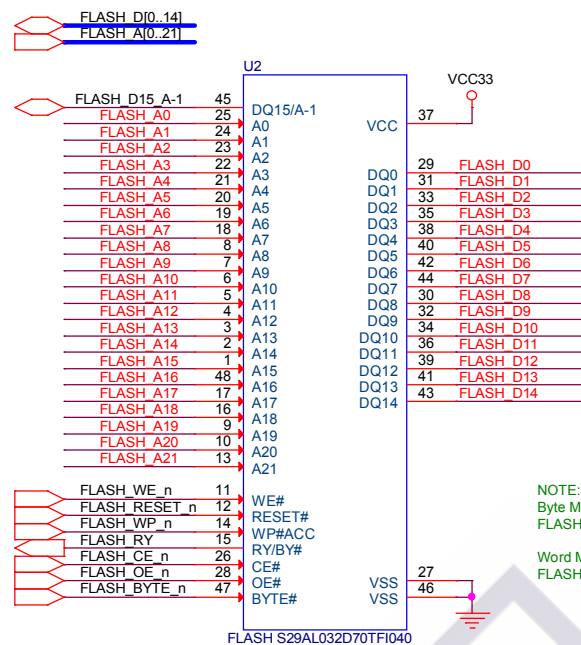





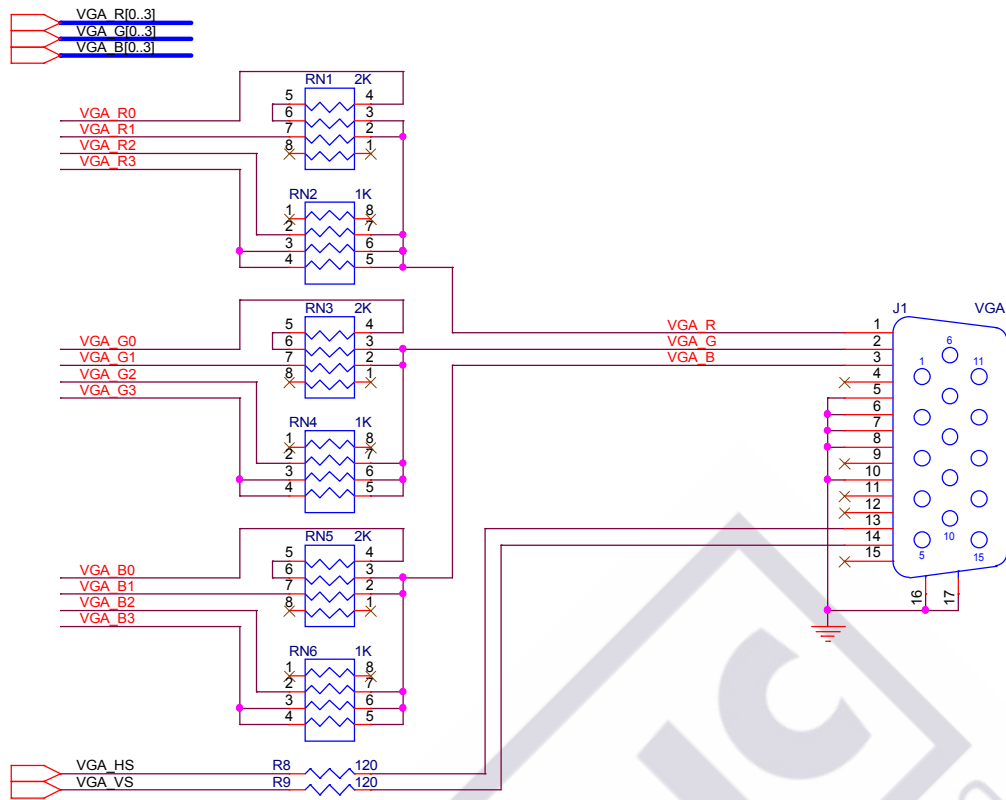
D
C
B
A



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Size	Document Number		Rev
A	SDRAM		0A
Date:	Monday, September 27, 2010	Sheet	4 of 19
2		1	

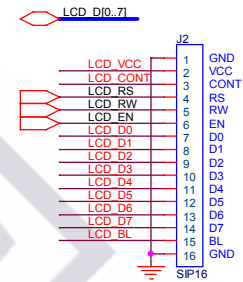
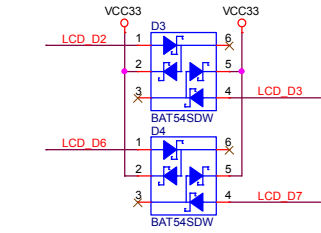
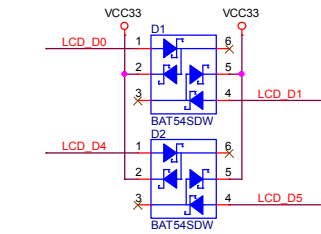
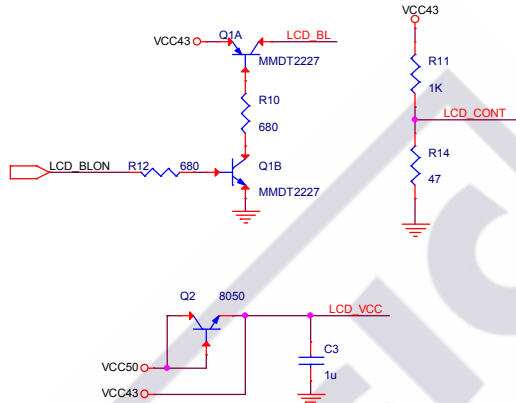
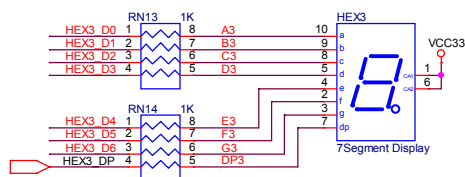
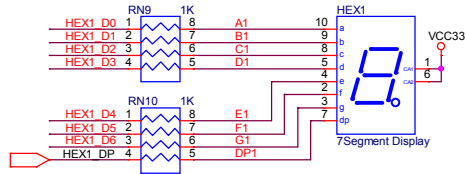
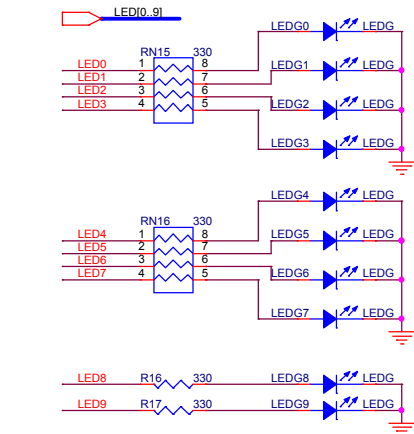
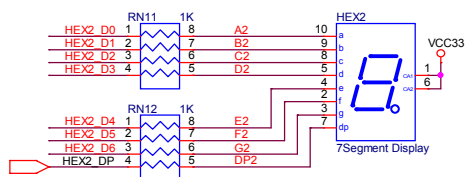
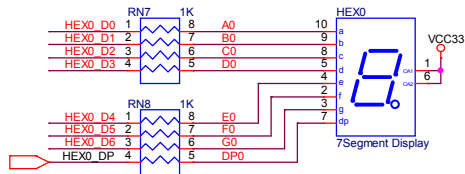


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Title ALTERA DE0			
Size A	Document Number FLASH		Rev 0A
Date:	Monday, September 27, 2010	Sheet 5	of 19

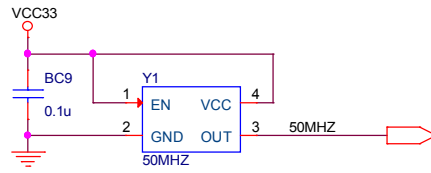


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Title		ALTERA DE0	
Size A	Document Number		Rev 0A
Date:	Monday, September 27, 2010		Sheet 6 of 19

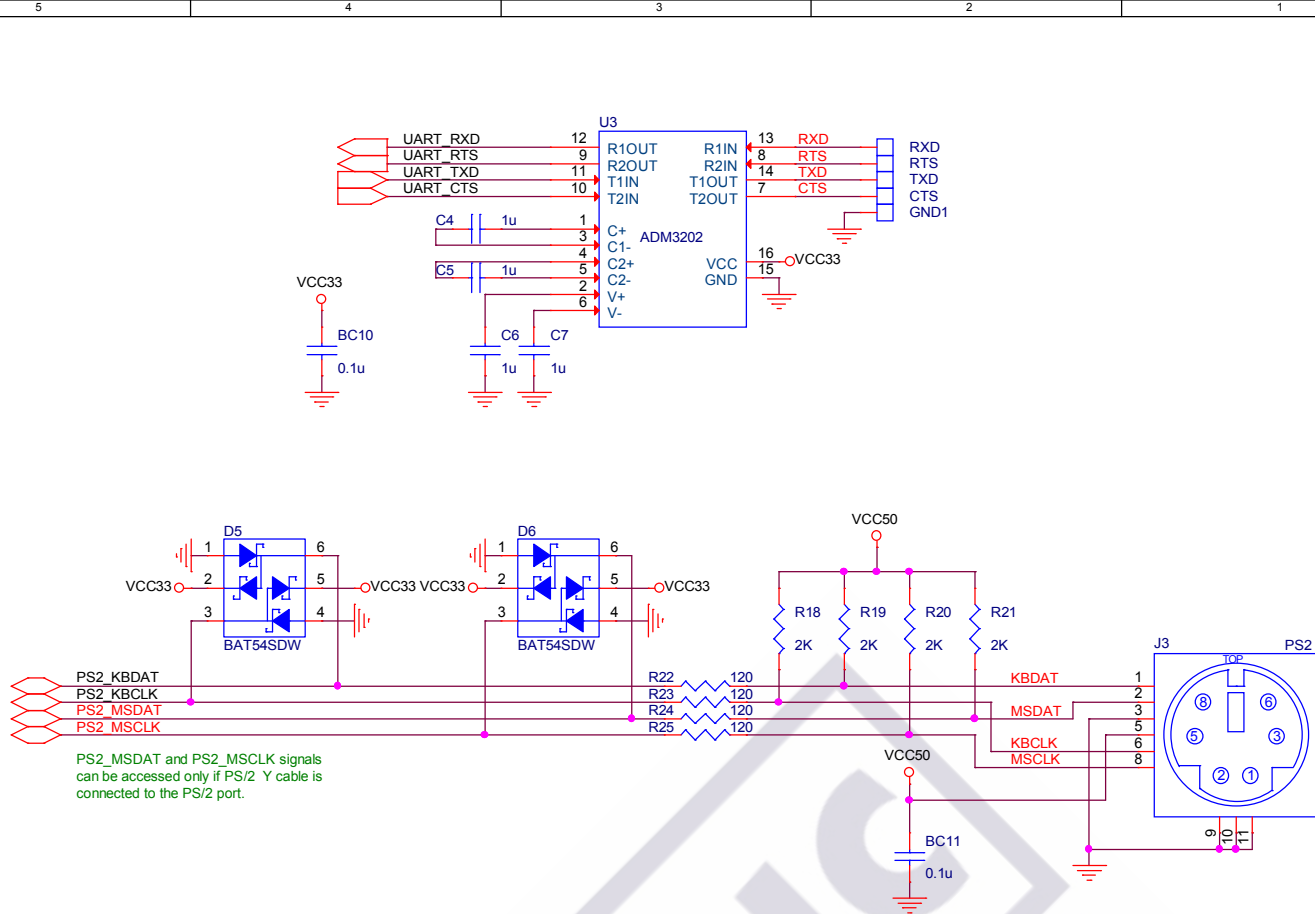
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HEX2_D10_6I
HEX1_D10_6I
HEX3_D10_6I



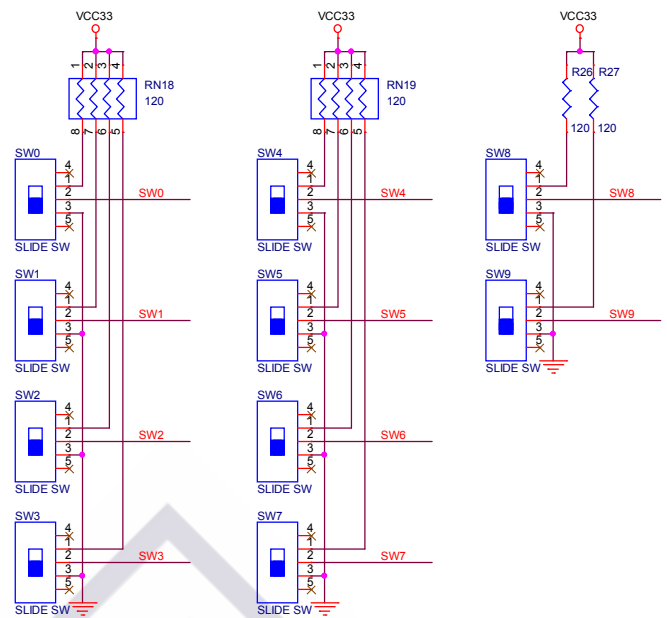
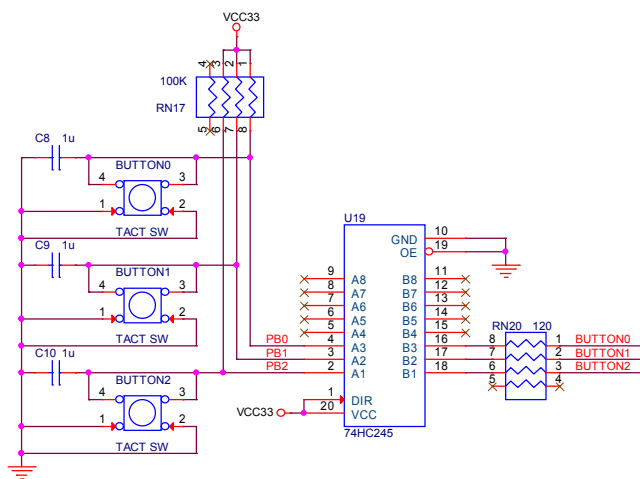
Title			
ALTERA DE0			
Document Number			
7 SEGMENT, LED and LCD Connector			
Date			
Monday, September 27, 2010			
Sheet			
7 of 19			
Rev			
0A			



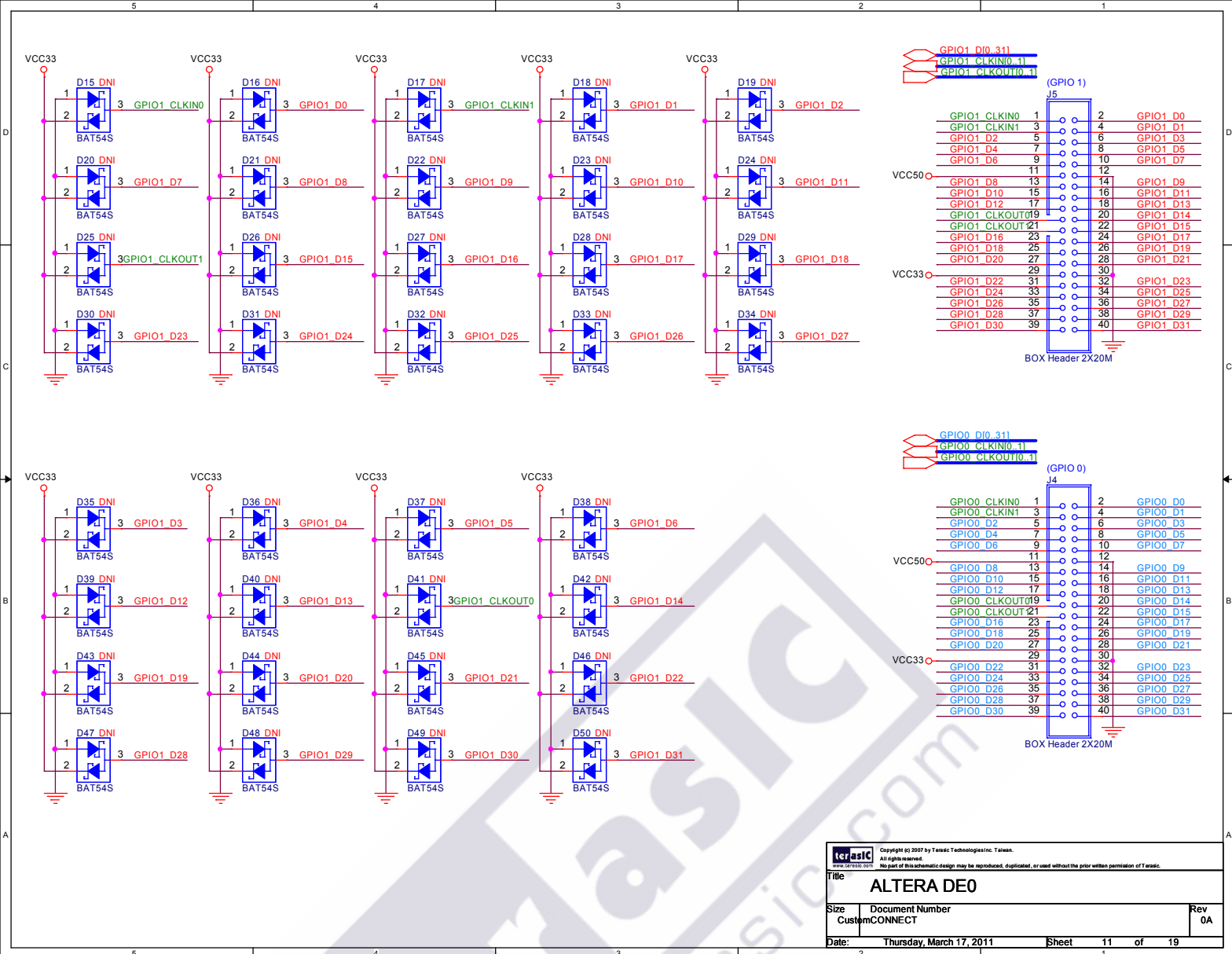
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Size A	Document Number CLOCK		Rev 0A
Date:		Monday, September 27, 2010	Sheet 8 of 19

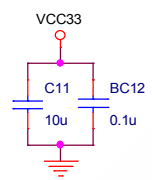
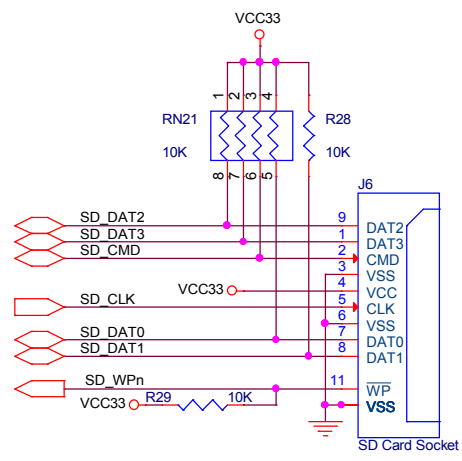



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Size	A	Document Number	PS2 AND RS232
Date:	Monday, September 27, 2010	Sheet	9 of 19
		Rev	0A



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Size	Document Number	Rev	
Custom	Button and Switch	0A	
Date:	Monday, September 27, 2010	Sheet	10 of 19





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Title			
ALTERA DE0			
Size	Document Number		Rev
A	SD Card Socket		0A
Date:	Monday, September 27, 2010	Sheet	12 of 19
2		1	

