AIN SHAMS UNIVERSITY
FACULTY OF ENGINEERING
International Credit Hours Engineering Programs (i.CHEP)

Laboratory Report
Logic Design

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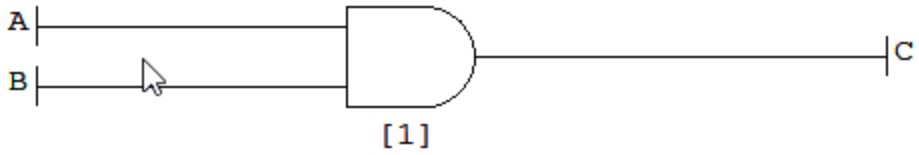
4. **Group: 2**
5. **Section: 3**

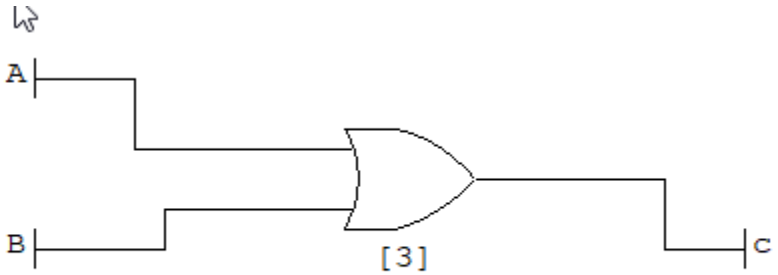
Course Code **EPM 211**	*Course Name* **Electrical Materials**	
	Semester Fall 2020	**Date of Submission** 13/1/2021

Q1



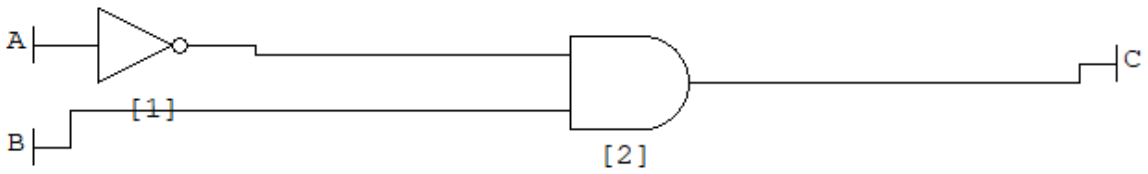
A	B	=>	C
0	0		0
0	1		0
1	0		0
1	1		1

Q2



A	B	=>	c
1	X		1
X	1		1

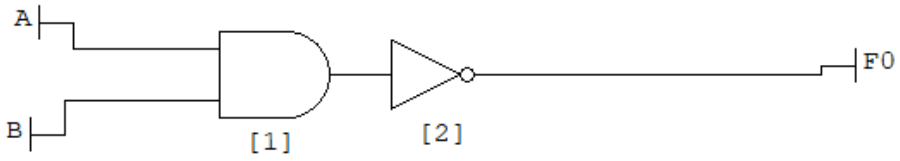
Q3



A	B	=>	C
0	0		0
0	1		1
1	0		0
1	1		0

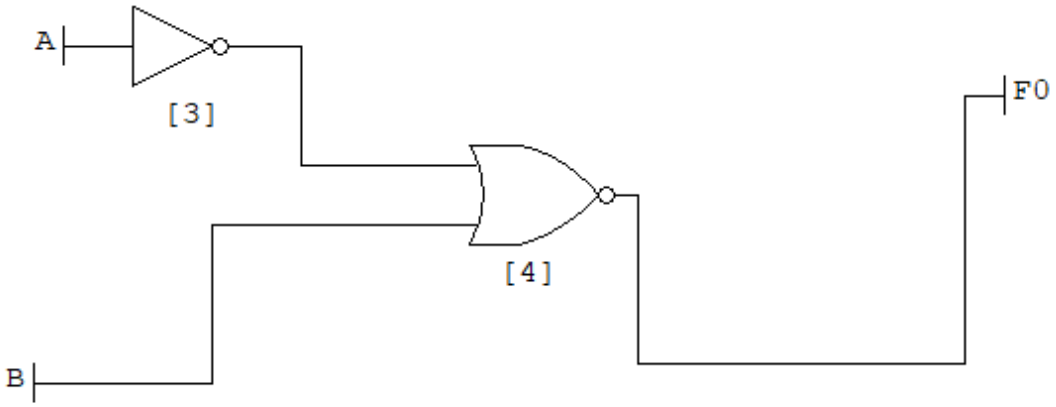
Q4

Yes



A	B	=>	F0
0	0		1
0	1		1
1	0		1
1	1		0

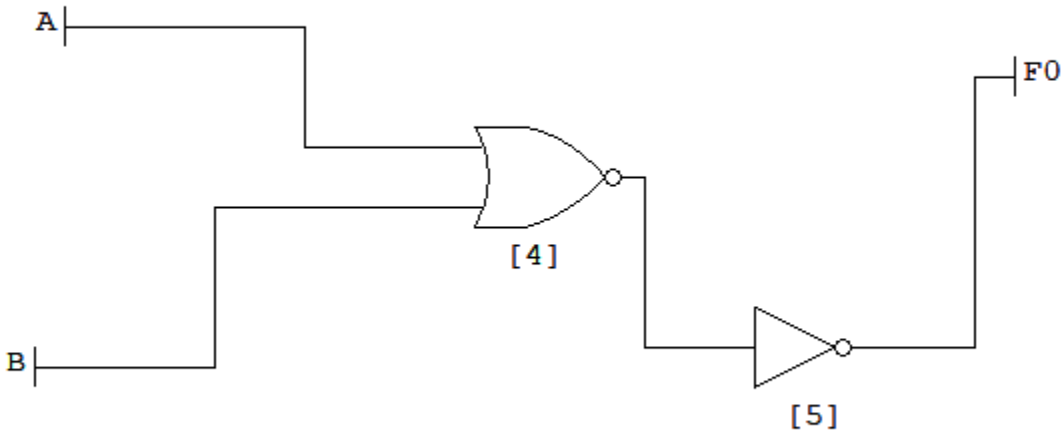
Q5



A	B	=>	F0
0	0		0
0	1		0
1	0		1
1	1		0

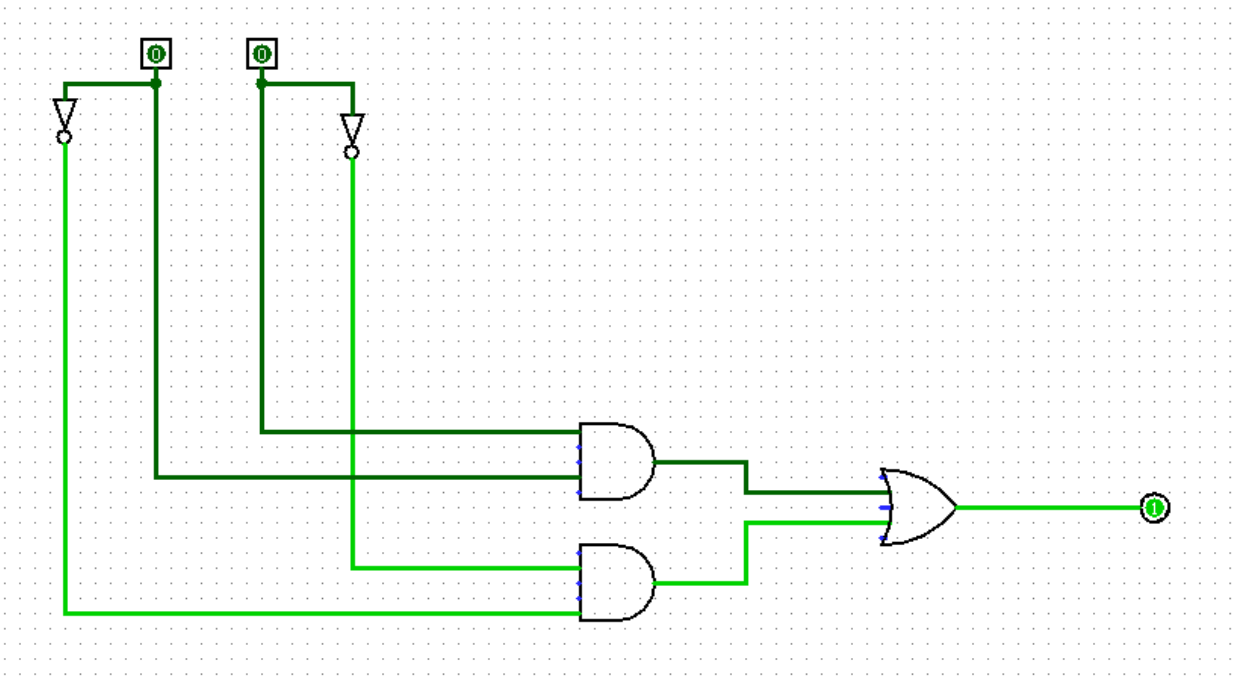
Q6

Yes



A	B	=>	F0
0	0		0
0	1		1
1	0		1
1	1		1

Q7



a	b	x
0	0	1
0	1	0
1	0	0
1	1	1

a	b	x
0	0	1
0	1	0
1	0	0
1	1	1

The Two Truth tables are the same, therefore, A xnor B is the same as A.B + A'.B'