Assignment _٢: Basic Operational Concept

١. The decoded instruction is stored in _____
a) IR
b) PC
c) Registers
d) MDR
Answer:

٢. The instruction -> Add LOCA, R٠ does _____
a) Adds the value of LOCA to R٠ and stores in the temp register
b) Adds the value of R٠ to the address of LOCA
c) Adds the values of both LOCA and R٠ and stores it in R٠
d) Adds the value of LOCA with a value in accumulator and stores it in R٠
Answer:

٣. Which registers can interact with the secondary storage?
a) MAR
b) PC
c) IR
d) R٠
Answer:

٤. During the execution of a program which gets initialized first?
a) MDR
b) IR
c) PC
d) MAR
Answer:

٥. Which of the register/s of the processor is/are connected to Memory Bus?
a) PC
b) MAR
c) IR
d) Both PC and MAR
Answer:

٦. ISP stands for _____
a) Instruction Set Processor
b) Information Standard Processing
c) Interchange Standard Protocol
d) Interrupt Service Procedure
Answer:

٧. The internal components of the processor are connected by _____
a) Processor intra-connectivity circuitry
b) Processor bus
c) Memory bus
d) Rambus
Answer:

٨. _____ is used to choose between incrementing the PC or performing ALU operations.
a) Conditional codes
b) Multiplexer
c) Control unit
d) None of the mentioned
Answer:

٩. The registers, ALU and the interconnection between them are collectively called as _____
a) process route
b) information trail
c) information path
d) data path
Answer:

١٠. _____ is used to store data in registers.
a) D flip flop
b) JK flip flop
c) RS flip flop
d) None of the mentioned
Answer:.