Create pipe (unidirec comm buff w/ 2 file descrips fd[0] read & fd[1] write) #include <unistd.h>

int pipe(int fd[2])

Data write & read FIFO base. No external/permanent name; only accessed via 2 fds.

Pipe can only be used by process that created it & its descendants.

close(fd) closes a file descriptor

dup(newrd) dupilicates rd, dup2(newrd, oldrd) copies (more like alias)		
Read: Not nec atomic; may read less bytes Blocking: if no data, write fd still opens.	Write: Atomic for at most PIPE_BUF bytes (512, 4k, 64k) Blocking: if buffer full & read fd open. When all fd to read closed, causes SIGPIPE sig for calling	

Pros: simple, flexible, efficient comms

Cons: no way to open already existing pipe: impossible for 2 arbitrary processes to share same requiring sending several msgs (FTP, HTTP) pipe (unless created by common ancestor)

IPC: UNIX Shared Mem

Parent & child processes run in separate addy spaces.

Shared mem segment: piece of mem that can be allocated & attached to addy space; process w/ this mem seg attached will have access to it but race conditions can occur

Procedure: find a key (unix uses key to identify shared mem segments) -> shmget() allocates a shared mem -> shmat() attach shared mem to addy space -> shmdt() detach mem from addy space -> shmctl() deallocate shared mem

Kevs: global; If other processes know key, can access shared mem

Can ask sys to provide private key using IPC_PRIVATE

key_t SomeKey;

SomeKey = 1234;

Autogenerate:

key_t = ftok(char *path, int ID); Asking for Shared Memory:

Use shmget() to request a shared mem (returns shared mem ID):

shm_id = shmget(key_t key, int size, int flag) Flag for our purpose either 0666 (rw) or IPC_CREAT|0666

Include following:

#include <sys/types.h>

#include <sys/ipc.h> #include <svs/shm.h>

Attaching Shared Memory:

Use shmat() to attach existing shared mem to addy space (ret void pointer to mem): shm prt = shmat(int shm id, char *ptr, int flag);

shm id ID from shmget(), use NULL for ptr, flag = 0

Detaching & Removing Shared Memory:

To detach shared memory, use shmdt(shm_ptr); shm ptr is pointer returned by shmat().

After shared mem detached, it's still there (can be reattached to use again. Removing a shared mem will make it stop existing)

To remove shared memory, use shmctl(shm ID, IPC RMID, NULL); shm ID is shared mem ID returned by shmget()

Types: Message passing (Blocking/non-blocking, Datagrams, virtual circuits, streams, Remote Procedure Calls (RPC)), Shared Memory

Shared Memory: >=2 processes share part of their addy space

Adv: fast & easy to use (but concurr access to shared data can cause issues, and must sync

access to shared data)

Disady: senders & receivers must be on same machine, less secure (processes can directly access part of addy space of other processes)

Message Passing: processes want to exchange data send & receive msgs
One send and one receive:

send(addr, msg, length);

receive(addr, msg, length); Adv: senders & receivers can be on diff machines; receiver can inspect msgs received before processing them

Disady: hard to use (every data transfer requires send() & receive(), and receiving process must expect send())

Defining Issues of Msg Passing: Direct/Indirect communication, Blocking/Non-Blocking primitives, exception handling, quality of service

Direct Comm: send & rec calls specify processes as destination/source:

send(process, msg, length);

receive(process, msg, &length);

No intermediary b/w sender & receiver. Ex. each phone hardwired to another

Proc exe rec call must know identity of all procs likely to send msgs (bad for servers) Indirect Comm: send & rec primitives specify intermediary as destination or source (mailbox: sys obj created by kernel @ request of user process):

send(mailbox, msg, size);

receive(mailbox, msg. &size): Diff procs can send msgs to same mailbox. A proc can rec msgs from procs it knows nothing about, can wait for msgs from diff senders (answ 1st msg rec)

Private Mbox aka ports: proc requesting creation & children are only ones that can rec msgs through that mbox. Ceases to exist when proc requesting its creation (and all children) terminates.

Public Mboxes: owned by sys. & shared by all procs having right to rec msgs through it (works best when all procs on same machine). Survives termination of proc that requested

Blocking Primitives: blocking send doesn't return til receiving process has received msg; no buffering needed, Blocking receive doesn't return til msgs have been received (std choice, but not good choice for direct comm)

Non-Blocking Primitives: Non-blocking send returns as soon as msg has been accepted for delivery by OS (assuming OS can store msg in a buffer, std choice). Non-blocking receive returns as soon as it has either retrieved msg or learned mbox is empty (retrieve() acts as receive() for receiver).

Simulating blocking receives: can sim blocking receive w/ non-blocking receiving within a loop (aka busy wait)

Code = receive(mbox, msg, size);

sleep(1); // delay

} while (code == EMPTY MBOX):

Simulating blocking sends: can sim blocking send w/ 2 non-blocking sends & a blocking

Sender sends msg & requests ACK -> sender wait for ACK from receiver using blocking Compare & Swap Instructions: receive -> receivers sends ACK

Non-blocking primitives require buffering to let OS store msgs that have been sent but not received. Buffers have a bounded capacity, but theoretically unlimited capacity. Exception Condition Handling: must specify what to do if 1/2 processes dies. Esp important when the 2 procs on diff machines (must handle host failures & network partitions) Quality of Service: when sender & receiver on diff machines, msgs can be lost, corrupted or

duped, and can arrive out of sequence. Can still decide to provide reliable msg delivery Datagrams: msgs sent individually (can be lost, duped, out of seq). Reliable: msgs resent until ACK. Unreliable: msgs not ACK (works well when msg requests

reply which acts as implicit ACK) UDP (User Datagram Protocol) best known datagram protocol. Provides unreliable datagram

services which is best for short interactions Virtual Circuits: establishes logical connection b/w sender & receiver. Msgs guaranteed to

arrive in seq w/o lost/duped msgs. Requires virtual connection before sending any data. Best for transmitting large data amounts

Streams: like virtual circuits, but does NOT preserve msg boundaries (seamless stream of

TCP (Transmission Control Protocol): best known stream protocol, providing reliable stream serv. Heavyweight (needs 3 msgs to estab virtual connection)

Remote Procedure Calls (RPC): applies to client-server model send reg(args): rcv regs(&args):

process(args, &results): send reply(results);

rcv_reply(&results);

Adv: hides all details of msg passing, provides higher level of abstraction, extends wellknown model of programming

Disady: illusion not perfect (RPCs don't behave exactly like regular procedure calls, client & server don't share same addy space), programmer must be aware of differences

User program contains user code & calls user stub that appears to call server procedure rpc(xyz, args, &results);

User stub procedure generated by RPC package: parcks args into request msg & performs required conversions (arg marshaling) -> sends request msg -> waits for server reply -> unpacks results & performs required conversions (arg unmarshaling)

Server stub: generic server generated by RPC pack waits for client requests, unpack request args & performs required data conversions, calls appropriate server procedure (which is written by user & does actual processing), packs results into reply msg (performs required

conversions), sends reply msg Client & server processes don't share same addy space: no global variables, can't pass by ref, can't pass dynam data structs via pointers; RPC can pass args by value & result (passes curr val to RP & copies returned val in user program).

CHAPTER 5: CONCURRENCY

Multi App: invented to allow processing time to be shared among active apps Structures Appts: extension of modular design & struct programming OS Structure: OS themselves implemented as set of processes/threads

Key Terms:

Atomic Operation: function/action implemented as sequence of >=1 instructions appearing indivisible. Sequence is guaranteed of exe as a group or not at all

Critical Section: section within proc that requires access to shared resources & mut not be exe whle another process is in a corresponding section of code

Deadlock: situation where >= 2 processes unable to proceed be each is waiting for one of the

Livelock; >=2 procs continuously change states in response to changes in other proc(s) w/o doing useful work

Mutual Exclusion: requirement when one process is in crit sect that accesses shared resources, no other proc may be in crit sect that accesses any of those shared resources Principles: Interleaving & overlapping (ex of concur processing), Uniprocessor - relative

speed of exe of procs can't be predicted; dependent on activities of other procs, OS interrupt handling, OS scheduling policies

Difficulties: sharing of global resources, hard for OS to optimize resource allocation

management, hard to locate programming errors

Rare condition: occurs when multi procs/threads read & write data items. Final result depends on order of exe ("loser" updates last, determines final val)

OS Concerns (Design & mgmt. issues): OS keeping track of various procs, allocation & deallocate resources for each active proc, protect data & phys resources of each proc against interference, ensure procs & outputs are independent of processing speed

Interaction Process

Degree of Awareness	Relationship	Influence of 1 Process on the other	Potential Ctl Probs
Processes unaware of each other	Competition	Results of 1 process independent of others Timing may be affected	- Mut Exclusion - Deadlock (renewable) - Starvation
Processes indirectly aware of each other	Coop by sharing	Results of 1 proc may depend on info fr others Timing may be affected	Mut Exclusion Deadlock (renewable) Starvation Data coherence
Processes directly aware of each other (have comm prim available to them)	Coop by comm	Results of 1 proc may depend on info from others Timing may be offected.	- Deadlock (consumable) - starvation

Resource Competition: concurr procs competing for use of same resource (I/O devices, mem, proc time, clock). Control Probs: need mutual exclusion, deadlock, starvation

Mutual Exclusion Requirements: Must be enforeced, proc that halts has to w/o interfering w other procs, no deadlock/starvation, proc must not be denied access to crit sect when no other proc using it, no assump made about relative proc speeds/# of procs, proc remains inside crit sect for finite time only

Mutual Exclusion hardware supp: Special machine instructions - compare & swap (compare & exchange instruction). Compare made b/w mem val & test val. If same, swap.

same basic problems True.
T / F - Concurrency issues are a concern on multiprocessor systems, but do not impact uniprocessor systems
False, Impact both types of systems.
The following requirement(s) must be met by any facility or capability that is to provide support for mutual exclusion: - 1. Only one process at a time can be allowed
In a uniprocessor system, mutual exclusion can be guaranteed by: - Disabling Interrupts
The situation where Process 1 (P1) holds Resource 1 (R1), while P2 holds R2, and P1 needs R2 to complete an
P2 needs R1 to complete is referred to as deadlock.
When only one process is allowed in its critical code section at a time, then is enforced mutua
exclusion in multiprocessor configurations, special machine instructions that carry out two actions in a single instructio
cycle are said to do so atomically.
The technique in which a process can do nothing until it gets permission to enter its critical section but

```
Exchange Instructions:
const int n = /* # processes */;
const int n = /* # processes */;
                                                        void P(int i) {
void P(int i) {
                                                          int keyi = 1
  while (true) {
    while (compare_and_swap(bolt, 0, 1) == 1)
                                                           while (true) {
                                                             do exchange (keyi, bolt)
       /* do nothing*/:
                                                             while (keyi != 0):
     /* crit section */
                                                             /* crit section */;
    bolt = 0:
                                                             bolt = 0:
     /* remainder */;
                                                             /* remainder */;
,
void main⊖ {
                                                        void main() {
  bolt = 0;
                                                          bolt = 0; bolt = 0;
  parbegin (P(1), P(2), ..., P(n));
                                                          parbegin (P(1), P(2), ..., P(n));
```

Special Machine Instruct Adv: applicable to any # of pros on single/multiple processors sharing main mem, simple & easy to verify, can be used to supp multi crit sects (each can be

Disady: busy-wait employed, while a proc is waiting for access to ctir section it cont to consume processor time, starvation is possible when proc leaves crit sect & >= 1 proc is waiting deadlock is possible

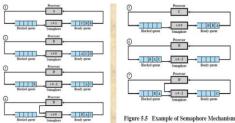
Common Concurrency	y Mechanisms
Semaphore	An integer value used for signaling among processes, Only three operations may be performed on a semaphore, all of which are atomic: initialize, decrement, and increment. The decrement operation may result in the blocking of a process, and the increment operation may result in the unblocking of a process. Also known as a counting semaphore or a general semaphore
Binary Semaphore	A semaphore that takes on only the values 0 and 1.
Mutex	Similar to a binary semaphore. A key difference between the two is that the process that locks the mutex (sets the value to zero) must be the one to unlock it (sets the value to 1).
Condition Variable	A data type that is used to block a process or thread until a particular condition is true.
Monitor	A programming language construct that encapsulates variables, access procedures and initialization code within an abstract data type. The monitor's variable may only be accessed via its access procedures and only one process may be actively accessing the procedure and only one process may be actively accessing the continuous procedures and only one process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process and the continuous process are continuous process. The continuous process are continuous process and the continuous process are continuous process are continuous process. The continuous process are continuous process are continuo
Event Flags	A memory word used as a synchronization mechanism. Application code may associate a different event with each bit in a flag. A thread can wait for either a single event or a combination of events by checking one or multiple bits in the corresponding flag. The thread is blocked until all of the required bits are set (AND) or until at least one of the bits is set (OR).
Mailboxes/Messages	A means for two processes to exchange information and that may be used for synchronization.
Spinlocks	Mutual exclusion mechanism in which a process executes in an infinite loop waiting for the value of a lock variable to indicate

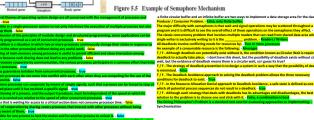
Semaphore: A variable w/ int val where only 3 ops are defined -> no way to inspect/manip semaphores other than those ops: 1) may be init to nonneg int val 2) semWaita op decrements val 3) semSignal op increments val

Cons: no way to know before proc decrements semaphore if block, no way to know which proc will cont immediately on uniproc sys when 2 running concurr, don't know if another proc is waiting so # of unblocked proc is 0 or 1

```
Semaphore Primitives
    struct semaphore {
      int count:
      queueType queue:
     void semWait(semaphore s) {
       s.count--:
      if (s.count < 0) {
         /* place process in squeue */:
           * block this process
     void semSignal(semaphore s) {
       s count++:
       if (s.count \leq 0) {
          * remove process P from s.queue */;
          * place process P on ready list */:
```

Strong semaphores: the proc that's been blocked the longest released fr queue (FIFO) Weak semaphores: order where procs removed from queue not specified





```
Mutual Exclusion using semaphores
   const int n = n /* # processes */
   semaphore s = 1:
   void P(int i) {
      while (true) {
         semWait(s);
         /* crit section */;
         semSignal(s):
         /* remainder */:
    void main() {
      parbegin (P(1), P(2), \dots, P(n));
```

Producer/Consumer Problem: Ensure that produced can't add data into full buff & consumer can't remove data from empty buff

General situation: >= 1 producers gen data & placing in buff where a consumer taking items out individually, but only 1 producer/consumer may access buff at any one time. Solution using semaphores:

```
/* program producerconsumer */
semaphore n = 0, s = 1;
void producer() {
  while (true)
    produce():
     semWait(s):
    append():
    semSignal(s);
    semSignal(n);
void consumer() {
  while (true) {
    semWait(n)
    semWait(s);
    take():
   semSignal(s):
    consume();
void main() {
  parbegin (producer, consumer);
```

Implementation of semaphore: imperative semWait & semSignal ops implemented as atomic primitives in hardware/firmware, Dekker's/peterson's algorithm can be used. Use one of hardware-supported schemes for mutual exclusion

Monitors: construct that provides equiv functionality to semaphores that is easier to control that is implemented in a number of langs, & as program lib. Software module consisting of >= 1 procedures, an init sequence, & local data

Characteristics: local data variables accessible only by mon's procedures (no ext procedure), process enters mon by invoking one of its procedures, only 1 proc exe in mon at a time. Synchronization: achieved by condition variables contained within & only accessible within mon. cwait(c) suspends exe of calling proc on condition c, csignal(c) resumes exe of some proc blocked after cwait on same condition

Solution to bounded-buff prod/cons prob using monitor void producer() char x; while (true) { Afull, automoty end(x); if (count = %) cwait(notfu buffer(nextin) = %; nextin = (nextin = 1) % %; /* one more item is buffer char x; while (true) (oid main() parbegin (producer, consu

Message Passing: when proc interact with another these requirements must be satisfied and msg: synchronization (ensure mut exclu) and comm (to exchange info) Blocking send, Blocking rec: both sender & rec blocked til msg delivered, allowing for tight

Nonblocking send, blocking rec: sender continues but rec blocked til requested msg arrives

Most useful combo. Sends >= 1 msg to to variety destinations as

Nonblocking send, nonblocking rec: neither party required to wait

Readers/Writers Prob: data area shared among many procs, following conditions must be satisfied: any # of readers may read file simultaneously, only 1 writer may write at a time, if writer is writing, no reader may read it



```
Detection, Prevention & Avoidance
Sol to Reader/Wrtiers Prob using semaphore (reader prio)
                                                                                                       Prevention:
                                                                                                           conservative; undercommits resources & imposes restricts. Policies to elim condition
     /* program readersandwriters *
                                                                                                           Requesting all resources at once
     int readcout:
                                                                                                              o Adv: works well for procs performing single activity burst, No preempt needed
      semaphore x = 1, wsem = 1;
                                                                                                              o Disadv: inefficient, delays proc init, future resource reqs must by known
      void reader() {
        while (true)
                                                                                                           Prevention Strats:
                                                                                                              o Indirect: prevents occurrence of 1 of 3 necessary conditions
          semWait(x):
          readcout++:
                                                                                                              o Direct: prevent occurrence of circular wait
          if (readcount == 1) semWait (wsem);

    Pre-emption

                                                                                                              o Adv: convenient when applied to resources whose state saved & stored easily
           semSignal(x):
          READUNITO:
                                                                                                              o Disady: preempts more often than necessary
                                                                                                           Resource ordering
           semWait(x):
                                                                                                              o Adv: feasible to enfore via compile-time checks, needs no run-time comp since prog
          readcount--:
          if (readcount == 0) semSignal(wsem);
                                                                                                                 solved in sys design
           semSignal(x);
                                                                                                              o Disady:disallows incremental resource requests
                                                                                                       Avoidance: Midway b/w detection & prevention. Make dynamic choices based on state of
                                                                                                       resource allocation
                                                                                                           Manip to find at least 1 safe path
      void writer() {
                                                                                                           Adv: no pre-emp and rollback processes needed.
        while (true) {
                                                                                                           Disadv: future resource reqs must be known by OS, procs can be blocked for long
           semWait(wsem)
           WRITEUNIT();
                                                                                                           periods, proc under consideration must be independent & no sync reqs, fixed # of
           semSignal(wsem);
                                                                                                            resources to allocate, no proc may exit while holding resources
                                                                                                           Resource Allocation Denial (Bankers Algo): doesn't grant an incremental resource
                                                                                                           request to proc if might lead to deadlock
                                                                                                           Process Init Denial: doesn't start proc if its demands might lead to deadlock
      void main() {
        readcount = 0:
                                                                                                       Detection: very liberal; requested resources are granted where possible. Detect & take action
        parbegin (reader, writer);
                                                                                                        - Invokes periodically testing for deadlock
                                                                                                           Adv: never delays proc init, facilitates online handling. Disadv: preempt loss & can
 Sol to Reader/Wrtiers Prob using semaphore (writer prio)
           t, writecount;
1, y = 1, z = 1, weem = 1, reem = 1;
                                                                                                       Deadlock Detection Algorithm:
                                                                                                              Mark each process that has a row in the allocation matrix of all zeros.
                                                                                                             Initialize a temporary vector W equal to the available vector.
                                                                                                             Find index i s.t i is unmarked & ith row of Q \le W. If not found, terminate
                                                                                                         4. If such a row is found, mark process i & add the corresponding row of the allocation
                                                                                                       Peterson's Algorithm (Correct Solution taken from slides):
                                                                                                      Need to observe state of both processes, which has right to insist on entering into CS
                                                                                                                                                boolean flag[2];
                                                                                                                                                     int turn;
                                                                                                       Process 0
  Sol to Reader/Writer Prob using Msg Passing
                                                                                                           flag[0] = TRUE:
                                                                                                                                                           flag[1]=TRUE:
                                                                                                           turn = 1:
                                                                                                                                                           turn = 0
                                       if (count > 0) {
   if (impty (finished)) {
     receive (finished, mag);
     receive;
                                                                        while (flag[1] && turn == 1)
                                                                                                                                                           while (flag[0] && turn == 0)
                                                                                                               /* do nothing */;
                                                                                                            /*CS*/
                                                                                                                                                           /*CS*/
                                                                                                           flag[0]=FALSE;
                                                                                                                                                          flag[1]=FALSE;
   oid writer(int 1)
                                                                                                       Bankers Algo
                                                                                                       C = Claim matrix, A = Allo matrix,
                                                                        if (count -- 0) {
    send (writer id, "OK");
    receive (finished, mag);
    count - 190;
                                                                                                       R = Resource vector, A = available vector
                                                                                                      CHAPTER 6: DEADLOCK AND STARVATION
                                                                                                       V = R - A = [9 3 6] - [9 2 5] = [0 1 1]
 Deadlock: permanent blocking of set of procs that compete for sys resources or comm w
each other. Set is deadlocked when each proc in the set is blocked waiting for event that can
                                                                                                       C-A_row \leftarrow V, True, then V = V + A_row
 only be triggered by another blocked process in the set
Resource categories:
Resource categories:

Reusable: can be safefly used by only proc at a time & isn't depleted after use (processors, I/O C-A_1: [2 2 2] > [0 1 1] so fail channels, main & secondary mem, devices & data structs)

Consumable: created/produced & destroyed/consumed. Interrs, sigs, msgs, & info in I/O buff C-A_3: [1 0 3] < [6 2 3] so, new V = [6 2 3] + [2 1 1] = [8 3 4] 

Canada (2 2 0) < [8 3 4] so, new V = [8 3 4] + [0 0 2] = [8 3 6]
   - Mutual Exclusion: one lone process may use resource at a time. If access to resource
                                                                                                       Since not all process were finished, go back to failed P1:
      required it, then it must be supported by OS
                                                                                                       C-A_1: [2 2 2] < [8 3 6], so new V = [8 3 6] + [1 0 0] = [9 3 6]
    Hold-&-Wait: proc may hold allocated resources while awaiting assnt of others. Requires
     proc to request all required resources at one tie & blocking til all requests can be granted
                                                                                                       Given the following matrices Q and A, and the available vector V.
     No pre-emption: no resource can be forcibly removed from proc holding it; if proc
                                                                                                       calculate the R vector and run the deadlock detection algorithm to
                                                                                                       determine the processes that are deadlocked.
     holding certain resources is denied further request, it must first release original resources
     & request them again

    Circular wait: closed chain of procs exists, s.t. each proc hold >= 1 resource needed by

                                                                                                             T1 1 0
     next proc in chain. Define linear ordering of resource types.
                                                                                                             0 1 0
                                                                                                                                      1 0 0
 Resource Allocation Graphs:
                                                                                                             1 0 2
                                                                                                                                A = \begin{bmatrix} 0 & 0 & 1 \end{bmatrix}
                                                                                                                                                           V = [1 \ 1 \ 0]
                                                                                                             1 1 2
                                                                                                                                       1 1 1
                                                                                            R vector = resource vector. Obtain by adding vectors A+V R = \begin{bmatrix} 2 & 3 & 2 \end{bmatrix} + \begin{bmatrix} 1 & 1 & \theta \end{bmatrix} = \begin{bmatrix} 3 & 4 & 2 \end{bmatrix}
                                                                                            Check A to find row of all \theta 's & mark. If no, continue. Find a row in Q <= allocation vector V.
                                                                                            Applicable to P1: V = [1 \ 1 \ \theta] + [\theta \ 1 \ \theta] = [1 \ 2 \ \theta] [1 \ 1 \ \theta] is original vector V, [1 \ 2 \ \theta] is a row from A.
                             Rb
                                                                                            Figure 6.6 Resource Allocation Graph for Figure 6.1b
                                                              (b) Resource is held
                                                                                             Applicable to P2:
                                                                                                Rb
                                                                   Rb
                                                                                            If cannot repeat, the remaining process >= vector V, deadlocked
```

Note: P5 is also supposed to be marked with a *

This is still bankers, just a different method (don't ask me I didn't do this one)

A restaurant has a single employee taking orders and has three seats for its customers The employee can only serve one customer at a time and each seat can only accommodate one customer at a time. Complete the following function template in a way that guarantees that customers will never have to wait for a seat while holding the food

```
semaphore employee = 1;
 void customer ()
       semWait(&seats):
        semWait(&employee);
       order_food();
semSignal(&employee);
       eat();
semSignal(&seats);
} // customer
```

- a) Set the initial values of the semaphores (5 points).
 b) Select the missing instructions after order food() and eat() from the
- following list (15 points): semWait(&employee);
- semWait(&seats): semSignal(&employee):

```
main.cpp €
                     static pthread_mutex_t bsem;
static pthread_cond_t waitTurn = PTHREAD_COND_INITIALIZER;
static int turn;
                          void *print_in_reverse_order(void *void_ptr_argv)
                                             int threadID = *((int*) void_ptr_argv);
pthread_mutex_lock(&bsem);
while(threadID)=turn)
pthread_cond_wait(&waitTurn,&bsem);
| pthread_cond_wait(imutTurn,ibsem); | pthread_cond_wait(imutTurn,ibsem); | pthread_mutex_uniok(ibsem); | pthread_lock(ibsem); | pthread_waitex_lock(ibsem); | pthread_cond_broadcast(imutTurn); | pthread_cond_broadcast(imutTurn); | pthread_cond_untex_uniok(ibsem); | pthread_cond_uniok(ibsem); | pthread_cond_untex_uniok(ibsem); | pthread_cond_uniok(ibsem); | pthr
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  : // if it is the Castro family's turn
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             turn NULL; // return NULL to indicate success
                                                  int nthreads;
                                             int nthreads;
std::cin > nthreads;
std::cin > nthreads;
std::cin > nthreads;
std::cin > nthreads;
std::cin = nthreads;
std::cin = nthreads;
std::cin = nthreads;
std::cin = nthreads - 1;
                                                                     \label{threadNumber[i]} threadNumber[i] = i; \\ pthread\_create(\$tid[i],nullptr,print\_in\_reverse\_order,\$threadNumber[i]); \\ \end{cases}
                                  / Noit for the other threads to finish.

for (int ! 0; ! nihreads; i+)

delete [ threadkumber; delete [ tid;
return 0;

return 0;

return 0;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      strepy(family[i], "RINCON"); // set the name of the family to RINCON to // otherwise strepy(family[i], "CASTRO"); // set the name of the family to CASTRO strepy(family[i], "CASTRO"); // set the name of the family to CASTRO
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 deadlock detection
step 1 get R; R - A + V
step 2 iteratively compare v to row of Q till end
step 2.2 if all q <- v, then v-v+row of A
step 2.3 and mark row of W with *, meaning not deadlocked
step 3 repeat with step 2 with using deadlocked rows till W doesn't change anymore
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 obeallock bankers/avorounce
step 1 get (Y = R - A
step 1.2 get C-A matrix
step 1.2 get C-A matrix
step 2.1 get C-A matrix
step 2.2 if C-a c v, then v-v-row of A
step 2.2 if C-a c v, then v-v-row of A
step 2.3 and mark row of N with ", meaning not deadlocked
step 3 gepeat with step 2 using indecoded ross till W doesn't change anymore
step 4 if no deadlocks left in C-A, then current state is safe
```

oid access one at a time(void family void ptr) // thread functio

hile (busy - true || stremp(fam.turn)!-0) // check if the house is busy or if it is not the famil-

- What is the main objective of the deadlock prevention techniques? [Adopt a policy at the design level to eliminate one of the conditions for deadlock.]
- Explain why in the deadlock detection algorithm, you must mark each process that has a row in the allocation matrix of all zeros? [Because a process without resources cannot be deadlocked.1 . Does disabling the interrupts guarantee mutual exclusion in multiprocessors? Explain your answer [Disabling interrupts will not prevent other processes from executing on a different
- processor and accessing the critical section at the same time.]
- · How does a monitor guarantee mutual exclusion? [The data of the monitor is only accessible thru its methods and only one process can call these methods at a particular time]
- . What is the difference between a livelock and a deadlock? [Livelock = A situation in which two or more processes continuously change their states without doing any useful work.
- Deadlock = A situation in which two or more processes are unable to proceed because each is waiting for one of the others to do something]
- What is the major disadvantage of the special machine instructions? [Busy wait.]
- Select the resource that is NOT reusable: [Messages]
- Select the condition for a deadlock that guarantees that no resource can be forcibly removed from a process holding it: [No Preemption]
- Select the instruction from the correct solution of Peterson's algorithm that represents a busy wait: [while(flag[1] && turn=1)]
- Select the policy used by a weak semaphore to release the processes in the queue after a signalSem: [Not specified]
- Choose the most useful combination when selecting the primitives in the message passing solution: [Nonblocking send, Blocking receive]
- · It is the condition in which multiple processes try to get access to a shared resource at the same time: [Racing condition]
- Select the type of semaphore that is normally used to create a critical section: [Mutex]
- . The OS needs to be concerned about competition for resources when the processes are: [Unaware of each other]
- Select the method that must be part of a program based on monitors to solve for the producer_consumer problem: [take_from_buffer();]
- · Select the primitive that is NOT an atomic operation [None of the above]

Practice Exam 2

- · Concurrency is possible in Uniprocessor systems. [T]
- · A situation in which a runnable process is overlooked indefinitely by the scheduler is: [Starvation]

Old Exam 2

- A Deadlock avoidance mechanism requires knowledge of future process requests [T]
- When a thread calls a signal over a condition variable, if there is no waiting thread on the signaled condition variable, this signal is lost. [T] In message passing, a solution based on mailboxes uses direct addressing [F]
- A disadvantage of the deadlock detection algorithm s that frequent checks consume considerable processor time [T]
- Peterson's algorithm is a hardware-based solution to guarantee mutual exclusion. [F]
- In deadlock avoidance, the solution is executed after assigning the resources to a process. [F]
- Select the matrix of the Banker's Algorithm that is equal to the matrix Q of the deadlock detection algorithm [Matrix C-A]
- Select the concurrency mechanism that is a hardware solution [Exchange Instruction] Sclect the option that is a deadlock prevention approach: [Requesting all resources at offce]

 The code ON THE LEFT comes from a previous practice exam
- · Select the option that is not a requirement for mutual exclusion: [Using the relative process speeds or the number of processes as parameters to guarantee mutual exclusion]
- In the deadlock detection algorithm, if all processes are marked, then: [No deadlock was detected]
 - · Selection that option that is not a recovery strategy of the deadlock detection algorithm [NOT: abort all deadlocked processes, Successively abort deadlocked processes until deadlock no longer exists, Successively preempt resources until deadlock no longer exists]