Haiyu Mao

https://hybol1993.github.io/

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## **EDUCATION**

•	Tsinghua University Ph.D. of Computer Science and Technology; Advisor: Jiwu Shu; GPA: 4.00	Beijing, China  Aug. 2015 – July. 2020
•	University of Florida  Visiting Scholar of Electronic and Computer Engineering; Advisor: Tao Li	Florida, USA  Oct. 2017 – May. 2019
•	Northeastern University Bachelor of Software Engineering; GPA: 90/100	Shenyang, China Aug. 2011 – July. 2015

## Publications

- Haiyu Mao, Mingcong Song, Tao Li, Yuting Dai, and Jiwu Shu.

  "LerGAN: A Zero-Free, Low Data Movement and PIM-Based GAN Architecture." In 2018 51st Annual IEEE/ACM International Symposium on Microarchitecture (MICRO 2018), pp. 669-681. IEEE.
- Haiyu Mao, Xian Zhang, Guangyu Sun, and Jiwu Shu.

  "Protect non-volatile memory from wear-out attack based on timing difference of row buffer hit/miss." In Proceedings of the Conference on Design, Automation Test in Europe (DATE 2017), pp. 1627-1630. European Design and Automation Association.
- Haiyu Mao, Chao Zhang, Guangyu Sun, and Jiwu Shu.
   "Exploring data placement in racetrack memory based scratchpad memory." In 2015 IEEE Non-Volatile Memory System and Applications Symposium (NVMSA 2015), pp. 1-5. IEEE.

## Projects

Selected Awards	
Comprehensive Secondary Scholarship	2017
• Guanghua Scholarship	2016
• Scholarship Provided by The Mayor of Shenyang (6 in the whole university)	2015
• Top 10 Excellent Undergraduates of Northeastern University (10 in the whole university)	2014
• Outstanding Undergraduate in Shenyang (0.26%)	2014
ullet Outstanding Student Pacesetter in Northeastern University (0.5%)	2012, 2013, 2014
• National Scholarship in China (1%)	2012, 2013, 2014

## PROGRAMMING SKILLS

- Programming Languages Proficient: C, C++, Java Used: Javascript, Python, GO, MPI, OpenMP, CUDA, Matlab
- Frameworks: Caffe, TensorFlow
- Simulator: Gem5, DRAMSim, NVSim, NVmain, CACTI, Destiny