



Name of the Institute/ School/ Department: K J Somaiya School of Engineering

Name of the college: 16-KJSSE **Type of Programme:** Full time UG **Class:** TY B. Tech **Semester:** VI

FIRST HALF - 2025

Time Table of End Semester Theory (REG & KT) Examination T.Y.B.Tech-Sem-VI - (SVU 2020)

Candidates for the above examinations are requested to be present at the place of examination, fifteen minutes before the commencement of the examinations and will not be allowed after the half an hour of the commencement of the examinations. The candidates are required to carry the hall ticket and college Identity Card. THEY ARE FORBIDDEN TO CARRY ANY ELECTRONICS GADGETS LIKE MOBILE PHONE, SMART WATCH, ETC., ANY BOOKS, NOTES OR PAPER ETC. IN THE EXAMINATION HALL. Seating arrangement will be displayed on college notice boards on the day of examination. The ESE examinations of T.Y. B Tech (SEM. VI) will be conducted as per the following schedule: -

DAY	MONDAY	WEDNESDAY	FRIDAY	WEDNESDAY	FRIDAY	TUESDAY
DATE	05-May-25	07-May-25	09-May-25	14-May-25	16-May-25	20-May-25
TIME	2.30 pm–5.30 pm	2.30 pm–5.30 pm	2.30 pm–5.30 pm	2.30 pm–5.30 pm	2.30 pm–5.30 pm	2.30 pm–5.30 pm
COMPUTER ENGINEERING	Digital Signal & Image Processing	Departmental Elective-II 1. Applied Cryptography 2. Cloud Computing 3. <i>Data Mining and Business Intelligence</i> 4. <i>Machine Learning</i> 5. <i>Microservices Foundation</i>	Information Security	Artificial Intelligence	Honour & Minor	Honour & Minor
ELECTRONICS AND COMPUTER ENGINEERING	Digital Signal & Image Processing	Computer Communication Networks	Operating Systems and Compilers	Department Elective – II 1. Basics of VLSI 2. Computer Graphics 3. Applied Data Science 4. #Mobile App Development 5. Mobile Communication and Ad hoc Networks	Honour & Minor	Honour & Minor

#The college authority reserves rights to do the minor/major changes in the schedule

Candidates are permitted to use the scientific but not programmable calculator at the examination. Candidates for the examination are expected to provide themselves with charts and drawing instruments if required. The exchange of charts, drawing instruments or any other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances. Any unfair means in the examination hall during the theory examination is punishable as per the rules and regulations

Mumbai – 400 077

DATE: - 31-01-2025

Aux

Prepared and Verified by ACE/IEC

Projet

Controller of Examinations



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ELECTRONICS ENGINEERING	Department Elective-II 1. Data Communication and Networking 2. Electrical Machine 3. Database Management System 4. Data Science Using Python 5. System Design Using FPGA	Basic of VLSI	Analog Integrated Circuits and Applications	Introduction to Automation and Mechatronics	Honour & Minor	Honour & Minor
ELECTRONICS AND TELECOMMUNICATION ENGINEERING	Wireless Communication	Computer Communication Networks	Department Elective-II 1. VLSI Design 2. Machine Learning 3. Cyber Security 4. Internet of Things	Optical fiber Communication	Honour & Minor	Honour & Minor

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INFORMATION TECHNOLOGY	Object Oriented Software Engineering	Modeling and Simulation	Department Elective-II 1. Development Framework 1 2. Internet of Things 3. Vulnerability Analysis And Penetration Testing	Cloud Computing	Honour & Minor	Honour & Minor
MECHANICAL ENGINEERING	Elements of Machine Design	Energy Conversion-II	Computer Aided Engineering	Department Elective-II 1. Automation 2. Non-Conventional Energy Sources 3. Operations Research 4. Automobile Engineering 5. Mechanical Vibrations	Honour & Minor	Honour & Minor

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