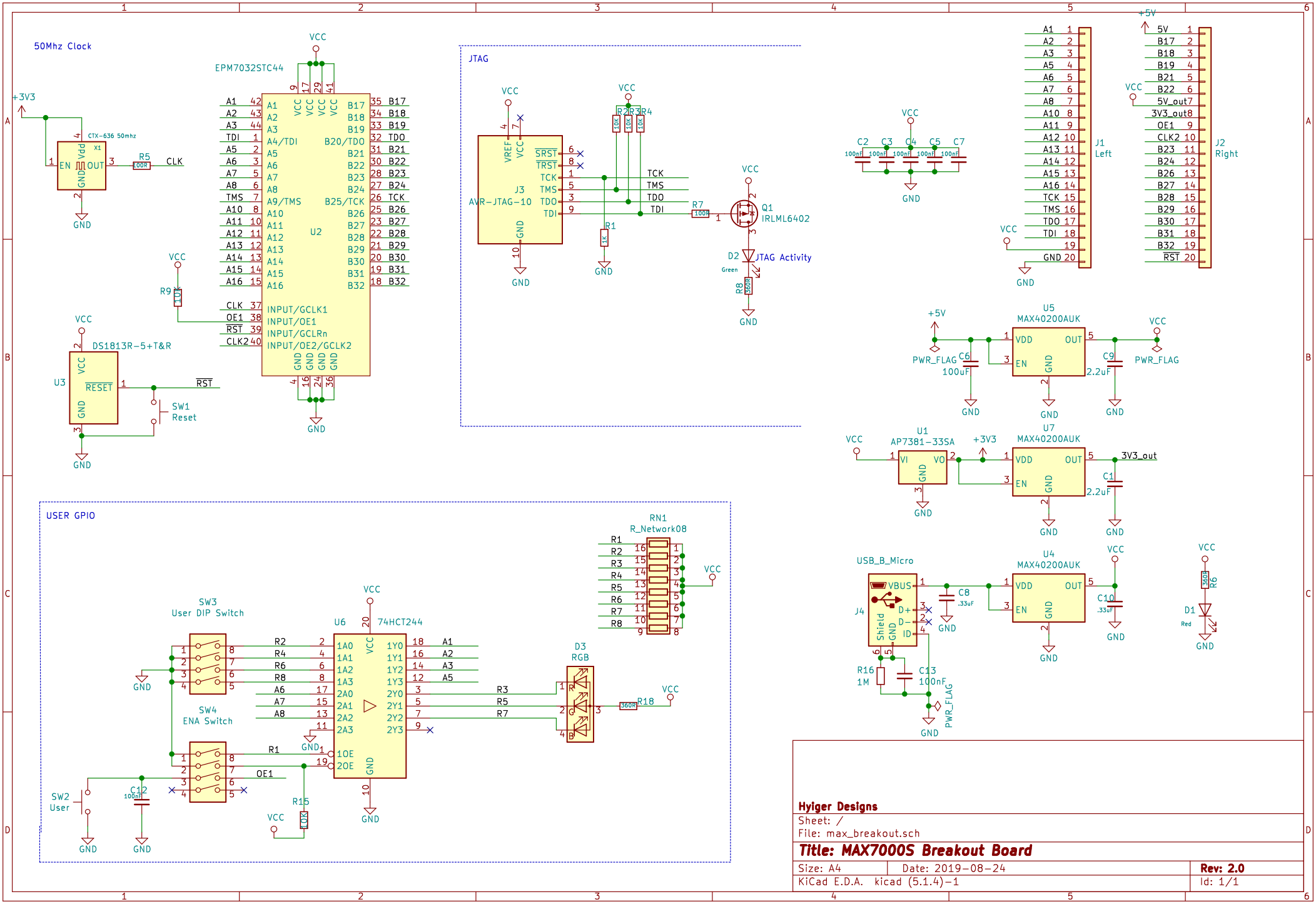
50Mhz Clock
+3V3
CTX-636 50mhz
X1
EN
Vdd
OUT
GND
R5
100R
CLK
EPM7032STC44
U2
INPUT/GCLK1
INPUT/OE1
INPUT/GCLRn
INPUT/OE2/GCLK2
A9/TMS
B25/TCK
B20/TDO
A4/TDI
R9
10K
U3
DS1813R-5+T&R
RESET
RST
SW1
Reset
JTAG
J3
AVR-JTAG-10
VREF
SRST
TRST
TCK
TMS
TDO
TDI
R1
1K
R2
R3
R4
10K
R7
100R
Q1
IRLML6402
D2
Green
JTAG Activity
R8
360R
C2
C3
C4
C5
C7
100nF
J1
Left
J2
Right
5V
3V3_out
OE1
CLK2
U5
MAX40200AUK
PWR_FLAG
C6
100uF
C9
2.2uF
U1
AP7381-33SA
VI
VO
U7
3V3_out
C1
USB_B_Micro
J4
VBUS
D+
D-
ID
Shield
C8
.33uF
U4
C10
R16
1M
C13
100nF
D1
Red
R6
360R
USER GPIO
RN1
R_Network08
SW3
User DIP Switch
SW4
ENA Switch
U6
74HCT244
1A0
1A1
1A2
1A3
2A0
2A1
2A2
2A3
1Y0
1Y1
1Y2
1Y3
2Y0
2Y1
2Y2
2Y3
1OE
2OE
D3
RGB
R18
360R
R15
10K
SW2
User
C12
100nF
Hyiger Designs
Sheet: /
File: max_breakout.sch
Title: MAX7000S Breakout Board
Size: A4
Date: 2019-08-24
Rev: 2.0
KiCad E.D.A. kicad (5.1.4)-1
Id: 1/1