

FPGA-based Accelerated Cloud Computing with AWS EC2 F1 and SDAccel

SDx 2018.2



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Course Objectives

> After completing this course, you will be able to:

- >> Deploy an AWS EC2 F1 instance
- >> Describe accelerated application development framework on an F1 instance
- >> Demonstrate an accelerated application using Makefile method
- >> Describe three development flows in SDAccel
- >> Analyze various generated reports
- >> List and apply various optimization techniques
- >> Use RTL Kernel wizard to create an RTL based design
- >> Debug using ChipScope cores

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Course Outline

The course consists of the following modules:

- > Introduction to AWS EC2 F1
- > Lab: Connecting to AWS EC2 F1 instance
- > SDAccel tool overview
- > Lab: Makefile flow
- > Development flows
- > Lab: Developing application in GUI mode

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Course Outline

- > Optimizing techniques
- > Lab: Optimizing an application
- > RTL Kernel wizard
- > Lab: Creating an RTL kernel using wizard
- > Debugging techniques
- > Lab: Debugging

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Platform Support

- > AWS EC2 F1 instance
- > SDAccel Suite 2018.2
- > Supported Operating Systems
 - >> Cent OS 7.4 and Cent OS 7.5 (64 Bit)

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