

FPGA-based Accelerated Cloud Computing with AWS EC2 F1 and SDAccel

SDx 2018.2



Course Objectives

> After completing this course, you will be able to:

- >> Deploy an AWS EC2 F1 instance
- >> Describe accelerated application development framework on an F1 instance
- >> Demonstrate an accelerated application using Makefile method
- >> Describe three development flows in SDAccel
- >> Analyze various generated reports
- >> List and apply various optimization techniques
- >> Use RTL Kernel wizard to create an RTL based design
- >> Debug using ChipScope cores

Course Outline

The course consists of the following modules:

- > Introduction to AWS EC2 F1**
- > Lab: Connecting to AWS EC2 F1 instance**
- > SDAccel tool overview**
- > Lab: Makefile flow**
- > Development flows**
- > Lab: Developing application in GUI mode**

Course Outline

- > **Optimizing techniques**
- > **Lab: Optimizing an application**
- > **RTL Kernel wizard**
- > **Lab: Creating an RTL kernel using wizard**
- > **Debugging techniques**
- > **Lab: Debugging**

Platform Support

- > **AWS EC2 F1 instance**
- > **SDAccel Suite 2018.2**
- > **Supported Operating Systems**
 - >> Cent OS 7.4 and Cent OS 7.5 (64 Bit)

Adaptable.
Intelligent.

