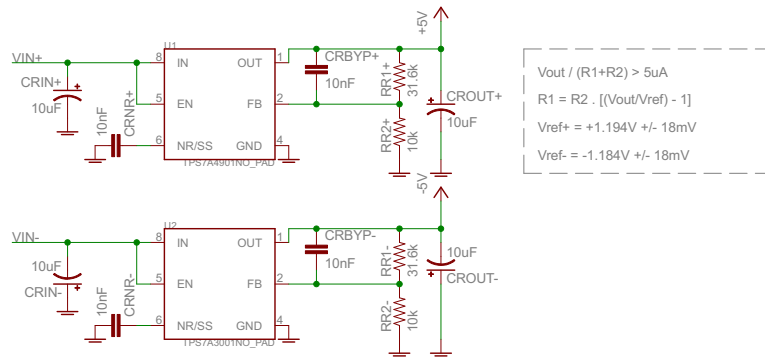
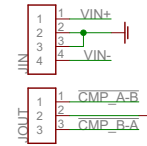


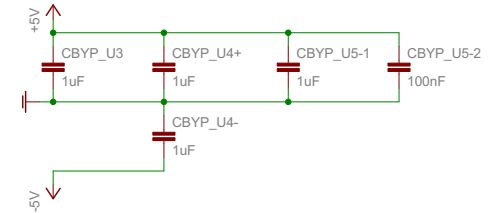
Low-Noise power regulation



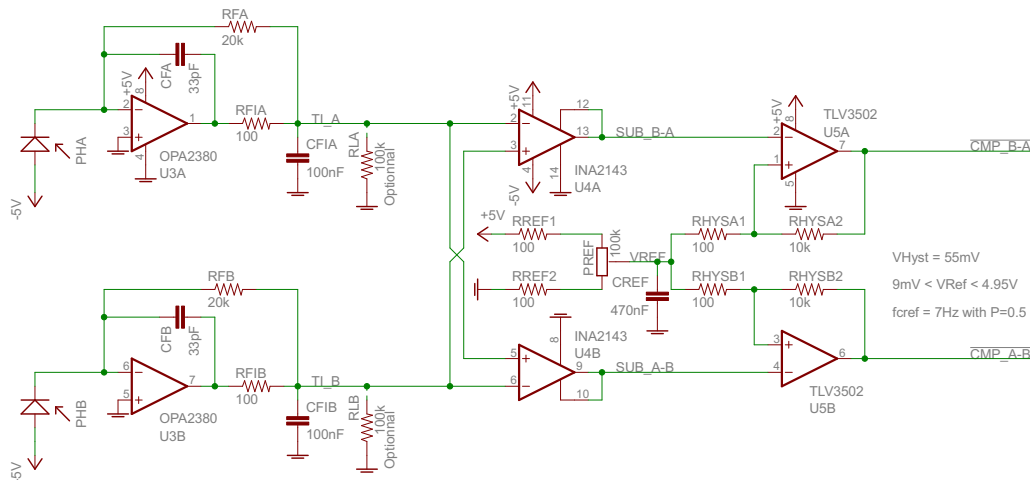
Connectors



Bypass capacitors



Analog chain



$$V_{Ref} = 5V \cdot [(RREF1 + P \cdot PREF) / (RREF1 + RREF2 + PREF)]$$

$$0 < P < 1$$

$$V_{Hyst} = (V_{+} \cdot RHYSA1) / (RHYSA1 + RHYSA2) + 6mV$$

$$f_{cref} \sim 1/[2 \cdot \pi \cdot (P \cdot PREF) \cdot CREF]$$

IGrebot 2013
Beacon V3.0
[PDD] Board

Title:	PDD
Version:	1.0
27/11/2012 23:16:26	
Drawn By:	Paul M.
	1/1