

# A Survey of Graphical Processing Units

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*Abstract—*

## I. INTRODUCTION

## APPENDIX

Notes that won't be included in the final draft:

### A. Topics

List of project topics and the papers that correspond to those topics

- 1) *Memory Models:* [1]
- 2) *Architecture:* [2]
- 3) *Performance:*

## REFERENCES

- [1] A. Bakhoda, G. L. Yuan, W. W. L. Fung, H. Wong, and T. M. Aamodt, "Analyzing cuda workloads using a detailed gpu simulator," in *2009 IEEE International Symposium on Performance Analysis of Systems and Software*, 2009, pp. 163–174.
- [2] W. W. L. Fung and T. M. Aamodt, "Thread block compaction for efficient simt control flow," in *2011 IEEE 17th International Symposium on High Performance Computer Architecture*, 2011, pp. 25–36.