# IMPLEMENTATION OF FACTORIZED CONVOLUTIONAL NEURAL NETWORKS AND ANALYSIS OF THE COMPUTATION WITH DIFFERENT MODELS

# IMPLEMENTATION OF FACTORIZED CONVOLUTIONAL NEURAL NETWORKS AND ANALYSIS OF THE COMPUTATION WITH DIFFERENT MODELS

A thesis submitted to Indian Institute of Technology Kharagpur for B.Tech
Project
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#### **CERTIFICATE**

This is to certify that the work presented in this thesis is entitled Implementation of IMPLEMENTATION OF FACTORIZED CONVOLUTIONAL NEURAL NETWORKS AND ANALYSIS OF THE COMPUTATION WITH DIFFERENT MODELS has been carried out by Sayantan Kirtaniya under my supervision. This is his bonafide work and is worthy of consideration for a B.Tech term project - 2 of the Institute.

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## **DECLARATION**

#### I certify that:

- 1) the work described in this project is original and has been done by me with the support and guidance of my supervisor;
- 2) I have not submitted the work to any other institute for any other degree or diploma;
- 3) I have strictly adhered to ethical norms and guidelines while preparing the thesis;
- 4) I have strictly followed the guidelines presented by the Institute in doing the thesis;
- 5) Whenever I have used materials (data, models, figures, and text) from other sources, I have given due credit to authors and papers by citing them. Details of the citations are available in the references;

Sayantan Kirtaniya

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### **ABSTRACT**

Computation is a big factor when we try to implement the convolutional neural networks so we need so factorization in the layers, CP decomposition can be a technique we can try to implement in the convolutional layers such that the idea of the models helps to get train faster, in this thesis, we are going to impediment wider networks to factorized layers such that the models become efficient and fast, also we are going to analyze the computation time and effect in the accuracy of the model to find out the best solution of our problem statement.

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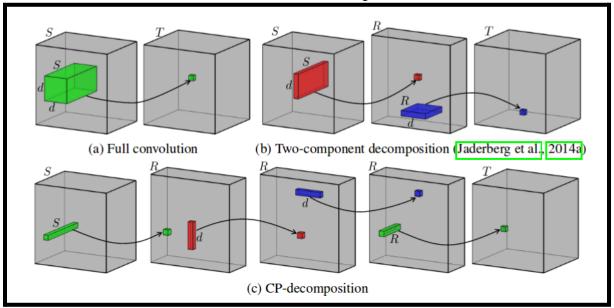
#### 1. Introduction

The main goal of the thesis we are going to target is that we are implementing factorized neural networks in images such that the convolution computation will decrease.

Generally, we can use bottleneck layers to decrease the computation of the neural network. A bottleneck layer's main goal is to minimize the size of the input tensor in a convolutional layer with kernels larger than 1x1 by lowering the number of input channels, or the depth of the input tensor.[1]

The so-called wider convolutional layer is another simple technique to speed up convolutions.

Separable Convolutions in Depth, Instead of convolution simultaneously over all channels of an image, we execute a separate 2D convolution with a depth of channel multiplier on each channel. The intermediate channels in channels \* channel multipliers are concatenated and mapped to out channels using a 1x1 convolution. As a result, there are much fewer parameters to train.



Source: V. Lebedev et al, Speeding-up Convolutional Neural Networks Using Fine-tuned CP-Decomposition Numerous publications, including those by V. Lebedev et al., demonstrate various tensor decomposition strategies that dramatically reduce the number of parameters and, as a result, the number of calculations necessary.[2]

TensorTrain decomposition and Tucker are two examples of such techniques. Tensorly is a fantastic package for PyTorch and NumPy that handles all of the low-level implementations for us. Although there is nothing comparable in TensorFlow, there is a TensorTrain aka TT scheme implementation.[4]

#### 2. Conceptual areas and algorithms

Tensor decompositions are a logical technique to apply a low-rank approach to a multidimensional situation. Remember that the low-rank decomposition of a matrix *A* of dimension n m and rank R is:

$$A(i,j) = \sum_{r=1}^{R} A_1(i,r)A2(j,r), i = \overline{1,n}, j = \overline{1,m},$$

[6]This leads to the concept of variable separation. The canonical polyadic decomposition (CP-decomposition, also known as CANDECOMP/PARAFAC model) is the easiest technique to separate variables in cases of multiple dimensions (Kolda & Bader, 2009). For a d-dimensional array A, of size  $n_1 \times \cdots \times n_d$  a CP-decomposition has the following form:

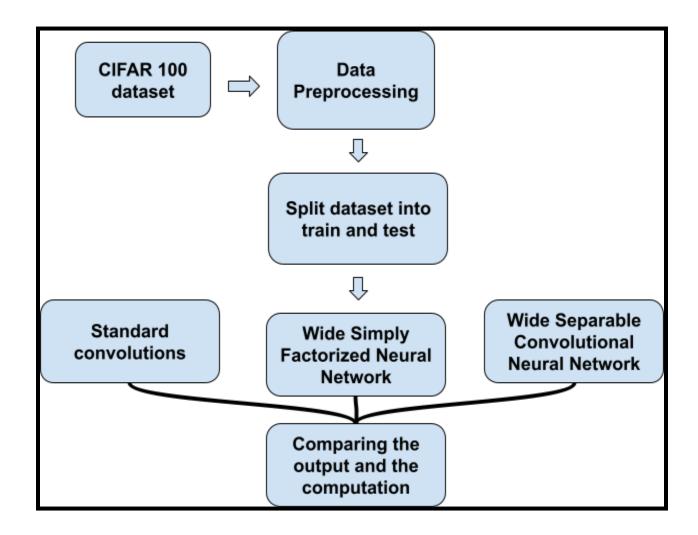
$$A(i_1,...,i_d) = \sum_{r=1}^{R} A_1(i_1,r)...A_d(i_d,r)$$

Canonical rank refers to the lowest possible R. The benefit of this decomposition is that we just need to save a little amount of data, and elements  $(n_1 \times \cdots \times n_d)R$  instead of the whole tensor with  $n_1 \dots n_d$  elements.

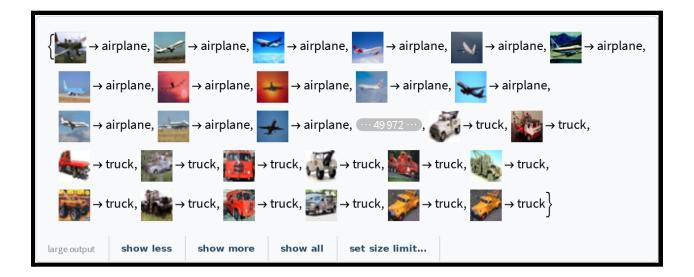
For **library** usage we are going to use the Tensorly - Pytorch and Numpy for the computational purposes.

## 3. Proposed idea

Here is the detailed workflow that we are going to implement in the next part:



We are going to take the help with the CIFAR 100 dataset, which has 100 different classes. It has 100 classes containing 600 images each. There are 500 training images and 100 testing images per class. The 100 classes in the CIFAR-100 are grouped into 20 superclasses.[5]



And then we will use a simple convolutional model with standard layers, a wider model with simple factorized layers and wider separable convolutional layer network. And then we will analyze the accuracy and then we are going to understand the time of the computational difference with networks.

We will train each of the models for 5 epochs to understand the computational differences as well as the accuracy of the different models.

## 4. <u>Implementation of the models</u>

The models we are going to implement are a simple convolutional model with standard layers, a wider model with simple factorized layers and a wider separable convolutional layer network.[7]

Let's look at the models architecture which we have implemented is given here,
The **simple convolutional model** is:

Lever (type)

Output Shape

Perem #

Layer (type)	Output Shape	Param #	
== input_1 (InputLayer)	(None, 32, 32, 3	3) 0	
conv2d_1 (Conv2D)	(None, 32, 32,	96) 2688	
batch_normalization_l	(Batch (None, 32,	32, 96) 384	
activation_1 (Activatio	n) (None, 32, 32,	96) 0	
conv2d_2 (Conv2D)	(None, 32, 32,	96) 83040	
batch_normalization_2	2 (Batch (None, 32,	32, 96) 384	
activation_2 (Activation	on) (None, 32, 32,	96) 0	
conv2d_3 (Conv2D)	(None, 16, 16,	96) 83040	
batch_normalization_3	B (Batch (None, 16,	16, 96) 384	
activation_3 (Activation	on) (None, 16, 16,	96) 0	
conv2d_4 (Conv2D)	(None, 16, 16,	192) 166080	
batch_normalization_4	4 (Batch (None, 16,	16, 192) 768	
activation_4 (Activation_4)	on) (None, 16, 16,	192) 0	
conv2d_5 (Conv2D)	(None, 16, 16,	192) 331968	<del></del>

atch (None, 16, 16, 192	) 768
(None, 16, 16, 192)	0
(None, 8, 8, 192)	331968
atch (None, 8, 8, 192)	768
(None, 8, 8, 192)	0
(None, 8, 8, 192)	331968
atch (None, 8, 8, 192)	768
(None, 8, 8, 192)	0
(None, 8, 8, 192)	37056
atch (None, 8, 8, 192)	768
(None, 8, 8, 192)	0
(None, 8, 8, 100)	19300
atch (None, 8, 8, 100)	400
(None, 8, 8, 100)	0
_1 ( (None, 100)	0
(None, 100)	0
	(None, 16, 16, 192)  (None, 8, 8, 192)  (Atch (None, 8, 8, 192)  (None, 8, 8, 190)  (Atch (None, 8, 8, 100)  (None, 8, 8, 100)  (None, 8, 8, 100)  (None, 8, 8, 100)

==

Total params: 1,392,500 Trainable params: 1,389,804 Non-trainable params: 2,696

The model which is: a wider model with simple factorized layers

\_\_\_\_\_

Layer (type)	Output Shape	Param #
==		
input_5 (InputLayer)	(None, 32, 32, 3)	0
conv2d_91 (Conv2D)	(None, 32, 32, 1	192) 1920
conv2d_92 (Conv2D)	(None, 32, 32, 1	192) 110784
batch_normalization_7	70 (Batc (None, 32, 32	2, 192) 768
activation_79 (Activation_79)	on) (None, 32, 32, 19	92) 0
conv2d_93 (Conv2D)	(None, 16, 16, 9	26) 165984
batch_normalization_7	71 (Batc (None, 16, 16,	, 96) 384
activation_80 (Activati	on) (None, 16, 16, 90	6) 0
conv2d_95 (Conv2D)	(None, 16, 16, 3	84) 110976
conv2d_96 (Conv2D)	(None, 16, 16, 3	84) 442752
batch_normalization_7	72 (Batc (None, 16, 16	5, 384) 1536
activation_81 (Activation_81)	on) (None, 16, 16, 38	84) 0
conv2d_97 (Conv2D)	(None, 8, 8, 128	3) 442496
batch_normalization_7	73 (Batc (None, 8, 8, 1	128) 512
activation_82 (Activati	on) (None, 8, 8, 128	3) 0
conv2d_98 (Conv2D)	(None, 8, 8, 256	6) 295168
batch_normalization_7	<sup>7</sup> 4 (Batc (None, 8, 8, 2	256) 1024
activation_83 (Activati	on) (None, 8, 8, 256	5) 0

conv2d_99 (Conv2D)	(None, 8, 8, 100)	25700	
batch_normalization_75 (B	atc (None, 8, 8, 100)	400	
activation_84 (Activation)	(None, 8, 8, 100)	0	
global_average_pooling2d_	10 (None, 100)	0	
activation_85 (Activation)	(None, 100)	0	

Total params: 1,600,404 Trainable params: 1,598,092 Non-trainable params: 2,312

#### The architecture of $separable\ convolutional\ layer\ network\ is$ :

Layer (type)	Output Shape	 Param #	
== input_4 (InputLayer)	(None, 32, 32,	3) 0	
separable_conv2d_23	(Separab (None, 32	2, 32, 192) 795	
batch_normalization_	55 (Batc (None, 32,	, 32, 192) 768	
activation_62 (Activat	ion) (None, 32, 32	2, 192) 0	
conv2d_68 (Conv2D)	(None, 16, 16	5, 96) 165984	
batch_normalization_	56 (Batc (None, 16,	16, 96) 384	
activation_63 (Activat	ion) (None, 16, 16	, 96) 0	
separable_conv2d_24	(Separab (None, 16	5, 16, 384) 38112	
batch_normalization_	57 (Batc (None, 16,	16, 384) 1536	

activation_64 (Activation)	(None, 16, 16, 384)	0	
conv2d_70 (Conv2D)	(None, 8, 8, 128)	442496	
batch_normalization_58 (B	Batc (None, 8, 8, 128)	512	
activation_65 (Activation)	(None, 8, 8, 128)	0	
conv2d_71 (Conv2D)	(None, 8, 8, 256)	295168	
batch_normalization_59 (B	Batc (None, 8, 8, 256)	1024	
activation_66 (Activation)	(None, 8, 8, 256)	0	
conv2d_72 (Conv2D)	(None, 8, 8, 100)	25700	
batch_normalization_60 (B	Batc (None, 8, 8, 100)	400	
activation_67 (Activation)	(None, 8, 8, 100)	0	
global_average_pooling2d_	8 ( (None, 100)	0	
activation_68 (Activation)	(None, 100)	0	

==

Total params: 972,879 Trainable params: 970,567 Non-trainable params: 2,312

Lets us look at the results of each of the models and observe the output as well as the discussion of the results.

#### 5. Results

For the model 1: **simple convolutional model**, the results are shown below,

```
Epoch 1/5
1562/1562 [==========] - 36s 23ms/step - loss:
3.9801 - categorical_accuracy: 0.1236 - top_k_categorical_accuracy: 0.3421
Epoch 2/5
3.5620 - categorical_accuracy: 0.2003 - top_k_categorical_accuracy: 0.4800
Epoch 3/5
1562/1562 [===========] - 33s 21ms/step - loss:
3.3145 - categorical_accuracy: 0.2480 - top_k_categorical_accuracy: 0.5488
Epoch 4/5
1562/1562 [===========] - 32s 21ms/step - loss:
3.1350 - categorical_accuracy: 0.2808 - top_k_categorical_accuracy: 0.5931
Epoch 5/5
2.9940 - categorical_accuracy: 0.3082 - top_k_categorical_accuracy: 0.6263
Accuracy: 0.31860977564102566, Top5 Accuracy 0.630208333333334 and Loss
2.879887064297994.
```

For the model 2: **a wider model with simple factorized layers**, the results are shown below,[8]

For the model 3: **separable convolutional layer network**, the results are shown below,

Let's look at the three results side by side,

Models	AVG. Time of computation	Accuracy	TOP 5 Accuracy	Loss
simple convolutional model	33s 21ms/step	0.318	0.630	2.879
A wider model with simple factorized layers	49s 31ms/step	0.292	0.590	3.020

separable	28s 18ms/step	0.278	0.576	3.142
convolutional				
layer network				

- By analyzing the model results we can say that the computational time has decreased by a good amount but the accuracy have decreed by a vault of 0.14, which may be affect the model robustness.
- The loss and Top 5 accuray for the test set are also decreased for the wider simple and wider factorized models, so we have partially made the model better based on its computation time.
- There is a large possibility that the model accuracy will get better because of the increase in the higher number of epochs.

### 6. <u>Future work</u>

- In the future we can try to implement more models such that we can get better outputs and the models become more robust.
- We use these models in the different medical datasets as there is a very less number of factorized layers implemented in the areas of neural networks.
- We can make ensemble models from the outcomes of the different models and make the output with better accuracy.

#### 7. Reference

#### Full citations information and papers information:

- [1] F. Chollet, Xception: Deep Learning with Depthwise Separable Convolutions
- [2] V. Lebedev et al, Speeding-up Convolutional Neural Networks Using Fine-tuned CP-Decomposition
- [3] C. Szegedy et al, Rethinking the Inception Architecture for Computer Vision
- [4] S. Zagoruyko and N. Komodakis, Wide Residual Networks
- [5] Speeding-up convolutional neural networks using fine-tuned cp-decomposition
- [6] Factorized Convolutional Neural Networks
- [7] Factorized Convolution Kernels in Image Processing
- [8] TENSORFLOW TOOLS