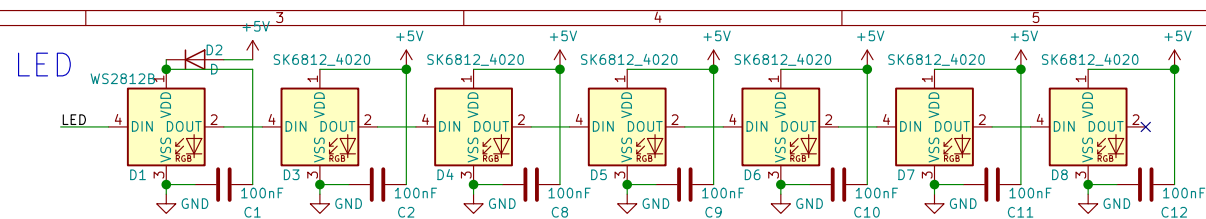


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SchumyHao

[illegible]

The image displays three circuit diagrams illustrating the connection of an ESP8266 module to an STM32F103C8T6 microcontroller.

Diagram 1: Microcontroller Pin Connections

The STM32F103C8T6 microcontroller is shown with its pins connected to the ESP8266 module. The connections are as follows:

- Power:** VDD (pin 2) is connected to +3V3, and GND (pin 1) is connected to GND.
- Reset:** RESET (pin 24) is connected to E18_RST.
- GPIO:** P0.0 (pin 23) is connected to E18_RX, P0.1 (pin 22) is connected to E18_TX, P0.2 (pin 21) is connected to E18_TX, P0.3 (pin 20) is connected to E18_TX, P0.4 (pin 19) is connected to E18_TX, P0.5 (pin 18) is connected to E18_TX, P0.6 (pin 17) is connected to E18_TX, and P0.7 (pin 16) is connected to E18_TX.
- Other:** P1.0 (pin 15) is connected to GND, P1.1 (pin 14) is connected to GND, P1.2 (pin 13) is connected to GND, P1.3 (pin 12) is connected to GND, P1.4 (pin 11) is connected to GND, P1.5 (pin 10) is connected to GND, P1.6 (pin 9) is connected to GND, and P1.7 (pin 8) is connected to GND.

Diagram 2: ESP8266 Pin Connections

The ESP8266 module is shown with its pins connected to various components. The connections are as follows:

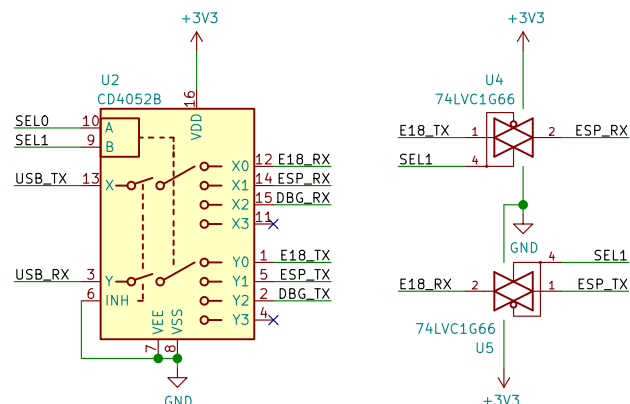
- Power:** VDD (pin 1) is connected to +3V3, and GND (pin 9) is connected to GND.
- Reset:** RST (pin 15) is connected to E18_RST.
- GPIO:** EN (pin 2) is connected to GND, RST (pin 15) is connected to E18_RST, FOUT (pin 16) is connected to GND, RXD (pin 11) is connected to E18_RX, and TXD (pin 12) is connected to E18_TX.
- Other:** IO0 (pin 100) is connected to GND, IO2 (pin 102) is connected to GND, IO4 (pin 104) is connected to GND, IO5 (pin 105) is connected to GND, IO12 (pin 1012) is connected to GND, IO13 (pin 1013) is connected to GND, IO14 (pin 1014) is connected to GND, IO15 (pin 1015) is connected to GND, and IO16 (pin 1016) is connected to GND.

Diagram 3: ESP8266 Pin Connections

The ESP8266 module is shown with its pins connected to various components. The connections are as follows:

- Power:** VDD (pin 1) is connected to +3V3, and GND (pin 9) is connected to GND.
- Reset:** RST (pin 15) is connected to E18_RST.
- GPIO:** EN (pin 2) is connected to GND, RST (pin 15) is connected to E18_RST, FOUT (pin 16) is connected to GND, RXD (pin 11) is connected to E18_RX, and TXD (pin 12) is connected to E18_TX.
- Other:** IO0 (pin 100) is connected to GND, IO2 (pin 102) is connected to GND, IO4 (pin 104) is connected to GND, IO5 (pin 105) is connected to GND, IO12 (pin 1012) is connected to GND, IO13 (pin 1013) is connected to GND, IO14 (pin 1014) is connected to GND, IO15 (pin 1015) is connected to GND, and IO16 (pin 1016) is connected to GND.

1\0	0	1
0	opt1	opt2
1	opt3	opt4



z2m partner

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