Bluetrum Technology

AB5305A

Audio Player Microcontroller

Versions: 0.0.1

2018/09/27

Declaration

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Bluetrum Technology

Revision History

Date	Version	Comments	Revised by
2018-09-27	0.0.1	First draft	Leo

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1 Product Features

CPU and Flexible IO

- 32bit High performance CPU with DSP instruction
- Flexible GPIO pins with Programmable pull-up and pull-down resistors;
- Support GPIO wakeup or interrupt;

Bluetooth Radio

- Compliant to Bluetooth 5.0 and BLE specification (QDID: 115952);
- TX output power +2db in typical;
- RX Sensitivity with -90.5dBm @Basic Rate;

FM Tuner

- ♣ Support frequency band 76~108MHz;
- Auto search tuning;
- Programable de-emphasis(50/75uS);
- Receive signal strength indicator (RSSI);

Audio Interface

- Audio codec with 16bit stereo DAC and two channel 16bit ADC;
- Support flexible audio EQ adjust;
- Support Sample rate 8, 11.025, 12, 16, 22.05, 32, 44.1 and 48KHz;
- 4 channel Stereo Analog MUX;
- Two channel MIC amplifier input;
- High performance Stereo audio ADC with 90dB SNR;

High performance Stereo audio DAC with 95dB SNR, with headphone amplifier output;

Peripheral and Interfaces

- Three 32-bit timers;
- Three multi-function 32-bit timers, support Capture and PWM mode;
- WatchDog;
- Three full-duplex UART;
- ♣ SPI;
- ♣ IR controller;
- SD Card Host controller;
- Full speed USB 2.0 HOST/DEVICE controller;
- Sixteen Channels 10-bit SARADC;
- Build in PMU, such as LDO;

Package

♣ SSOP24L;

Temperature

- **↓** Operating temperature: -40° \mathbb{C} to +85° \mathbb{C} ;
- **♣** Storage temperature: -65 $^{\circ}$ C to +150 $^{\circ}$ C;

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2 Package Definition

2.1 Pin Assignment

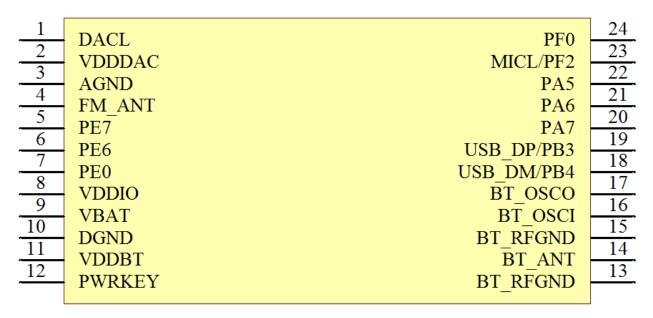


Figure 2-1 Pin assignment for SSOP24L

2.2 Pin Descriptions

Table 2-1 SSOP24L pin description

Pin No.	Name	Туре	Function
1	DACL	А	DAC L
2	VDDDAC	PWR	DAC power
3	AGND	GND	DAC Ground
4	FM_ANT	Α	FMRX ANT
			ADC9
			AUXR2
			SDDAT0-G3
			SPI1DO-G4
5	PE7	I/O	TX0-G4
			HSTRX-G4
			LPWM3-G1
			TMR4CAP_G1/IR_G8
			PE7
			ADC8
6	PE6	I/O	AUXL2
			SPDIF4

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			SDCLK-G3
			SPI1CLK-G4
			RX0-G4
			HSTRX-G9
			FMOSC-G6
			LPWM2-G1
			TMR3CAP_G7/IR_G7
			PE6
			SPI0DI-G3
			TX0-G6
			LPWM2-G2
7	PE0	I/O	
			IISDI-G2
			TMR3CAP_G5/IR_G5
			PE0
8	VDDIO	PWR	VDDIO power output
9	VBAT DGND	PWR GND	VBAT power input Digital Ground
11	VDDBT	PWR	BT power
12	PWRKEY	A	Power key input
13	BT_RFGND	GND	BT RF Ground
14	BT_ANT	A	BT ANT
15	BT_RFGND	GND	BT RF Ground
16	BT_OSCI	Α	26M OSC input
17	BT_OSCO	A	26M OSC output
			ADC6
			USB DM
18	USB_DM/PB4	I/O	SPI0CLK-G3
			RX0-G3
			PB4
			ADC5
			USB DP
19	USB_DP/PB3	I/O	SPI0DO-G3
	000_0171 00	1,0	
			TX0-G3
			PB3
			ADC2
			AUXR0
			SDDAT0-G1
			SPI1DO-G2
20	PA7	I/O	TX0-G1
			TX1-G1
			HSTRX-G1
			PW M2-T4
		ļ	PA7
			ADC1
			AUXL0
21	PA6	I/O	SDCLK-G1
			SPI1CLK-G2
			RX0-G1
	1		

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RX1-G1 HSTRX-G6 FMOSC-G2 PWM1-T4 TMR3CAP_G2/IR_G2 PA6 ADC0 SDCMD-G1 SPI1DI-G2 22 PA5 I/O FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3 SPI1DI-G5		ı	T	
FMOSC-G2 PWM1-T4 TMR3CAP_G2/IR_G2 PA6 ADC0 SDCMD-G1 SPI1DI-G2 FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				RX1-G1
PWM1-T4 TMR3CAP_G2/IR_G2 PA6 ADC0 SDCMD-G1 SPI1DI-G2 FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				HSTRX-G6
TMR3CAP_G2/IR_G2 PA6 ADC0 SDCMD-G1 SPI1DI-G2 FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				FMOSC-G2
PA6 ADC0 SDCMD-G1 SPI1DI-G2 PMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				PWM1-T4
22 PA5 I/O FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				TMR3CAP_G2/IR_G2
SDCMD-G1 SPI1DI-G2 PA5 I/O FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				PA6
22 PA5 I/O FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				ADC0
22 PA5 I/O FMOSC-G1 PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3		PA5 I/O		SDCMD-G1
PWM0-T4 TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				SPI1DI-G2
TMR3CAP_G1/IR_G1 PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3	22	PA5	I/O	FMOSC-G1
PA5 ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				PWM0-T4
ADC10 MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				TMR3CAP_G1/IR_G1
23 MICL/PF2 I/O MICL SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				PA5
23 MICL/PF2 I/O SPI1DO-G5 TX0-G7 LPWM3-G2 PF2 AUXL3				ADC10
23 MICL/PF2 I/O TX0-G7 LPWM3-G2 PF2 AUXL3				MICL
TX0-G7 LPWM3-G2 PF2 AUXL3	22	MICL/PE2	1/0	SPI1DO-G5
PF2 AUXL3	23	IVIICL/FF2	1/0	TX0-G7
AUXL3				LPWM3-G2
				PF2
24 PF0 I/O SPI1DI-G5				AUXL3
	24	PF0	I/O	SPI1DI-G5
PF0				PF0

Note: I/O: Digital input/output; I: Digital input; A: Analog Pin; PWR: Power Pin; GND: Ground.

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3 Characteristics

3.1 PMU Parameters

Table 3-1 PMU voltage input Parameters

<u> </u>							
Sym	Characteristics	Min	Тур	Max	Unit	Conditions	
VUSB	Charger Voltage input	3.0	5.0	5.0	V		
VBAT	Voltage input	3.0	3.7	5.0	V		

Table 3-2 3.3V LDO Parameters

Sym	Characteristics	Min	Тур	Max	Unit	Conditions
VDDIO	3.3V LDO voltage output	3.0	3.3	3.6	V	Light Loading condition
△VVDDIO	Output Mismatch 1-sigma	-	56	-	mV	VDDIO=3.3v
ILOAD	Maximum output current	-	-	150	mA	@VBAT=3.6v
ISC	Short Circuit Current Limit	-	-	300	mA	@VBAT=3.8v

Table 3-3 1.6V LDO Parameters

Sym	Characteristics	Min	Тур	Max	Unit	Conditions
VDDBT	1.6V LDO voltage output	-	1.6	-	V	Light Loading condition
△VVDDBT	Output Mismatch 1-sigma	-	27	-	mV	VDDBT=1.6v
ILOAD	Maximum output current	-	-	100	mA	@VBAT=3.0v
ISC	Short Circuit Current Limit	-	-	200	mA	@VBAT=3.8v

Table 3-4 1.2V LDO Parameters

Sym	Characteristics	Min	Тур	Max	Unit	Conditions
VDDCORE	1.2V LDO voltage output	-	1.2	-	V	Light Loading condition
△VVDDCORE	Output Mismatch 1-sigma	-	20	-	mV	VDDCORE=1.2v
ILOAD	Maximum output current	-	-	80	mA	@VBAT=3.6v
ISC	Short Circuit Current Limit	-	-	120	mA	@VBAT=3.8v

3.2 IO Parameters

Table 3-5 I/O Parameters

		14516 5 5 17 5 1	a.a	•			
GPIO—Electrica	al Characteristics						
Symbol	Description	Related GPIO	Min	Typical	Max	Units	Conditions
V _{IL}	Low-level input voltage		-0.3		1.27	V	VDDIO=3.3V
VIH	High-level input voltage		2.03		3.6	V	VDDIO=3.3V
Driver Ability 1	Output Driver Ability 1			32		mA	VDDIO=3.3V
Driver Ability 0	Output Driver Ability 0			8		mA	VDDIO=3.3V
R _{PUP0}	Internal pull-up resister 0		8	10	12	ΚΩ	
R _{PUP1}	Internal pull-up resister 1		0.24	0.3	0.36	ΚΩ	
R _{PUP2}	Internal pull-up resister 2		160	200	240	ΚΩ	
R _{PDN0}	Internal pull-down resister 0		8	10	12	ΚΩ	
R _{PDN1}	Internal pull-down resister 1		0.24	0.3	0.36	ΚΩ	
R _{PDN2}	Internal pull-down resister 2		160	200	240	ΚΩ	

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3.3 Audio DAC Parameters

Table 3-6 Audio DAC Parameters

Sym	Characteristics	Min	Тур	Max	Unit	Conditions
						VCM cap=1uF
						VDDDAC cap=1uF
SNR		-	96	-	dB	with A-wt filter
						Output -3dBV
						Fin=1KHz
						VCM cap=1uF
			-86	-	dB	VDDDAC cap=1uF
THD+N		-				with A-wt filter
						Output -3dBV with 10K loading
						Fin=1KHz
Output Range	Maximum output voltage	•	2.6		V _{peak-peak}	32ohm Loading

3.4 Audio ADC Parameters

Table 3-7 Audio ADC Parameters

Sym	Characteristics	Min	Тур	Max	Unit	Conditions
						VCM cap=1uF
						VDDDAC cap=1uF
SNR		-	90	-	dB	with A-wt filter
						Input sine amplitude, 850mV
						RMS
						Fin=1KHz
		-	-87	-		VCM cap=1uF
						VDDDAC cap=1uF
THD+N					dB	with A-wt filter
						Input sine amplitude, 850mV
						RMS
						Fin=1KHz.
Input Pange	Input sine wave peak amplitude	0		VCM	V	From aux input, aux 0db gain,
Input Range		U		V CIVI	V	VCM represent VCM voltage.

3.5 BT Parameters

Table 3-8 BT Parameters

Characteristics	Min	Typical	Max	Unit	Conditions						
Maximum Transmit Power	-	2	-	dBm							
RMS DEVM	-	5.5	-	%	Maximum TX power 2-DH5 packet						
Peak DEVM	-	12.5		%							
EDR Relative Transmit Power		-0.2		dB							
Sensitivity @ Basic Rate		-90.5		dBm	BER=0.1%, using DH5 packet						
Sensitivity @ EDR		-89.5		dBm	BER=0.01%, using 2-DH5 packet						

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3.6 Current Parameters

Table 3-9 Current Parameters

Sym	Characteristics	Min	Тур	Max	Unit	Conditions
IRTC	RTC mode current	-	4	-	uA	4.2V input, room temp.
Sleep	Sleep current	-	500	2000	uA	3.3V input, room temp

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