

Trần Thanh Tĩnh

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Ho Chi Minh, Vietnam



EDUCATION

University of Information Technology

Faculty: Computer Engineering

Aug 2014 – Sep 2018

GPA: 7.12/10

RESEARCH

University of Information Technology

Project

Aug 2017 - Present

Design IP core part used to motion detect.

Key capabilities:

- Using Hardware Description Languages (Verilog HDL).
- C/C++ on Linux.
- Using AXI-Stream bus, AXI-Lite bus.

Jan 2017 - Jun 2017

Design Gabor filter used in processing fingerprint

Main abilities:

- Using Hardware Description Languages (Verilog HDL).
- Using digital signal processing knowledge

ACTIVITIES

Part-time Job

AEC center (American English Communication center)

Jan 2017 - Present

- Program counseling.
- Trainee management.
- Education supporter

SKILLS

- C/C++, Java, PHP, Verilog HDL.
- Processing image, video.
- English communication skills.