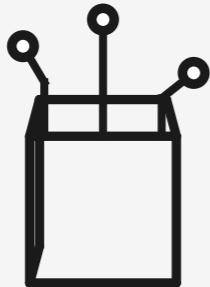


# Circuit Forge - Iterations

Isometric cube + circuit traces - refining the concept

---

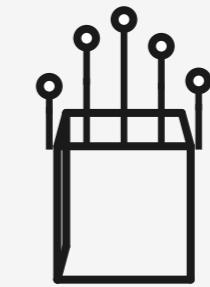
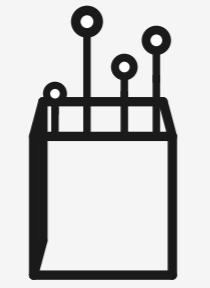


**ARCHFORGE**

16 V1

**Original Style**

Refined version of  
your draft



**ARCHFORGE**

17 V2

**Network Spread**

5 traces fanning  
outward

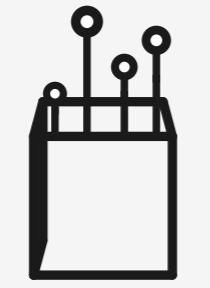


**ARCHFORGE**

18 V3

**Asymmetric Growth**

Dynamic varying  
heights



**ARCHFORGE**

19 V4

**Minimal Three**

Ultra-clean  
symmetrical