CLEAR S CLEAR S BOOTSTF	TORAG		L0681	15,022026,030037,044,049,053053N000000N00001026 16,105106,110117B101/I9I#071029C029056B026/B001/0991 15,022029,036040,047054,061068,072/061039					1 2 3
				FORTRAN COMPILER IF COND PHASE PHASE 44				PAGE	1
SEQ PG	GLIN	LABEL	OP	OPERANDS	SFX CT	LOCN	INSTRUCTION	TYPE	CARD
101			JOB	FORTRAN COMPILER IF COND PHASE PHASE 44					
102			CTL	6611					
103		*							
104				STRUCTIONS ARE GENERATED FOR IF ( SENSE SWITCH I )					
105 106		* AND :	IF (S	ENSE LIGHT I )					
106		x1	EOU	89		0089			
108		X2	EQU	94		0003			
109		X3	EQU	99		0099			
110		*	~ -						
111		* STUF	IN T	HE RESIDENT AREA					
112		*							
113		PHASID	-	110 PHASE ID, FOR SNAPSHOT DUMPS		0110			
114				184 GLOBAL ERROR FLAG WM MEANS ERROR		0184			
115				333 CORE DUMP SNAPSHOT		0333			
116				700 LOAD NEXT OVERLAY		0700 0707			
117 118		CDOVLY		707 CS AT START OF OVERLAY LOADER 769 1 IF RUNNING FROM CARDS, N IF FROM TAPE		0769			
119		*	EQU	709 I IF KONNING FROM CARDS, N IF FROM TAFE		0703			
120			ORG	838			0838		
121		LOADDD		*&1 LOAD ADDRESS		0838			
122	838	BEGINN	BCE	DONE, 0&X1,	8	0838	B 870 0 0		4
123	846		MCW	0&X1,SEQNO	7	0846	M 0 0 U45		4
124	853		MCW	CODE	1		M		4
125	854		BCE	IFCOND, CODE, W IF ( SENSE SWITCH I ) IFCOND, CODE, K IF ( SENSE LIGHT I ) SNAPSH C	8		B 893 U42 W		4
126	862		BCE	IFCOND, CODE, K IF ( SENSE LIGHT I )	8		B 893 U42 K		4
127		DONE	DUU	Sidil Silye	9		B 333 C		4 5
128 129	875 882		SBR LCA	CLEARL&3,GMWM CONT,PHASID	7 7		H 710 W14 L U69 110		5 5
130	889		В	LOADNX		0889			5
131		TECOND		KLESS, 2&X1	7		M U70 0 2		5
132	900		SBR	TSTLES&6,2&X1	7		H S03 0 2		5
133	907		LCA	0&X1,0&X3 SEQNO, CODE, GMWM	7		L 0 0 0?0		5
134	914		SAR	X1	4	0914	Q 089		6
135	918		С	0&X3		0918			6
136	922		SAR	Х3		0922			6
137	926		LCA	1&X3,2&X3 REPLACE STATEMENT CODE WITH GMWM			L 0?1 0?2		6
138 139	933 937		SBR MCW	X3	4 7	0933	н 099 М 010 U36		6 6
140	937		MCW	0&X1,ON	1	0937			6
141	945		SAR	X1	4		0 089		7
142	949		MZ	X2ZONE,ON-1	7		Y U71 U35		7
143	956		MZ	X2ZONE,OFF-1	7		Y U71 U32		7
144	963		BWZ	*&5,SEQNO,2	8		V 975 U45 2		7
145	971		В	*&9	4		В 983		7
146	975			*&15,SEQNO-2,2	8		V 997 U43 2		7
147	983		MCW	SEQNO,X2	7	0983	M U45 094		8

-			FORTRAN COMPILER IF COND PHASE PHASE 44				PAGE	2
SEQ PG LIN	LABEL	OP	OPERANDS	SFX CT	LOCN	INSTRUCTION T	TYPE	CARD
148 990 149 997 150	*	MCW B	0&X2,SEQNO MORE	7 4	0990 0997	M 0!0 U45 B  20		8
151 1 001 152 1 005 153 1 009 154 1 016	BOTTOM	C SAR SBR B	0&X1 X1 X3,4&X3 BEGINN	4 4 7 4	1001 1005 1009 1016	C 0 0 Q 089 H 099 0?4 B 838		8 8 8
156 1 020 157 1 024 158 1 028 159	MORE *	MN SAR BCE	0&X1 X1 SLITE,CODE,K SWITCH I ) ON, OFF	4 4 8	1020 1024 1028	D 0 0 Q 089 B S54 U42 K		9 9 9
161 162	*	MCW MCW BCE B B B B CS CS SW MN MN MCW W B BCV B CC	0&X1,CH CH,*&8 OKSW,K0TO6,0 332 GLOBER SEQNO,246 ERR37 *&5 *&3	7 7 8 1 1 1 1 1 4 4 1 4 7 7 1 1 4 4 1 5 4 4 2 2 .	1036 1043 1050 1058 1059 1060 1061 1062 1063 1064 1068 1069 1073 1080 1081 1082 1086 1087	M 0 0 U72 M U72  57 B /02 U79 0 B B B B B / 332 / , 184 D U45 246 D D M V22 2 2 B  96 @ B  98 F 1		9 9 9 9 10 10 10 10 10 11 11 11 11 11 11 11 11
182 1 098 183 184 185	* * SENSE	B E SWIT	BOTTOM  CH NUMBER IS OK	4	1098	B   01		12
186 1 102 187 1 109 188 1 116 189 1 123 190 1 130 191 1 137 192 1 144 193 1 151 194 1 156 195 1 163 196 1 170 197 1 174	OKSW	A MN MCW MCW MCW S C BE MCW LCA LCA SBR	KP1,CH CH,BIN ON,BIN-1 OFF,X2 0&X2,X2 KP10,X2&1 SEQNO,X2 SAME OFF,BRANCH BRANCH,0&X3 BIN X3	7 7 7 7 7 7 5 7 7 4 4	1109 1116 1123 1130 1137 1144 1151 1156 1163 1170	A V23 U72 D U72 U41 M U36 U40 M U33 094 M 0!0 094 S V25 095 C U45 094 B S39 S M U33 U49 L U49 0?0 L U41 H 099		12 12 12 12 13 13 13 13 13 14 14

-		FORTRAN COMPILER IF COND PHASE PHASE 44			I	PAGE 3
SEQ PG LIN	LABEL OP	OPERANDS	SFX CT	LOCN	INSTRUCTION TY	YPE CARD
198 1 178 199 1 182 200 1 186	ALMOST C SAR LCA	0&X1 X1 1&X1,0&X3	4 4 7	1178 1182 1186	C 0 0 Q 089 L 0 1 0?0	14 14 14
201 1 193 202 1 197 203 1 205 204 1 209	SBR TSTLES BCE CS CS	X3 BEGINN,0,< NOT TOO BIG IF LESS-THAN NOT CLOBBERED 332	4 8 4 1	1193 1197 1205 1209	H 099 B 838 000 < / 332 /	14 15 15
205 1 210 206 1 212 207 1 219	CC MCW W	1 ERROR2,270	2 7 1	1210 1212 1219	M V61 270 2	15 15 15
208 1 220 209 1 222 210 1 230 211 1 235	CC BCE RWD HALT H	1 HALT,CDOVLY,1 1 HALT	2 8 5 4	1220 1222 1230 1235	F 1 B S35 769 1 U %U1 R . S35	15 16 16 16
212 213 1 239 214 1 246	* SAME LCA SBR	BIN,0&X3 X3	7 4	1239 1246	L U41 0?0 H 099	16 16
215 1 250 216 217 1 254	* SLITE MCW	ALMOST  0&X1,CH	7	1250	B /78 M 0 0 U72	16 16
218 1 261 219 1 268 220 1 276 221 1 277	MCW BCE B B	CH,1275 OKLITE,K1234,0	7 8 1 1	1261 1268 1276 1277	M U72 S75 B T17 V65 0 B	17 17 17 17
222 1 278 223 1 279 224 1 283	B CS CS	332	1 4 1	1278 1279 1283	B / 332 /	17 17 17
225 1 284 226 1 288 227 1 295	SW MN MN	GLOBER SEQNO,245	4 7 1	1284 1288 1295	, 184 D U45 245 D	18 18 18
228 1 296 229 1 297 230 1 301 231 1 302	MN MCW W BCV	ERR36 *&5	1 4 1 5	1296 1297 1301	D M W07 2 B T11 @	18 18 18
232 1 307 233 1 311 234 1 313	B CC B	*&3 1 BOTTOM	4 2 4	1307 1311		19 19 19
235 236 1 317 237 1 324	* OKLITE MCW A	K080,W3 CH,W3	7 7	1317 1324	M W10 W13 A U72 W13	19 19
238 1 331 239 1 338 240 1 342	MCW MCW MCW	W3,BW-1 OFF W3,SW	7 4 7	1331 1338 1342	M W13 U56 M U33 M W13 U61	19 19 20
241 1 349 242 1 356 243 1 363	MCW MCW S	ON, X2 0&X2, X2 KP10, X2&1	7 7 7	1363	M U36 094 M 0!0 094 S V25 095	20 20 20 20
244 1 370 245 1 377 246 1 382 247 1 389	C BE MCW LCA	SEQNO,X2 SAME2 ON,BRANCH BRANCH,0&X3	7 5 7 7	1370 1377 1382 1389	C U45 094 B U12 S M U36 U49 L U49 0?0	21 21 21 21
	2011	. ,	,		0.0	

phase-44.43.asc	Mon Jul 14 23:50:06 2008	4				
	FORTRAN COMPILER IF COND PHASE PHASE 44	PAGE 4				
SEQ PG LIN LABEL OP	OPERANDS	SFX CT	LOCN	INSTRUCTION	TYPE	CARD
248 1 396 LCA 249 1 400 LCA 250 1 404 SBR 251 1 408 B 252 1 412 SAME2 LCA 253 1 419 LCA 254 1 423 SBR 255 1 427 B 256 * DATA	SW BW X3 ALMOST SW,0&X3 BW X3 ALMOST	4 4 4 7 4 4 4	1419	L U61 L U57 H 099 B /78 L U61 0?0 L U57 H 099 B /78		21 21 21 21 22 22 22 22
258	#3 #3 @B &@ #1 #3 @B @ @V 1@ @V 1@ @CONTINUE@ @<@ @CONTINUE@ @<@ @K@ #1 @0123456@ @ERROR 37 - ILLEGAL SENSE SWITCH, STATEMENT @ &1 &10 @MESSAGE 2 - OBJECT PROGRAM TOO LARGE@ 1234	3 5 1 3 4 8 4 8 1 1 7 43 1 2 2 36 4	1449 1457 1461 1469 1470 1471 1472 1479 1522 1523 1525 1561 1565			22 22 22 23 23 23 23 23 23 24 24 26 26 26 27 28
277 1 607 ERR36 DCW 278 1 610 K080 DSA 279 1 613 W3 DCW 280 1 614 GMWM DCW 281 ORG 282 203 DSA 283 EX 284 END	@ERROR 36 - ILLEGAL SENSE LIGHT, STATEMENT @ 80 #3 @}@ 201 LOADD LOAD ADDRESS FOR CARD-TO-TAPE PROGRAM BEGINN	42 3 3 1	1607 1610 1613 1614	0201 838 B 838 / 000 080	GMARK	30 30 30 30 30 31 32

phase-44.43.asc			1	Ion Ju	1 14 2	3:50:0	6 2008		5				
			FORTRAN	COMPILE	R IF C	OND PHAS	E PHAS	E 44				PAGE	5
SYMBOL	ADDRESS	SYMBOL		SYMBOL			ADDRESS		ADDRESS	SYMBOL	ADDRESS	SYMBOL	ADDRESS
ALMOST CH	1178 1472	BEGINN CLEARL	838 707	BIN	1441	BOTTOM	1001 1469	BRANCH DONE	1449 870	BW ERR36	1457 1607	CDOVLY ERR37	769 1522

SYMBOL	ADDRESS												
ALMOST	1178	BEGINN	838	BIN	1441	BOTTOM	1001	BRANCH	1449	BW	1457	CDOVLY	769
CH	1472	CLEARL	707	CODE	1442	CONT	1469	DONE	870	ERR36	1607	ERR37	1522
ERROR2	1561	GLOBER	184	GMWM	1614	HALT	1235	IFCOND	893	K080	1610	K0TO6	1479
K1234	1565	KLESS	1470	KP1	1523	KP10	1525	LOADDD	838	LOADNX	700	MORE	1020
OFF	1433	OKLITE	1317	OKSW	1102	ON	1436	PHASID	110	SAME	1239	SAME2	1412
SEQNO	1445	SLITE	1254	SNAPSH	333	SW	1461	TSTLES	1197	W3	1613	X1	89
X2	94	X2ZONE	1471	Х3	99								