NO2	125682
SHEET	0
OF	20

DIAGNOSTIC TEST

IITLE	1622 CARD	INPUT/OUTPUT	DIAGNOSTIC TEST	(INTERLEAVING)	- 1003	
MACH.			BY JHM			DATE 4-11-62

ENGINEERING CHANGE HISTORY

E/C NO.	DATE	SHEETS AFFECTED
404674	11-4-61	1-20
404675	4-11-62	1,2,3,4,5,6,11,12,13, 16,17,18
	·	

E/C NO.	404674	404675	İ		
DATE	11-4-61	4-11-62			· ·

IO03

A. SCOPE

This is a Reliability Test Program for the 1620 - 1622 System. Cards are punched with a ripple pattern using alphanumeric characters which are read back in and compared to stored data.

Basically, the program operates in the following sequence:

- a) Check for Any Data Checks.
- b) Read a card.
- c) Check 1st character.
- d) Compare card information.
- e) Same as (a) through (d) above.
- f) Punch card.
- g) Loops back to (a).

There is a delay routine (cho-cho) designed to create every possible timing condition between reading and punching. The delay starts at approximately ten seconds between cards and progresses to zero delay (maximum reading and punching speed). One complete cycle of the delay (maximum delay to zero and back to maximum) takes approximately 30 minutes.

B. OPERATING INSTRUCTIONS

- 1. Reset 1620.
- 2. Place Program Deck in Card Reader Hopper.
- 3. Push "Load" P.B. on 1622.
- 4. 1620 will HALT after typing out Heading.
- a. Set Program SWS for desired options.
 - b. Ready the punch. (Note: To punch the initial ripple deck, place SW 1 ON and Start. After punching about 200 cards, press the SIE Key. Insert 4900652, Release, place SW 1 OFF and proceed.)
 - c. Place punched ripple cards in card reader hopper. Ready the reader.
- 6. Push START on 1620 to begin Program.
- 7. Place punched cards in reader hopper to be read and checked. Switch Settings (suggested)

Data Check = Program
I/O Check = Program

Arith Check = Must be Program

SW 1 = OFF (After punching ripple deck)

SW 2 = OFFSW 3 = OFF

SW 4 = OFF

SW 1 OFF Read and punch ON Punch only

SW 2 OFF Read and Punch ON Read only

SW 3 OFF Errors typed out
ON Bypass ETO (for trouble shooting)

SW 4 OFF Delay changes ON Delay remains constant

Error Type-Outs (ETOs):

There are several Data Checks made throughout the **program**. When an error has been detected, a type-out (if program SW 3 is OFF) will give the following information about the error:

- a) The memory location of the instruction that detected the error.
- b) Where possible the correct information will be typed out followed by the information containing the error.

0724 READ CHECK

00736 WRITE CHECK

00748 O/F INDICATOR ON

00760 MBR - E CHECK

00772 MBR - 0 CHECK

00784 MAR CHECK

Error Type=Outs (ETOs) (cont'd)

The above type-outs are due to a data check condition at the start of the read routine. All of these indicators should be OFF at the start of each Program pass.

00976 READ CHECK 1ST READ 01012 " " 2ND " 01048 " " 3RD "

These ETOs are due to a read check when the 1622 transfers the card information to the 1620. Three attempts are made to transfer each card if necessary. Check 1620 i/O translation circuit and check buffer (1622) for correct data

01096 MBR - E CHECK AFTER CARD READ 01108 MBR - O CHECK AFTER CARD READ

These ETOs are due to an MBR check and indicate trouble in the I/O translator or memory circuit (1620). The checks are made directly after a card is read by the 1620.

01120 READ-IN AREA O/F

The 1620 received more than 80 alpha characters from the 1622.

01204 NO 1ST COMPARE

The first character in the card Read-In was not one of the 48 alpha characters used by this program. Check to see if card contains correct data.

01360 MBR - E CHECK AFTER 1ST COMPARE 01372 MBR - O CHECK AFTER 1ST COMPARE

01564 MBR - E CK BEFORE COMPARE. 01576 MBR - O CK BEFORE COMPARE.

Due to data check while performing 1st compare routine. Check 1620 for correct internal transfer operation.

01624 E/Z TGR OFF COMPARE DATA FOLLOWS.

The correct card data will be typed from STORED DATA followed by the data read-in from the card.

This ETO is due to the information read-in not corresponding to the stored data. Check card and buffer (1622) for correct data.

01636 MBR - E CHECK AFTER COMPARE, DATA FOLLOWS. 01648 MBR - O CHECK AFTER COMPARE, DATA FOLLOWS.

Data typed-out is same as for E/Z TGR OFF (above). Caused during card compare routine by internal transfer operation (1620)

Error Type-Outs (ETOs) (cont'd)

01960 WRITE CHECK AFTER PUNCH.

Checked after each punch routine. Check punched card for correct data.

The complete normal typeout information will be as follows:

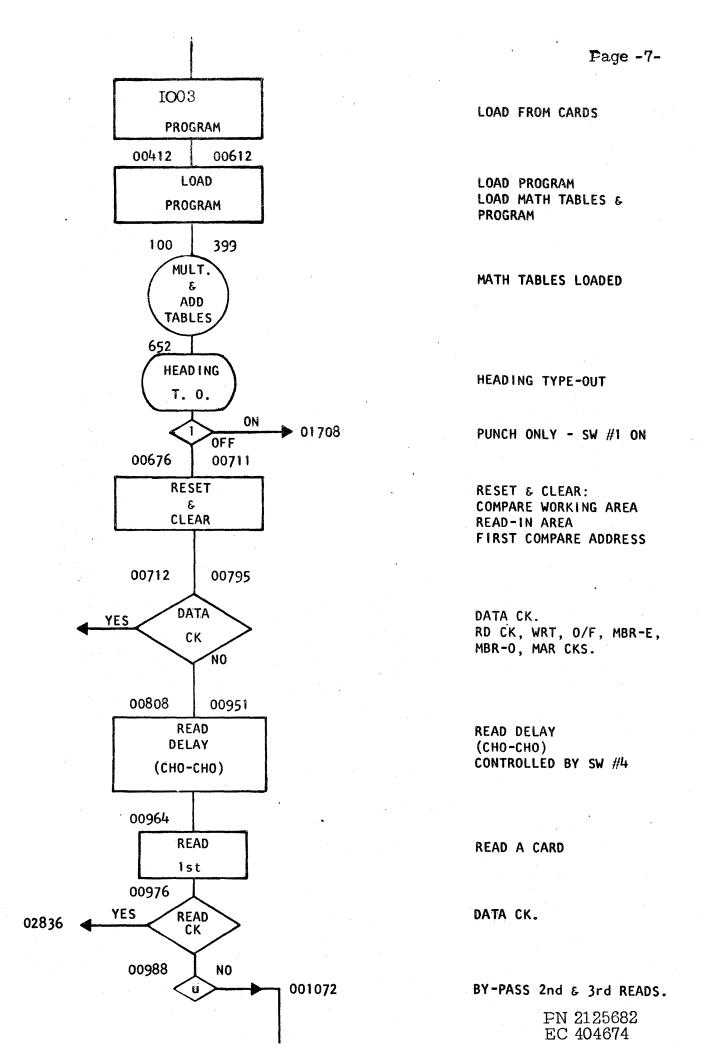
1622 CARD READER-PUNCH DELAY 1003. SW 1 ON= PUNCH ONLY. SW 2 ON= READ AND COMPARE ONLY. SW 3 ON= BY-PASS ETOS. SW 4 ON= STOP CHO-CHO DELAY CHANGE.

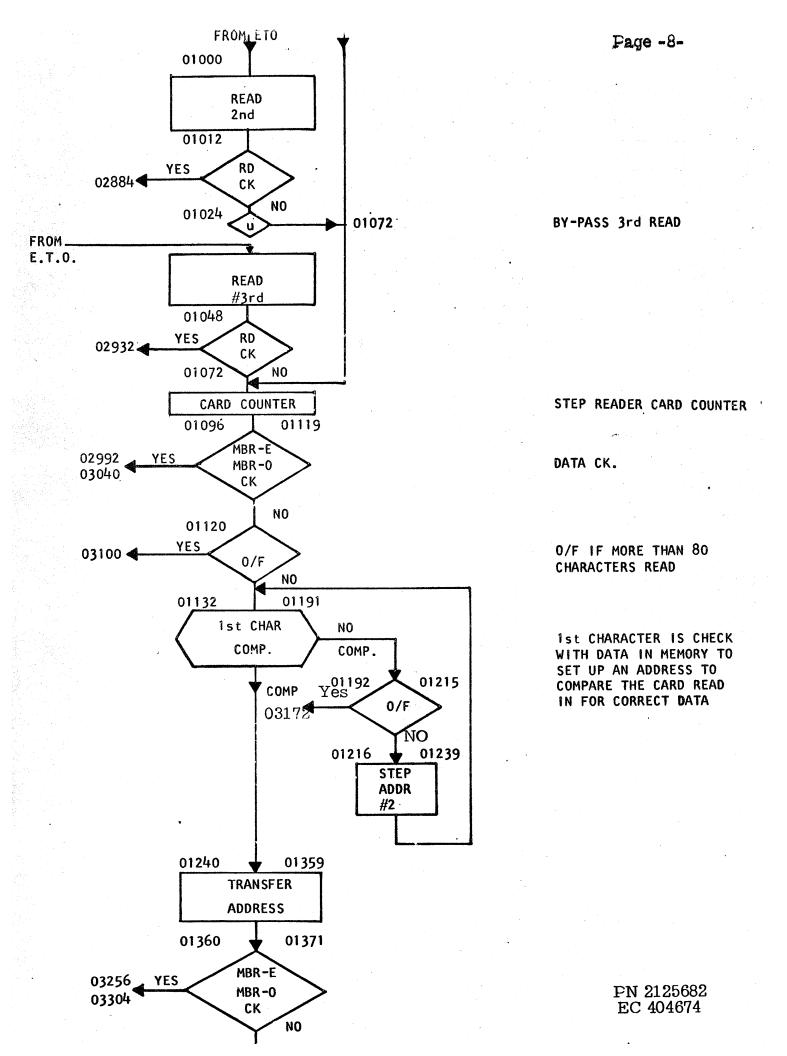
Page -5-

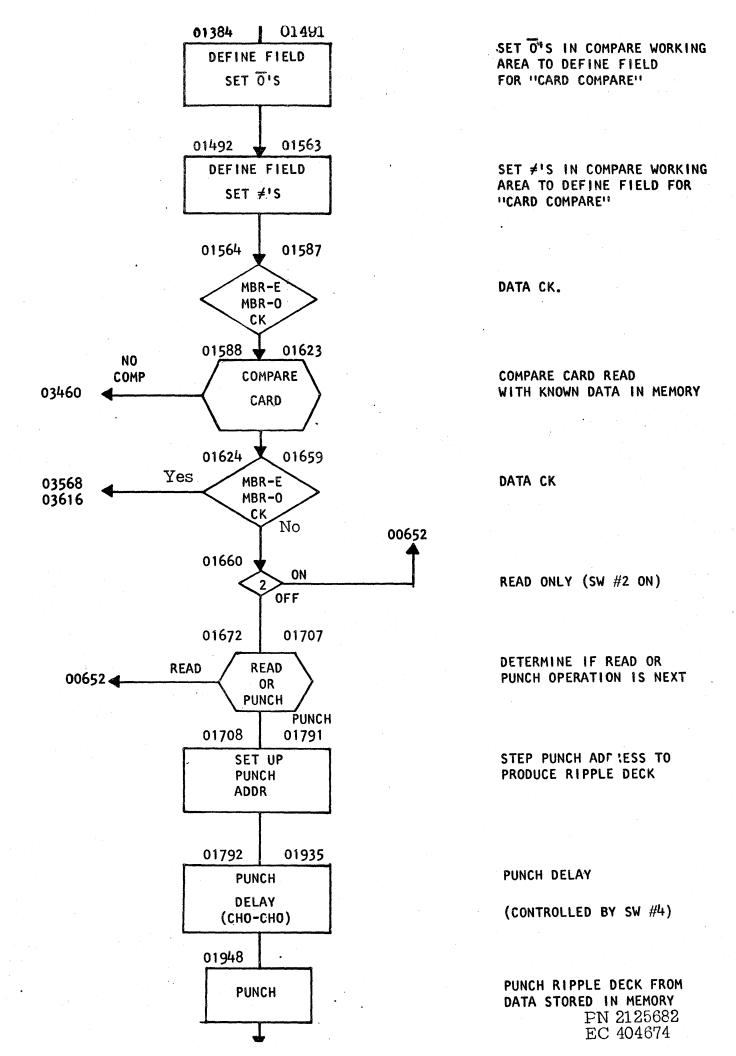
MEMORY ADDRESS ALLOCATIONS

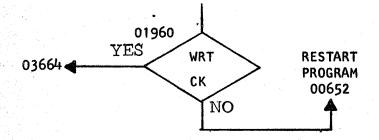
00000		00096	Load Card and Branch Instructions
100		399	Math. Tables
412		591	Load Program
652		2380	Main Program
2512		3807	ETO Routines
3809		5065	ETO Data
5101		5 2 5 9	Card Counter T. O. Data
5261		5599	Heading TO Data
5605		5925	Card Compare Data
5935	-	6165	Read-In Clear Data
6175		6405	1st Character Compare Data
6415		6735	Compare Working Area
6745	-	6975	Read-In Area

	0000	0 to 000	59			
•	36 36 36 36 36	00060 00100 00160 00220 00280	00500 00500 00500 00500 00500	Card #1		First and second Load Cards load the math tables and the Program Load Card. (Cards 3 through 7
	0006	0 to 000	95			contain the math tables.)
	36 36 49	00340 00000 00000	0050 0 00500 00000	Card #2		
	0000	0 to 000	59		<i>)</i>	
	36 11 14 46 49	00652 00006 00006 00652 00000	00500 00060 06772 01200	Card #8		Eighth Load Card contains instructions for loading core storage.

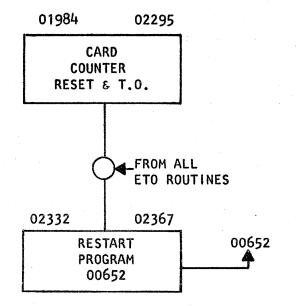






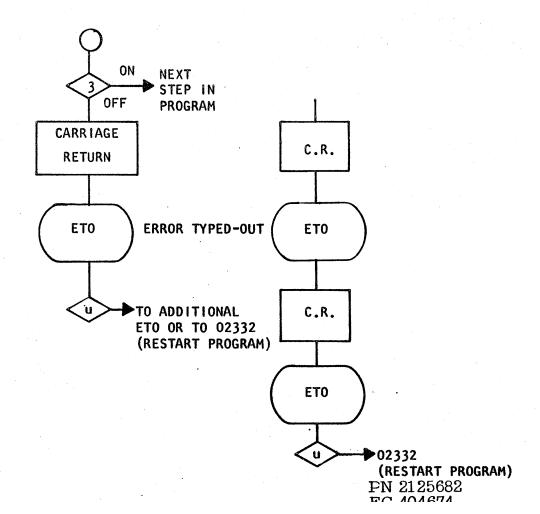


DATA CK.



MAINTAINS COUNT OF CARDS READ & PUNCHED.
COUNTER READ-OUT=02212
COUNTER RESET =02056

ALL ETO ROUTINES BRANCH TO HERE TO RE-START THE PROGRAM



Control of the Charles of the Control of the Contro	* *				
00652	49	03712	00000	NOP	
00664	46	01708	00100	BI	CHECK SW #1
	9 6	06735	05925	TF	
00676	2 6 2 6				RESET COMPARE AREA
0 06 88	20	06975	<u>0</u> 6165	TF	CLEAR READ-IN AREA
00700	16	01167	06175	\mathbf{TFM}	RESET FIRST COMP. ADDRESS
00712	41 46	00000	00000	NOP	
00724	46	02512	00600	$_{ m BI}$	READ CHK
00736	46	02572		BI	WR CHK
	4 6 4 6				
00748	40	02620	01400	BI	OVERFLOW
00760	46	Q2 668	01600	\mathtt{BI}	MBR-EVEN
00772	46	02728	01700	BI	MBR - ODD
00784	46	02776	00800	BI	MAR
00796	41	00000	00000	NOP	
00808	41	00952		NOP	
00820	11	00949		AM	ADD TO DELAY COUNTER
00832	46	00856		BI	OVERFLOW?
	40		01400		OVERTLOW!
00844	49	00820	00000	<u>B</u>	<u></u>
00856	16	00949	0000	\mathbf{TFM}	RESET DELAY COUNTER
00868	4 6 11	.00952	00400	BI	CHECK SW #4
00 88 0	11	00831	00002	$\mathbf{A}\mathbf{M}$	SHORTEN DELAY
00892	4 6	00916	01400	BI	OVERFLOW?
00904	49	00952	00000	B	OV MICE MOVV.
			00101	TFM	DEGMODE LONG DET AT
00916	16	00831			RESTORE LONG DETAY
00928	49	00952	00000	В	
00940	41	00 <u>0</u> 00	. 00000	NOP	
00952	41	00000	00000	NOP	
00964	37	06745	00500	RA	READ A CARD
00976	46	02836	00600	BI	READ CHK?
00988	49	01072	00000	B	BYPASS 2nd & 3rd tries
	007				
01 000	37	06745	00500	RA	READ 2nd ATTEMPT
01.012	46	02884	00600	BI	READ CHK?
01.024	49	01072	00000	В	BYPASS 3rd try
01 036	37	06745	00500	RA	READ 3rd ATTEMPT
01 04 8	46	02932	00600	BI	READ CHK?
01060	49	02932 01072	00000	B	T (1-1/2 #12) (1-1/2 # 2)
01072	11	02030	00001	ÂM	STEP CARD RD COUNTER
	11				SIEP CARD RD COUNTER
01084	43	02044	02023	BD	1 413 to TTTTTT OF THE
01096	46	02992	01600	BI	MBR-EVEN CHK?
01108	46	03040	01700	BI	MBR-ODD CHK?
01120	45	03100	06909	BNR	
01132	41	00000	00000	NOP	
01144	32	06744	00000	SF	
01156	24	06745	0 6175	C	COMPARE 1st CHARACTER
01168	47	01192	01200	BNI	OCTATE VITTING OFFICE OF THE
01180	49	01240	00000	В	
01192	14		0 6275	$\mathbf{C}\mathbf{M}$	
01204	4 6	03172	01200	BI	
01216	11	01167	00002	AM	STEP COMPARE ADDRESS
01228	49	01156	00000	В	
01240	41	00000	00000	NOP	·
01252	25	01320	01167	TD)	
	0E	01020			THE A MORETED A PAINT FROM
01264	25	01319	01166	TD >	TRANSFER ADDRESS
01276	25	01318	<u>01</u> 165	TD	
012 88	11	01,320	00240	$\mathbf{A}\mathbf{M}$	STEP TO COMPARE DATA ADD.
01300	49	01336	00000	В	

01312 01324 01336 01348 01360 01372	41 41 26 26 46 46	0 <u>00</u> 60 00060 01332 03514 03256 03304	00000 00000 0132 0 01320 01600 01700	NOP NOP TF TF BI BI	MBR-EVEN CHK? MBR-ODD CHK?
01384 01396 01408 01420 01432 01444 01456 01468 01480 01492 01504 01516 01528 01540 01552 01564	41 12 26 32 12 26 32 41 11 26 15 12 46	00000 01320 01426 00000 01320 01462 00000 01332 01510 00000 01332 01546 00000 01332 01546 00000	00000 00001 01320 00000 00001 01320 00000 00159 01332 0000≠ 0000≠ 00002 01600	NOP SM TF SF SM TF NOP AM TF TDM AM TF TDM SM SM BI	
01576	46	03400	01700	BI	COMPARE
01588	41	00000	00000	NOP	
01600	26	01618	01332	TF	
01612	24	06611	06903	C	
01624	47	03460	01200	BNI	
01636	46	03563	01600	BI	
01648	46	03616	01700	BI	
01660	46	00652	00200	BI	CHECK SW #2 READ-READ-PUNCH COUNTER
01672	11	01706	00050	AM	
01684	43	01703	01704	BD	
01696	49	00652	00000	B	
01708	41	00000	00 <u>0</u> 00	NOP	STEP PUNCH ADDRESS
01720	11	01954	00 0 02	AM	
01732	14	01954	05705	CM	
01744	46	01768	01200	BI	
01756 01768 01780 01792	49 16 16 41	01780 01954 01707 01936	00000 05605 00000 00000	B TFM TFM NOP	RESTORE PUNCH ADDRESS RESET RD-RD-PCH COUNTER
01804	11	01933	001 01	AM	ADD TO DELAY COUNTER OVERFLOW? RESET DELAY COUNTER
01816	46	01840	01400	BI	
01828	49	91804	00000	B	
01840	16	01933	00000	TFM	
01852	46	01936	00 <u>4</u> 00	BI	CHECK SW #4 SHORTEN DELAY OVERFLOW?
01864	11	01815	00004	AM	
01876	46	01900	01400	BI	
01888	49	01936	00000	B	
01900	16	01815	001 01	TF'M	RESTORE LONG DELAY PUNCH A CARD
01912	49	01936	00000	B	
01924	41	00000	00000	NOP	
01936	41	00000	00000	NOP	
01948	39	05605	00400	WA	
01960	46	03664	00700	BI	WRITE CHK? STEP CARD PCH COUNTER
01972	11	02042	00001	AM	
01984	43	02044	02035	BD	

01996 02008 02020 02032 02044 02056 02068 02080 02092 02104 02116 02128 02140 02152 02164 02176 02188 02200 02212 02224 02236 02248 02260 02272 02284 02296 02308 02308 02320 02320 02324 02356	41 49 41 41 41 34 39 38 34 39 41 26 26 49 41 41 34 39 38 34 39 41 41 41 41 41 41 49 48 49	00000 00652 00000 00000 00000 05101 02023 00000 05151 02035 00000 02042 02212 00000 02042 02212 00000 05101 02023 00000 05151 02035 00000 05151 02035	00000 0000≠ 0000≠ 0000≠ 00000 00102 00100 00100 00100 00100 00100 00210 00210 00000 002210 00000 00102 00100 00102 00100 00102 00100 00100 00100 00100 00100 00100 00100 00100 00100 00100 00100	NOP B NOP NOP NOP K WA WN K WA NOP TF B NOP K WA WN K WN H NOP K WN H NOP NOP NOP B H B		
02368	41	00000 FDDOD	00000	NOP		
			ROUTIN			
02512 02524 02536 02548 02560 02572 02584 02596 02608 02620 02632 02644 02656 02668 02680 02692 02704	46 34 39 49 41 46 34 39 49 46 34 39 49	00736 00000 03809 02332 00000 00748 00000 03849 02332 00760 00000 03891 00000 00772 00000 03943 02332	00300 00102 00100 00000 00300 00102 00100 00300 00102 00100 00300 00102 00100 00102 00100	BI K WA B NOP BI K WA B BI K WA B BI K WA B BI K B B BI K B B B B		

02716 02728 02740 02752 02764 02776 02788 02800 02812 02824 02836 02848 02860 02872 02844 02896 02903 02920 02932 02944 02956 02968 02980 02992 03004 03016 03028 03040 03052 03064 03076 03088 03100 03112 03124 03148 03160 03172 03184 03196 0328 0328 03196 0328 03292 03244 03196 03232 03244 03196 03232 03244 03256 03268 03280 03292 03304 03292 03304 03292 03304 03292 03304 03292 03304 03292 03280 03292 03304 03292 03280 03292 03304 03292 03304 03316	$\begin{array}{c} 4164999643394643394643996439916499916499916433446433446499964399643$	00000 00784 00000 03985 02332 00796 00000 04027 02332 00000 01000 04065 02332 01036 00000 04117 02332 01072 00000 04169 02332 01072 00000 01108 00000 01132 00000 04221 02332 01120 00000 04289 02332 01120 00000 04357 00000 04357 00000 04409 02332 01300 00400 04457 02332 010000 04457 02332 01384 00000	00000 00300 00102 00100 00000 00300 00102 00100 00300 00102 00100 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00000 00300 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102 00100 00102	NOP NOI KA BIKABIKABIKABIKABIKABIKAABIKAABIKABIKABI
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03328 03340 03352 03364 03376 03388 03400 03412 03424	39 49 46 34 39 46 34 39 49	04529 02332 01576 00000 04601 02332 01588 00000 04667 02332	00100 00000 00300 00102 00100 00000 00300 00102 00100 00000	WA B BI K WA B BI K WA B
03436 03448 03460	41 46	00000 01636	00000 00300	NOP BI
03472	34	00000	001 02	K
03484	39	04733	001 00	WA
03496	34	<u>0</u> 0000	001 02	K
03508	39	0 6453	00100	WA
03520	34	00000	00102	K
03532	39	06745	00100	WA
03544	49	02332	00000	B
03556	41	00000	00000	NOP
03568	46	01648	00300	BI
03580	34	00000	00102	K
03592	39	04819	00100	WA
03604	49	03496	00000	B
03616	46	01660	00300	BI
03628	34	00000	00102	K
03640	39	04913	00100	WA
03652	49	03496	00000	B
03664	46	01972	00300	BI
03676	34	00000	00102	K
03688	39	05009	00100	WA
03700	49	02332	00000	B
03712	15	00653	00001	TD M
03724	34	00000	00102	K
03736	39	05259	00100	WA
03748	34	00000	00102	K
03760	39	05343	00100	WA
03772	34	00000	00102	K
03784	39	05459	00100	WA
03784	39 49	02344	00000	WA B

04480 04492 04504	00 63 63	43520 45590 00435	04146 07162 65457	C K A F T E R 1 S T C O M P
04516 04528 04540 04552	41 70 00 00	59450 71737 54425 43520	≠0≠00 77200 92056 04146	A R E # # 0 1 3 7 2 M B R - 0 C K A F
04564 04576 04588	63 63 41	45590 00435 59450	07162 65457 ≠0≠00	$egin{array}{cccccccccccccccccccccccccccccccccccc$
04600 04612 04624	70 00 00	71757 54425 43520	67400 92045 04245	A R E ≠ ≠ 0 1 5 6 4 M B R - E C K B E
04636 04648 04660	46 56 03	56594 54574 0≠007	50043 15945 07175	FORE COMPARE
04672 04684 04696	77 59 00	76000 20560 42454	05442 04352 65659	. \(\neq \) 0 1 5 7 6
04708 04720 04732	45 41 70	00435 59450 71767	65457 30≠00 27400	E COMP ARE. ≠ 0 1 6 2 4
04744 04756 04768	00 47 23	45216 59005 00435	90063 64646 65457	E/Z T GROFF COMP
04780 04792 04804	41 63 53	59450 41004 56666	04441 65653 2030≠	A R E D A T A F O L L O W S . \neq 0 1 6 3 6
04816 04828 04840	00 00 45	70717 00544 00435	67376 · 25920 20041	MBR- CKA
04852 04864 04876	46 56 23	63455 54574 00000	90043 15945 04441	FTER COMPARE
04888 04900 04912 04924	63 53 00 00	41 004 56666 70717 00544	65653 2030≠ 67478 25920	T A F O L L O W S . # 0 1 6 4 8 M B R -
04936 04948 04960	. 56 46 56	00435 63455 54574	20041 90043 15945	M B R - O C K A F T E R C O M P A R E
04972 04984 04996	23 00 66	44416 46565 62030	34100 35356 ≠0000	, DATA FOLLO
05008 05020 05032	70 00 00	71797 66594 43520	67000 96345 04146	0 1 9 6 0 W R I T E C K A F
05044 05056	63 55	45590 43480	05764 30 ≠	TER PU NCH. ≠

05068 05080 05092 05104 41 05116 41 05128 64 05140 00 05152 64 05164 41 05176 64 05188 00 05200 43 05212 56 05224 00	$\begin{array}{cccc} 55434 & 80043 \\ 59440 & 04356 \\ 55634 & 55900 \\ 00330 & 00 \neq 00 \\ 41594 & 40043 \\ 64556 & 34559 \end{array}$		R E A D E R C A R D C O U N T E R U N C H C A R D C O U N T E R C A R D C C O U N T E R C A R D E C O U R E S E T
05236 00 05248 59	63560 06945 56030 ≠	•	TO ZE RO. ≠
	HEADING TYP	EOUT DATA	
05260 76 05272 59 05284 44 05296 55 05303 53 05320 70 05344 62 05356 55 05368 43 05380 68 05392 00 05404 56 05416 41 05428 00 05440 59 05452 68 05464 00 05476 00 05488 62 05500 62 05512 62 05512 62 05524 56 05536 56 05548 20 05560 45	42682 05741 62004 56356 03000 00000 66007 40000 55330 06263 57004 34856 43485 60044 53416 80043 41554 74503		1 A A B B B B B B B B B B B B B B B B B

CARD COMPARE DATA

05596 05608 05620 05632 05644 05656 05668 05680 05692 05704 05716 05728 05740 05752 05764 05776 05788 05800 05812 05824 05836 05848 05860 05872 05884 05896 05908 05920	00 43 49 56 64 76 04 21 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 49 56 56 56 56 56 56 56 56 56 56 56 56 56	00 000 44454 51525 57585 65666 71727 77787 33142 03230 44454 51525 57585 65666 71727 77787 33142 03230 44454 51525 57585 65666 71727 77787 33142 03230 44454 51525 570≠≠ READ-	04142 64748 35455 96263 76869 37475 93424 01013 04142 64748 35455 96263 76869 37475 93424 01013 04142 64748 35455 96263 76869 37475 93424 01013 04142 64748 35455 ≠0000	AR DATA	AGMNTZ5(\$BHNTZ
06038	00	00000	00≠0≠		<i>≠ ≠ ≠</i>
06100 06112	00 00	00000 00000	00 ≠ 00 ≠ 0000		<i>≠</i> ≠
06124 06136	00 00	000 ≠ 0 0 ≠ 000	00000		<i>≠ '</i> ≠
06148	0≠	00000	0000≠		≠ ≠
06160	00	000≠0	≠ 0 ≠ 00		≠ ≠ ≠

FIRST CHARACTER COMPARE DATA

06172 06184 06196 06203 06220 06232 06244 06256 06268 06280 06292 06304 06316 06328 06340 06352 06364 06376 06388 06400 06412	00 41424 46 47484 53 54555 59 62636 67 68697 73 74757 79 34240 20 10132 00 41424 46 47484 53 54555 59 62636 67 78697 73 74757 79 34240 20 10132 00 41424 46 47484 53 54555 59 62636 60 60	$\begin{array}{c} 9\overline{5}1\overline{5}2\\ 6\overline{5}7\overline{5}8\\ 4\overline{6}5\overline{6}6\\ 0\overline{7}1\overline{7}2\\ 6\overline{7}7\overline{7}8\\ 4\overline{3}\overline{1}4\\ 10\overline{3}\overline{2}3\\ 3\overline{4}4\overline{4}5\\ 9\overline{5}1\overline{5}2\\ 6\overline{5}7\overline{5}8\\ 4\overline{6}5\overline{6}6\\ 0\overline{7}1\overline{7}2\\ 6\overline{7}7\overline{7}8\\ 4\overline{3}\overline{3}\overline{1}4\\ 10\overline{3}\overline{2}3\\ 3\overline{4}4\overline{4}5\\ 9\overline{5}1\overline{5}2\\ 6\overline{5}7\overline{5}8\\ \end{array}$		H I I I I I I I I I I I I I I I I I I I	M N O P Q V 2 8 S T U 0 1 8 S T Z 0 6 7 S 4 5 6) - E K Q S 4 5 6) - E K Q S 4 5 6) - E K Q S 4 5 6) - E K Q S 4 5 6) - E K Q S 4 5 6) - E K Q S 4 5 6) - E K Q S 4 5 6) - E K Q S 7 8 8 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9 9
	COMPARI	E WORKING	G AREA		
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