

# KUMARESAN SHANMUGAM

## CONTACT



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## EDUCATION

### 2019 - 2023

#### BACHELOR OF ENGINEERING

**Sri Ranganathar Institute of Engineering and Technology (currently pursuing)**

**BRANCH -**

Electronics and Communication Engineering

#### **Secondary / higher secondary**

Government higher secondary school  
- singalandapuram

#### **Higher Secondary school**

Secondary acquired mark in percentage  
: 64.84

#### **Secondary school**

Secondary acquired mark in percentage  
: 76.6

## AREA OF INTEREST

- VLSI CUSTOM  
LAYOUT DESIGN

## SKILLS

- Digital electronics basics
- Basic logic gates design
- Basic analog block (BGR,ADC,OP AMP)  
design
- Worked with - cadence virtuoso L  
(180nm , 90nm),Assura
- Verification - DRC / LVS
- Floor planning
- Linux basics

## UNDERSTANDING OF ISSUES

- Latch up
- IR drop
- Antenna effort
- Matching concepts
- EM
- ESD

## **COURSES ATTENDED / PARTICIPATION**

- Complete a udemy course - custom layout course conducted by a Kunal Ghosh
- Analog layout laboratory - YouTube channel where I learnt basic Analog layout design concepts
- IIT Roorkee - YouTube channel where I learnt basic Digital VLSI design concepts

## **SNS INSTITUTIONS COIMBATORE -**

participated in paper presentation(CMOS MIXED IC DESIGN ),technical quiz, seminar (Trends in VLSI design)

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## **DECLARATION**

I hearby declare that the above information are true to my knowledge

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