

First International Conference on Electronics, Communication and Signal Processing

Programme Schedule

8th August - 10th August 2024

Day- 1: August 08, 2024 (Thursday)		
Timing	Venue	Programme
9:00 AM – 10:00 AM	Ground Floor, Administrative Building	Registration
10:00 AM – 11:30 AM	Main Auditorium	Inauguration
	5 th Floor, Administrative Building	
11:30 AM – 12:00 Noon	5 th Floor, Administrative Building	High Tea
12:00 Noon – 1:15 PM	Main Auditorium	Key Note Address 1 (Session Chair: Dr. Manisha Bharti)
	5 th Floor, Administrative Building	Prof. Sudeb Dasgupta, Department of Electronics and Communication Engineering, IIT Roorkee
1:15 PM – 2:00 PM	Administrative Building	Lunch
2:00 PM – 5:00 PM	Technical Sessions (Afternoon Sessions)	
	Main Auditorium, 5 th Floor, Administrative Building	Communication Systems and Networks: Session I
	Committee Room, 5 th Floor, Administrative Building	Signal Processing and Applications: Session I
	Committee Room, 4 th Floor, Administrative Building	VLSI Technology and Embedded Systems: Session I
	T&P Committee Room, 3 rd Floor, Administrative Building	Microelectronic Circuits and Systems: Session I
5:00 PM – 5:30 PM	Terrace, 5 th Floor, Administrative Building	Tea Break
5:30 PM – 6:15 PM		Key Note Address 2 (Session Chair: Dr. Rikmantra Basu)
		Prof. Guo En Chang, National Chung Cheng University, Taiwan

Day - 2: August 09, 2024 (Friday)		
Timing	Venue	Programme
9:00 AM – 10:00 AM	ECE Department, First Floor, Mini Campus Building	Registration
10:00 AM – 11:00 AM	Technical Sessions (Morning Sessions)	
	Committee Room No. 126 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session II
	Lab Room No. 122 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session III
	Committee Room No. 101 (EE), First Floor, Mini Campus Building	Signal Processing and Applications: Session II
	Lab Room No. 206 (ECE), Second Floor, Mini Campus Building	Signal Processing and Applications: Session III
	Lab Room No. 125 (ECE), First Floor, Mini Campus Building	VLSI Technology and Embedded Systems: Session II
11:00 AM – 11:30 AM	Committee Room No. 29 (AS), Ground Floor, Mini Campus Building	Microelectronic Circuits and Systems: Session II
	ECE Department, First Floor, Mini Campus Building	Tea Break
	Day - 2: August 09, 2024 (Friday)	
	Committee Room No. 126 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session II
	Lab Room No. 122 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session III
	Committee Room No. 101 (EE), First Floor, Mini Campus Building	Signal Processing and Applications: Session II
11:30 AM – 1:30 PM	Lab Room No. 206 (ECE), Second Floor, Mini Campus Building	Signal Processing and Applications: Session III
	Lab Room No. 125 (ECE), First Floor, Mini Campus Building	VLSI Technology and Embedded Systems: Session II
	Committee Room No. 29 (AS), Ground Floor, Mini Campus Building	Microelectronic Circuits and Systems: Session II
	ECE Department, First Floor, Mini Campus Building	Lunch
2:30 PM – 4:00 PM	Day - 2: August 09, 2024 (Friday)	
	Technical Sessions (Evening Sessions)	
	Committee Room No. 126 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session IV
	Lab Room No. 122 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session V
	Committee Room No. 101 (EE), First Floor, Mini Campus Building	Signal Processing and Applications: Session IV
	Lab Room No. 206 (ECE), Second Floor, Mini Campus Building	VLSI Technology and Embedded Systems: Session III
4:00 PM – 4:30 PM	Lab Room No. 125 (ECE), First Floor, Mini Campus Building	VLSI Technology and Embedded Systems: Session IV
	Corridor, ECE Department, First Floor, Mini Campus Building	Tea Break
4:30 PM – 5:30 PM	Day - 2: August 09, 2024 (Friday)	
	Committee Room No. 126 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session IV
	Lab Room No. 122 (ECE), First Floor, Mini Campus Building	Communication Systems and Networks: Session V
	Committee Room No. 101 (EE), First Floor, Mini Campus Building	Signal Processing and Applications: Session IV
	Lab Room No. 206 (ECE), Second Floor, Mini Campus Building	VLSI Technology and Embedded Systems: Session III
	Lab Room No. 125 (ECE), First Floor, Mini Campus Building	VLSI Technology and Embedded Systems: Session IV
7:00 PM onwards	Old Campus of NIT Delhi, Narela	Conference Dinner

Day - 3: August 10, 2024 (Saturday)		
Timing	Venue	Programme
9:30 AM – 10:00 AM	Terrace, 5 th Floor, Administrative Building	Registration
10:00 AM – 11:00 AM	Technical Sessions (Morning Sessions)	
	Main Auditorium, 5 th Floor, Administrative Building	Communication Systems and Networks: Session VI
	Committee Room, 5 th Floor, Administrative Building	Signal Processing and Applications: Session V
	Committee Room, 4 th Floor, Administrative Building	VLSI Technology and Embedded Systems: Session V
11:00 AM – 11:30 AM	Terrace, 5 th Floor, Administrative Building	Tea Break
Day - 3: August 10, 2024 (Saturday)		
11:30 AM – 12:30 PM	Main Auditorium, 5 th Floor, Administrative Building	Communication Systems and Networks: Session VI
	Committee Room, 5 th Floor, Administrative Building	Signal Processing and Applications: Session V
	Committee Room, 4 th Floor, Administrative Building	VLSI Technology and Embedded Systems: Session V
12:30 PM – 1:30 PM	Main Auditorium, 5 th Floor, Administrative Building	Valedictory Session
1:30 PM – 2:30 PM	Terrace, 5 th Floor, Administrative Building	Lunch