National Institute of Technology Delhi
Department of Electronics and Communication Engineering
First International Conference
Electronics, Communication and Signal Processing (ICECSP 2024)
August 08 – 10, 2024

Day- 1: August 08, 2024 (Thursday)		
Timing	**Venue**	**Programme**
9:30 AM – 10:30 AM	Outside Main Auditorium, 5th Floor, Administrative Building	Registration
10:30 AM – 11:30 AM	Main Auditorium, 5th Floor, Administrative Building	**Inauguration**

10:30 AM -10:35 AM	Welcome Address
10:35 AM – 10:40 AM	Lamp Lightning with Saraswati Vandana
10:40 AM – 10:55 AM	Felicitation of Guests on the Dias
10:55 AM – 11:00 AM	About ECE Department by Head ECE
11:00 AM – 11:05 AM	Introduction of the Conference by the Conference Chair
11:05 AM – 11:10 AM	Address by the IEEE Delhi Section
11:10 AM – 11:15 AM	Address by the Chief Guest
11: 15 AM – 11:20 AM	Address by the Conference General Chair and Director of NIT Delhi
11:20 AM – 11:25 AM	Concluding Remarks with Vote of Thanks
11:25 AM – 11:30 AM	Group Photograph

Timing	Venue	Programme
11:35 AM – 12:00 Noon	Terrace, 5th Floor, Administrative Building	High Tea
12:00 Noon – 1:15 PM	Main Auditorium 5th Floor, Administrative Building	*Key Note Address 1* **Prof. Sudeb Dasgupta**, Department of Electronics and Communication Engineering, IIT Roorkee
1:15 PM – 2:00 PM	Terrace, 5th Floor, Administrative Building	Lunch

2:30 PM – 5:00 PM	**Technical Sessions (Afternoon Sessions)**		
	Main Auditorium 5th Floor, Administrative Building	**Communication Systems and Networks: Session I**	Offline Presentations: Paper IDs: 102, 264, 288, 304, 363, 484 Online Presentations: Paper IDs: 31, 93, 99
	Committee Room 5th Floor, Administrative Building	**Signal Processing and Applications: Session I**	Offline Presentations: Paper IDs: 18, 59, 468 Online Presentations: Paper IDs: 40, 64, 161, 169
	Committee Room 4th Floor, Administrative Building	**VLSI Technology and Embedded Systems: Session I**	Offline Presentations: Paper IDs: 25, 183, 591 Online Presentations: Paper IDs: 80, 81, 147, 160, 217
	T&P Committee Room 3rd Floor, Administrative Building	**Microelectronic Circuits and Systems: Session I**	Offline Presentations: Paper IDs: 450, 538, 651, 592 Online Presentations: Paper IDs: 111, 230, 325, 329
5:00 PM – 5:30 PM	Terrace, 5th Floor, Administrative Building	Tea Break	
5:30 PM – 6:15 PM		*Key Note Address 2* **Prof. Guo En Chang**, National Chung Cheng University, Taiwan	

Day - 2: August 09, 2024 (Friday)			
Timing	**Venue**	**Programme**	
9:00 AM – 10:00 AM	ECE Department, First Floor, Mini Campus Building	Registration and Breakfast	
10:00 AM – 11:00 AM	**Technical Sessions (Morning Sessions)**		
	Committee Room No. 126 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session II**	Online Presentations: Paper IDs: 145, 162, 168, 203, 216, 251, 272, 275, 290, 291, 296
	Lab Room No. 122 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session III**	Online Presentations: Paper IDs: 313, 328,338, 342, 348, 349,362, 431, 441, 447, 510
	Committee Room (EE) First Floor, Mini Campus Building	**Signal Processing and Applications: Session II**	Online Presentations: Paper IDs: 171, 213, 234, 236, 240, 242, 259, 273, 295, 297, 515
	Lab Room No. 206 (ECE) Second Floor, Mini Campus Building	**Signal Processing and Applications: Session III**	Online Presentations: Paper IDs: 299, 301, 312, 340, 361, 365, 379, 408, 413, 429, 637, 644
	Committee Room (CE) Ground Floor, Mini Campus Building	**VLSI Technology and Embedded Systems: Session II**	Online Presentations: Paper IDs: 218, 262, 277, 278, 282, 309, 376, 416, 421, 451
	Committee Room (AS) Ground Floor, Mini Campus Building	**Microelectronic Circuits and Systems: Session II**	Online Presentations: Paper IDs: 341,344, 474, 475, 476, 506, 511, 495

11:00 AM – 11:30 AM	ECE Department, First Floor, Mini Campus Building	Tea Break	
11:30 AM – 1:30 PM	Committee Room No. 126 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session II**	Online Presentations: Paper IDs: 145, 162, 168, 203, 216, 251, 272, 275, 290, 291
	Lab Room No. 122 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session III**	Online Presentations: Paper IDs: 313, 328,342, 348, 349,362, 431, 441, 447, 510
	Committee Room (EE) First Floor, Mini Campus Building	**Signal Processing and Applications: Session II**	Online Presentations: Paper IDs: 171, 213, 234, 236, 240, 242, 259, 273, 295, 297, 515.
	Lab Room No. 206 (ECE) Second Floor, Mini Campus Building	**Signal Processing and Applications: Session III**	Online Presentations: Paper IDs: 299, 301, 312, 340, 361, 365, 379, 408, 413, 429, 637, 644
	Committee Room (CE) Ground Floor, Mini Campus Building	**VLSI Technology and Embedded Systems: Session II**	Online Presentations: Paper IDs: 218, 262, 277, 278, 282, 309, 376, 416, 421, 451
	Committee Room (AS) Ground Floor, Mini Campus Building	**Microelectronic Circuits and Systems: Session II**	Online Presentations: Paper IDs: 341,344, 474, 475, 476, 506, 511, 494, 495
1:30 PM – 2:30 PM	ECE Department, First Floor, Mini Campus Building	**Lunch**	
2:30 PM – 4:00 PM	<td colspan="3" align="center">**Technical Sessions (Evening Sessions)**</td>		
	Committee Room No. 126 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session IV**	Online Presentations: Paper IDs: 517, 525, 526, 529, 559, 610, 611, 612
	Lab Room No. 122 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session V**	Online Presentations: Paper IDs: 626, 294, 252, 276, 293
	Committee Room (EE) First Floor, Mini Campus Building	**Signal Processing and Applications: Session IV**	Online Presentations: Paper IDs: 433, 458, 479, 508,

			536, 553, 594, 604, 620, 621, 632
	Committee Room (CE) Ground Floor, Mini Campus Building	**VLSI Technology and Embedded Systems: Session III**	Online Presentations: Paper IDs: 483, 489, 490, 492, 509, 521, 539, 543, 545.
	Committee Room (AS) Ground Floor, Mini Campus Building	**VLSI Technology and Embedded Systems: Session IV**	Online Presentations: Paper IDs: 547, 606, 615, 631, 398
4:00 PM – 4:30 PM	Corridor, ECE Department First Floor, Mini Campus Building	Tea Break	
4:30 PM – 5:30 PM	Committee Room No. 126 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session IV**	Online Presentations: Paper IDs: 517, 525, 526, 529, 559, 610, 611, 612
	Lab Room No. 122 (ECE) First Floor, Mini Campus Building	**Communication Systems and Networks: Session V**	Online Presentations: Paper IDs: 626, 294, 252, 276, 293
	Committee Room (EE) First Floor, Mini Campus Building	**Signal Processing and Applications: Session IV**	Online Presentations: Paper IDs: 433, 458, 479, 508, 536, 553, 594, 604, 620, 621, 632
	Committee Room (CE) Ground Floor, Mini Campus Building	**VLSI Technology and Embedded Systems: Session III**	Online Presentations: Paper IDs: 483, 489, 490, 492, 494, 509, 521, 539, 543, 545.
	Committee Room (AS) Ground Floor, Mini Campus Building	**VLSI Technology and Embedded Systems: Session IV**	Online Presentations: Paper IDs: 547, 606, 615, 631, 398
7:00 PM onwards	Old Campus of NIT Delhi, Narela	Conference Dinner	

Day - 3: August 10, 2024 (Saturday)			
Timing	**Venue**	**Programme**	
9:30 AM – 10:00 AM	Terrace, 5th Floor, Administrative Building	Registration and Breakfast	
10:00 AM – 11:00 AM	**Technical Sessions**		
	Main Auditorium 5th Floor, Administrative Building	**Communication Systems and Networks: Session VI**	Offline Presentations: Paper IDs: 384, 435, 438, 544, 552, 597,638, 641
	Committee Room 5th Floor, Administrative Building	**Signal Processing and Applications: Session V**	Offline Presentations: Paper IDs: 72, 170, 389, 647
	Committee Room 4th Floor, Administrative Building	**VLSI Technology and Embedded Systems: Session V**	Offline Presentations: Paper IDs: 401, 410, 605
11:00 AM – 11:30 AM	Terrace, 5th Floor, Administrative Building	Tea Break	
11:30 AM – 12:30 PM	Main Auditorium 5th Floor, Administrative Building	**Communication Systems and Networks: Session VI**	Offline Presentations: Paper IDs: 384, 435, 438, 544, 552, 597,638, 641
	Committee Room 5th Floor, Administrative Building	**Signal Processing and Applications: Session V**	Offline Presentations: Paper IDs: 72, 170, 389, 647
	Committee Room 4th Floor, Administrative Building	**VLSI Technology and Embedded Systems: Session V**	Offline Presentations: Paper IDs: 401, 410, 605

12:30 PM – 1:30 PM	Main Auditorium 5th Floor, Administrative Building	**Valedictory Session:**	
		12:30 PM – 12:35 PM	Welcome Address
		12:35 PM – 12:45 PM	Felicitation of Guests on the Dias
		12:45 PM – 12:50 PM	Concluding Remarks of the Conference by the Conference Chair
		12:50 PM – 1:00 PM	Address by the Guest of Honour of the Valedictory Session
		1:00 PM – 1:10 PM	Address by the Chief Guest of the Valedictory Session
		1:10 PM – 1:20 PM	Address by the Conference General Chair and Director of NIT Delhi
		1:20 PM – 1:25 PM	Vote of Thanks
		1:25 PM – 1:30 PM	Group Photograph
1:30 PM – 2:30 PM	Terrace, 5th Floor, Administrative Building	Lunch	