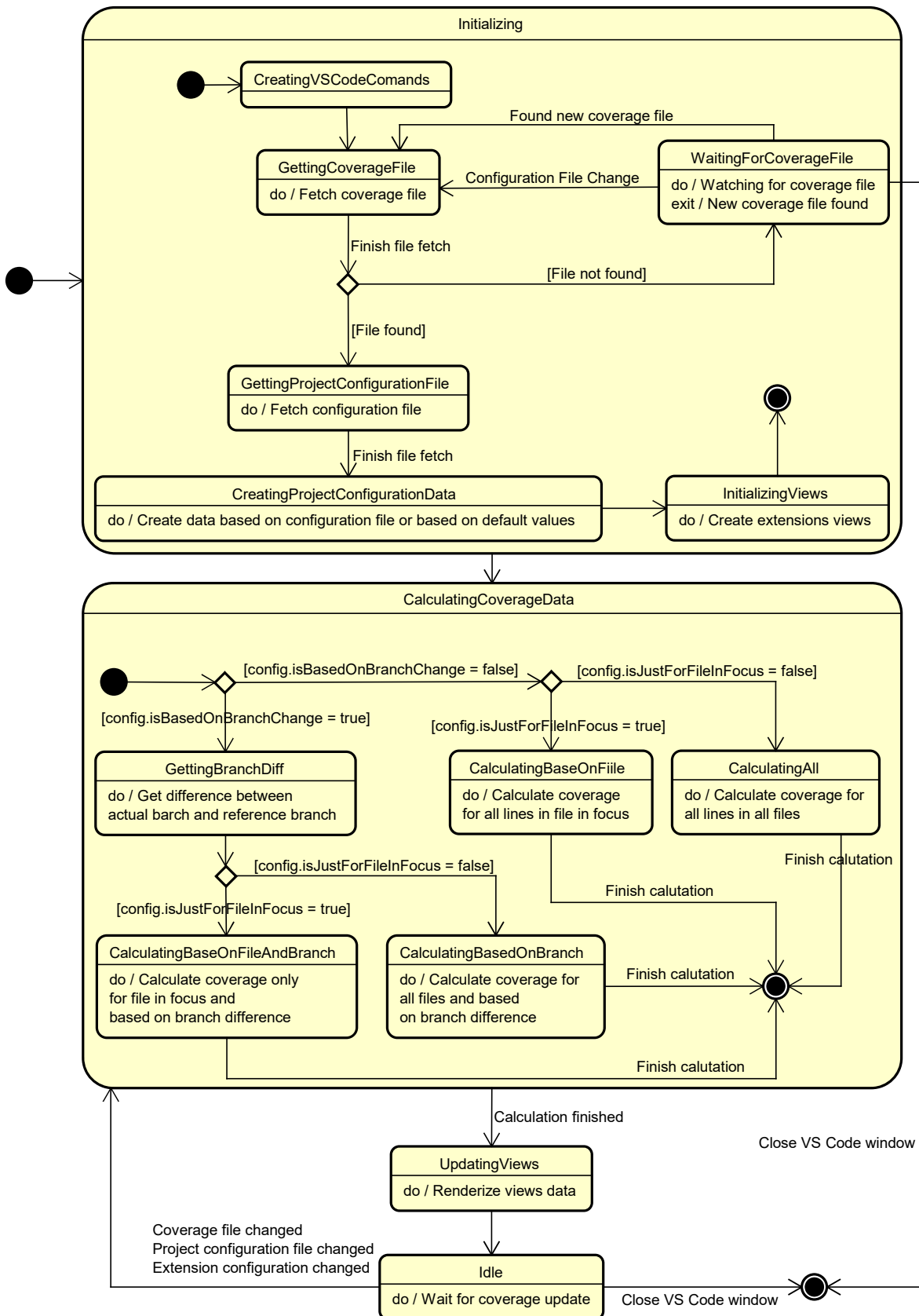


stm Covering Statemachine Diagram



stm Covering StateMachine Diagram Simple Version

