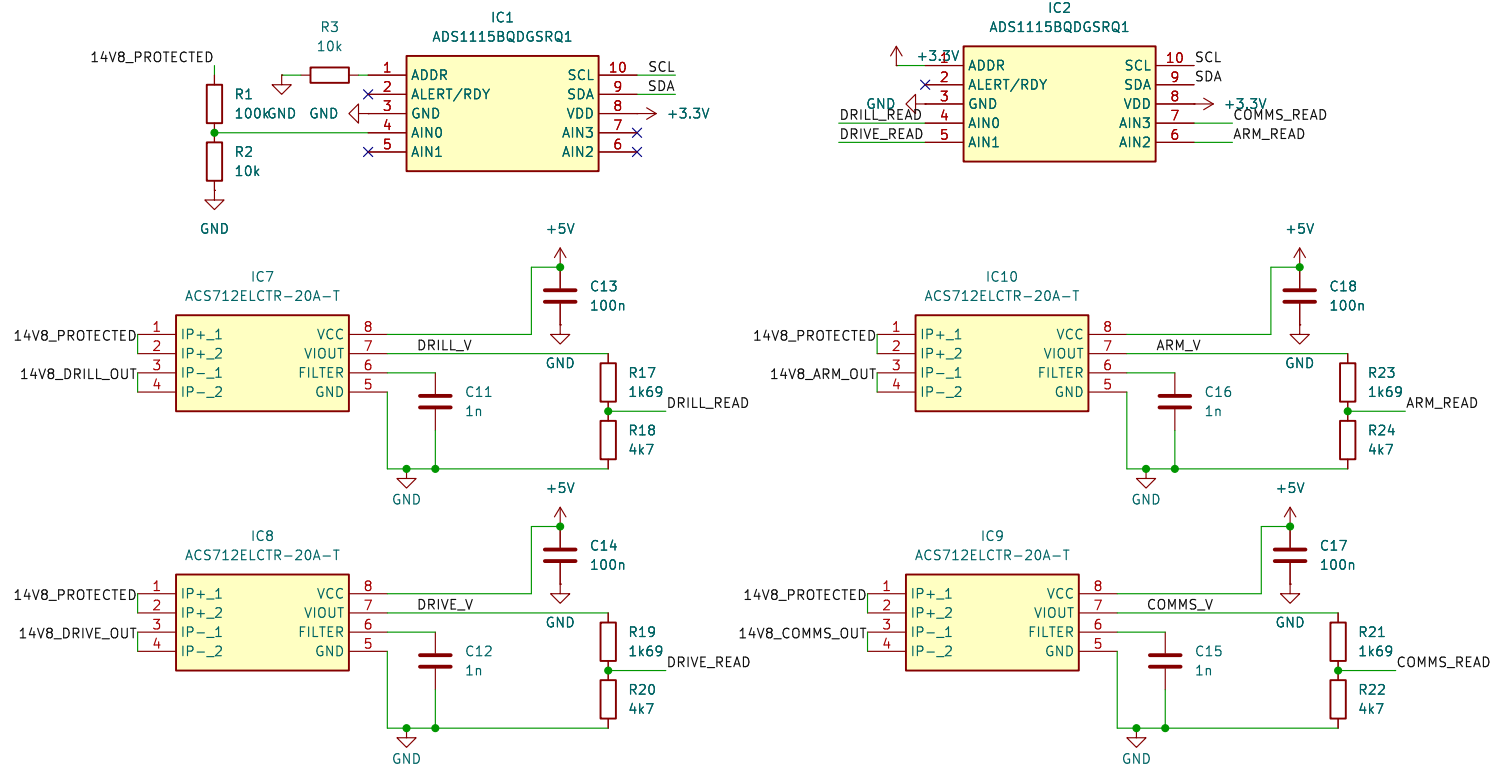


Resistor values changed to step voltage down to 3.3 maximum
- Double check PD on battery voltage

14V8_PROTECTED



D>SCL
D>SDA
D>14V8_ARM_OUT
D>14V8_DRILL_OUT
D>14V8_DRIVE_OUT
D>14V8_COMMS_OUT

Sheet: /Current_sensing/
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- Check that we need a XT90 connector for input voltage
- Check if we need an XT30 or a screw terminal for the input 5V

Input connectors

The diagram shows two input connectors, J1 and J2, represented as yellow rectangular boxes with two pins each. For J1, the top pin (labeled '1') is connected to 'GND' and the bottom pin (labeled '2') is connected to '+5V'. For J2, the top pin (labeled '1') is connected to 'GND' and the bottom pin (labeled '2') is connected to '4V8_IN'. The labels 'J1 Conn_01x02' and 'J2 Conn_01x02' are placed to the right of each connector box.

Power step down and verification

The diagram illustrates a power step-down circuit using the AMS1117-3.3 voltage regulator (U2). The circuit includes the following components and connections:

- Input Section:** A +5V input is connected to the input pin (VI, pin 3) of the regulator. A 1k resistor (R7) and an LED (D2) are connected in series between the +5V input and ground (GND).
- Regulator Section:** The AMS1117-3.3 regulator (U2) is shown with its input pin (VI, pin 3) connected to the +5V input, its ground pin (GND, pin 1) connected to ground, and its output pin (VO, pin 2) connected to the +3.3V output.
- Output Section:** The +3.3V output is connected to the output pin (VO, pin 2) of the regulator. A 10uF capacitor (C10) is connected between the +3.3V output and ground. A 1k resistor (R8) and an LED (D3) are connected in series between the +3.3V output and ground.
- Capacitors:** A 10uF capacitor (C9) is connected between the +5V input and ground. A 10uF capacitor (C10) is connected between the +3.3V output and ground.

[illegible]

14V8_PROTECTED — 14V8_PROTECTED
+5V — +5V
+3.3V — +3.3V

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