Digital Thread Reference Architecture

This is a placeholder for the layered DT architecture diagram.

The actual diagram should illustrate:

- Layer 1: Data Layer (MDM, Time-series, PLM/PDM)
- Layer 2: Model Layer (CAD/CAE, MBSE, Digital Twin)
- Layer 3: Service Layer (Validation, Simulation, Analytics)
- Layer 4: Application Layer (Engineering tools, MES, Dashboards)

Cross-cutting: Security, Data Quality, Interoperability