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| Exercício 6 |
| Input frequency = 24 MHz (para poder usar a porta USB 48 MHz pra visualizar os sinais) |
| System clock (APB2) = 54 Mhz |
| Prescaler (PSC) = 24 – 1 |
| Timer1 clock = 54 MHz / 24 = 2.25 MHz |
| ARR = 2.25 MHz / 120 (2x60 Hz) = 18750 – 1 |
| Channel 1 pulse (CCR1) = 3 / 6 \* ARR = 9375; Polarity = Low |
| Channel 2 pulse (CCR2) = 1 / 6 \* ARR = 3125; Polarity = High |
| Channel 3 pulse (CCR3) = 5 / 6 \* ARR = 15625; Polarity = High |

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|  |  |  | 2 \* ARR = T = 2 \* 8.333 ms = 16.666 ms 🡪 f = 1/T = 60 Hz | | | | | | | | | | | |  |  |  |
| Canal 1 | | | | | | | | | | | | | | | | | |
| ARR | | | | | | ARR | | | | | | ARR | | | | | |
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| Low |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| Canal 2 | | | | | | | | | | | | | | | | | |
| ARR | | | | | | ARR | | | | | | ARR | | | | | |
| High |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  | Atraso = 04/6\*ARR | | | |  |  |  |  |  |  |  |  |  |  |  |
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| Canal 3 | | | | | | | | | | | | | | | | | |
| ARR | | | | | | ARR | | | | | | ARR | | | | | |
| High |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  | Atraso = 4/6\*ARR | | | |  |  |  |  |  |  |  |
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