Schedules are subject to change and any changes will be posted on IEEEUTD.org and the IEEE Facebook Page, please check these for any changes as there are days when tutors may have to cancel their hours.

Tutors are listed by start times but their hours do overlap with each other.

EE/CE 3320 Digital Circuits Tutor Schedule

Located @ SPN 2.220 Doors open from 8AM-8PM

Time	Monday	Tuesday	Wednesday	Thursday	Friday
8:00 AM					
8:15 AM					
8:30 AM					
8:45 AM					
9:00 AM					
9:15 AM					
9:30 AM					
9:45 AM					
10:00 AM					
10:15 AM					
10:30 AM					
10:45 AM					
11:00 AM					
11:15 AM					
11:30 AM					
11:45 AM					
12:00 PM					
12:15 PM					
12:30 PM					
12:45 PM					
1:00 PM	Michael Nelson				
1:15 PM	12:00PM-2:00PM	Europe De monthe en	D II Th	E	
1:30 PM 1:45 PM		Jimmy Deguenther	Russell Thurman	Jimmy Deguenther	
	Russell Thurman	12:00PM-2:00PM	12:00PM-3:00PM	12:00PM-2:00PM	
2:00 PM 2:15 PM	1:00PM-3:00PM	Lukas Zekarias	Eddia Vanana	Michael Nelson	
2:30 PM		1:00PM-4:00PM	Eddie Vergara 3:00PM-5:00PM	1:00PM-3:00PM	
2:45 PM	Eddie Vergara	1.00FW-4.00FW	3.00FW-3.00FW	1.00/101-3.00/101	
3:00 PM	3:00PM-5:00PM	Jack Doan*	Chris Tsongas	Jack Doan*	
3:15 PM		3:00PM-5:00PM	3:00PM-5:00PM	2:30PM-5:00PM	
3:30 PM	Chris Tsongas	3.001 111 3.001 111	3.001 111 3.001 111	2.301 111 3.001 111	
3:45 PM	3:00PM-5:00PM				
4:00 PM					
4:15 PM					
4:30 PM					
4:45 PM					
5:00 PM					
5:15 PM					
5:30 PM					
5:45 PM					
6:00 PM					
6:15 PM					
6:30 PM					
6:45 PM					
7:00 PM					
7:15 PM					
7:30 PM					
7:45 PM					

^{*} Tutors Multiple Subjects