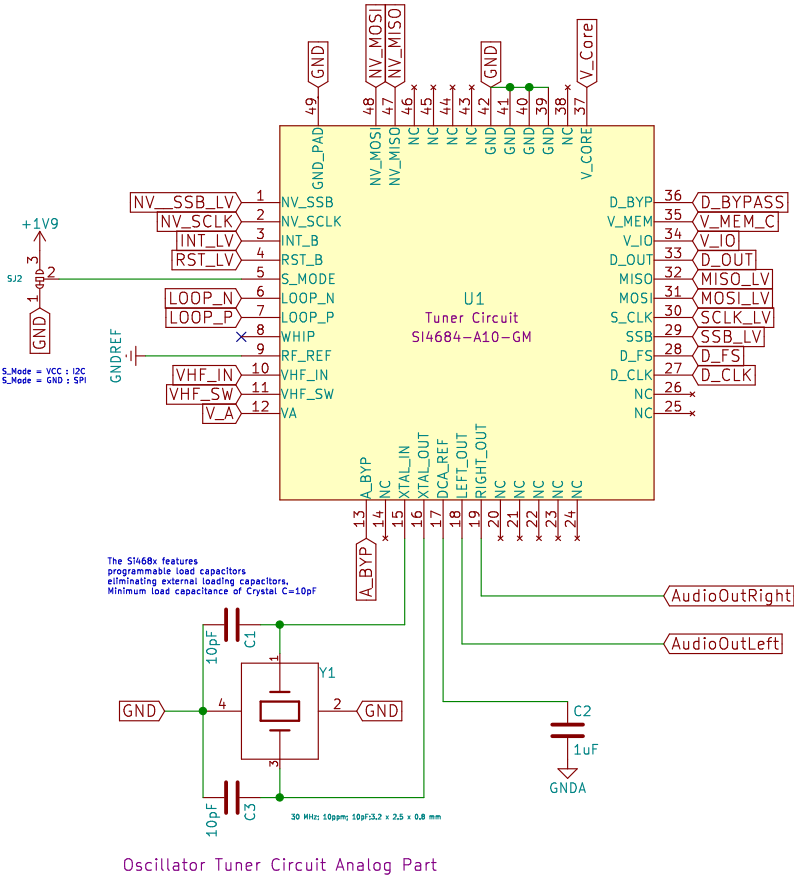


0.8.0

Changed: Bandpass filter in RF Front End for filtering the VCO spur – done
Changed: Reworked tactile switch pinning – done
Changed: Move Tactile switch to A0 – done
Changed: Logic Level Shifter from TXB0108 to TXS0108E – done
Added: Headphone remote function circuit via pin A1 – done
Added: Headphone antenna circuit – done
Added: Voltage selector 5V and 3.3V – done
Changed: Changed to SST26WF flash memory – done
Changed: Disconnected 5V, RESET from ICSP connector – done



Sheet: Version History

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File: Power Supply.sch

Sheet: Connectors

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Sheet: Audio

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Sheet: Flash Memory

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1.9V LDO Voltage Regulator

After Power Off is a RESET necessary to load firmware

Voltage Regulator Circuit
TPS73219DBVR

The diagram illustrates a Voltage Regulator Circuit using the TPS73219DBVR IC. The circuit is powered by VCC and GND. The input of the regulator (IN) is connected to VCC through a diode D1 and a 10uF capacitor C4. The enable pin (EN) is connected to GND through a 100nF capacitor C5. The output of the regulator (OUT) is connected to the load through a 10nF capacitor C6 and a 10uF capacitor C7. The output voltage is +1V9, which is also connected to a load resistor R2 and a diode D2 (1.95V). The ground pin (GND) is connected to GND through a 100nF capacitor C5. The output pin (OUT) is connected to the load through a 10nF capacitor C6 and a 10uF capacitor C7. The output voltage is +1V9, which is also connected to a load resistor R2 and a diode D2 (1.95V).

VCC

GND

D1

C4 10uF

C5 100nF

U2

IN

EN

GND

OUT

NR

C6 10nF

C7 10uF

+1V9

R2

D2 1.95V

VCC_5V

VCC

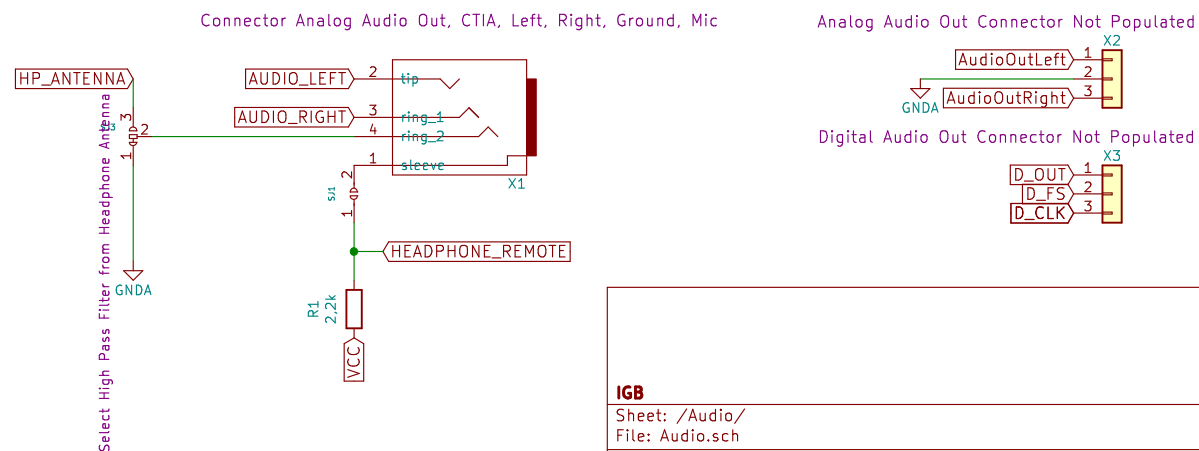
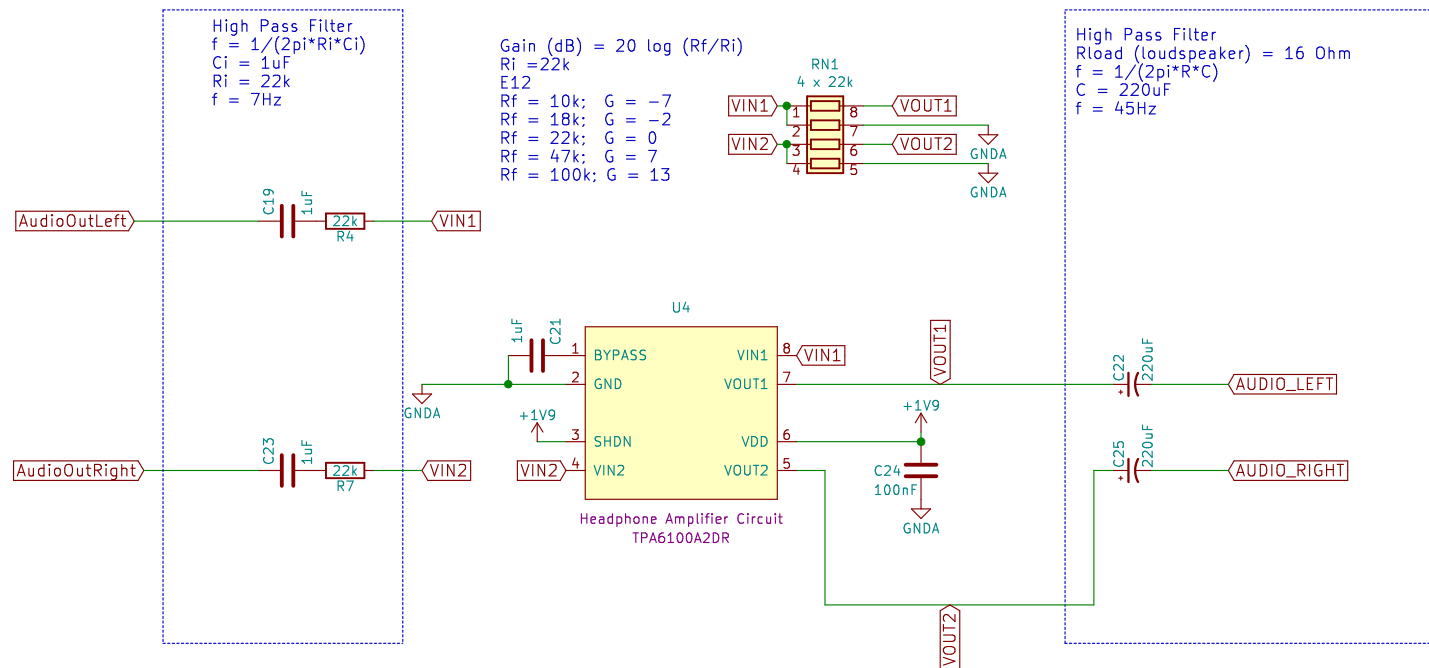
VCC_3.3V

Power Supply Decoupling

The diagram illustrates three power supply decoupling circuits for different voltage rails. Each circuit consists of a series of capacitors connected to a common ground. The capacitors are labeled with their values: 1uF, 10nF, and 10pF. The output rails are labeled V_MEM_C, V_Core, D_BYPASS, V_IO, D_BYPASS, V_A, and A_BYP.

- Top Circuit:** A +1V9 supply is connected to a series of capacitors C10 (1uF), C11 (10nF), and C12 (10pF). The output rails are V_MEM_C and V_Core, with a D_BYPASS connection.
- Middle Circuit:** A +1V9 supply is connected to a series of capacitors C13 (1uF), C14 (10nF), and C15 (10pF). The output rails are V_IO and D_BYPASS.
- Bottom Circuit:** A +1V9 supply is connected to a series of capacitors C16 (1uF), C17 (10nF), and C18 (10pF). The output rails are V_A and A_BYP.





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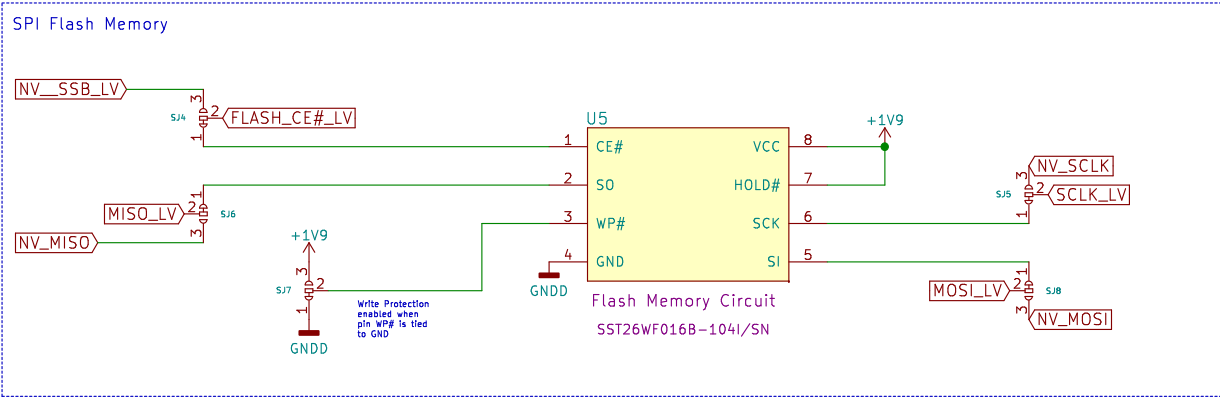
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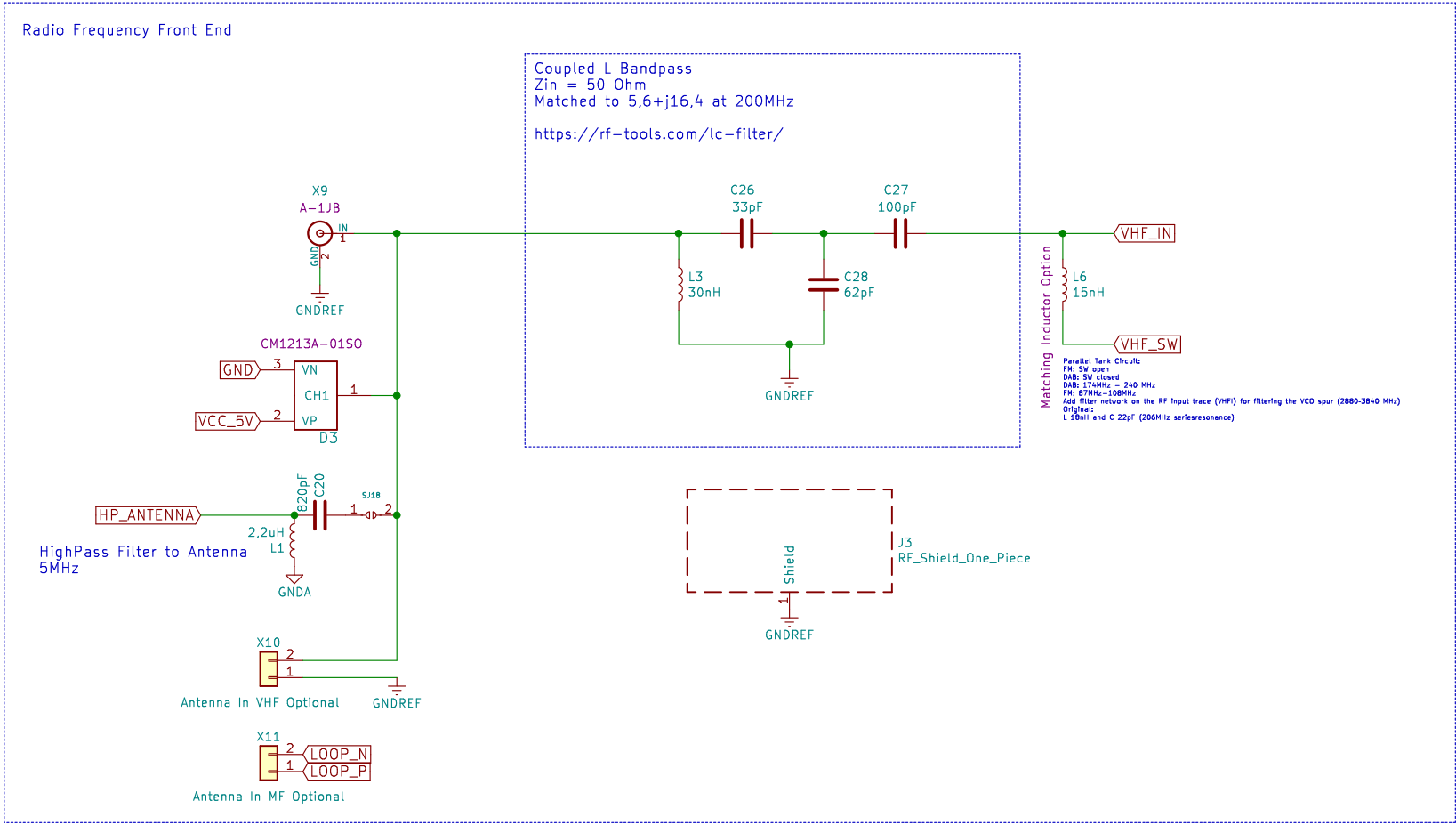
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	1	2	3	4	5	6
A	Roadmap:					A
B	1) Optimizing:					B
	New: 100k pullup resistors for SlaveSelect pins SSB_HV, FLASH_CE_HV – open					
	Changed: Transmission Line VHF In 50 Ohm – open					
	New: EMI Shielding – open					
	Changed: 22k Resistor Array for Audio Amplifier – open					
	New: Add ferrite bead L in voltage supply line for providing high impedance to any VCO leakage – open					
	Changed: Add operating temperature rating industrial –40°C – +85°C – open					
	2) New Functions:					
	New: On/off switch – open					
	New: Selector 3.3V to 5V in Voltage Supply – open					
	New: Low Noise Amplifier (LNA) in Frontend – open					
	New: I2S DAC amplifier – open					
	New: Fractal Antenna on Backside – open					
	New: Mobile App with Juce –open					
	New: Bluetooth Shield integration – open					
	New: Amplifier: shutdown pin, additional pin necessary – open					
	New: Amplifier: Programmable resistor/poti for variable Gain, additional pin necessary – open					
	New: LED array WS2812 for status bootloader, application, RSSI, SNR, FIC quality – open					
	3) Cancelled:					
	100k bypass resistor on DAC_Ref Tuner Circuit – No improvement – done					
	Known Issues:					
	Acceptable: NVSPI Flash only working if not connected to other SPI					
	– NVSPI Flash function disabled					
	Critical: 100Hz noise in audio signal is from pc usb connector					
	– Reduction with large bulk capacitor					
	Critical: Noise when loading firmware					
	– Audio lines routing away from SPI bus					
	Critical: When audio jack with remote functionis plugged in tactile switch signal is generated leading to start a function					
	– Actually not solved and feature disabled					
C	0.7.0 Schematic: Changed: Added sub schematic for version history, connectors, antenna – done Changed: Power Symbol 1.8V to 1.9V – done Changed: Amplifier Midrail Bypass Capacitor = 10uF to reduce noise – done Changed: Amplifier Rf, Rf, Rb = 22k to have proper amplification for headphones – done Changed: Amplifier VCC = 1.9V to have right voltage – done Changed: Audio Jack compatiBEL to CTIA with ring2 to GND – done Changed: Antenna filter to pi bandpass – done Changed: Tuner IC pin 8 disconnected (w/HP antenna) – done New: Added 10uF Bulk Capacitor at LDO Ingress to reduce noise – done New: LED resistor to dim light – done					C
	Changed: Check operating temperature rating Industrial –40°C – +85°C					
	0.6.0 Changed: 5/1, 5/2 land pattern like schematic – done Changed: Removed solder mask from all solder jumpers to be able to close by soldering – done Changed: Rewired Audio out Jack land pattern and schematic – done Changed: Added 100k pullups/down resistors flash memory – done Changed: Moved land patterns of all connectors in raster 1° – done Changed: Audio Jack sleeves direct to GND – done Changed: Audio gain from 20 (Rf = 100k) to 1.6 (Rf = 12k) – done Changed: Supply Voltage from 1.8V –>1.9V – done Changed: NV_SPI connections separated – done New: Solder Jumper between audio jack ring and analog In – done New: TVS Diode added – done					
	0.5.2 Changed: ICxx changed to Uxx –done Changed: Tactile switch changed from 5V to GND – done Changed: Move digital pin option 6.8 to analog pin 2.3 for compatibility with lcd shield – done Changed: Naming of connectors acc UN 81346-2 changed from Pxx to Xxx – done Changed: Audio Jack with Aux ring to A1– done Changed: Turn slii screen LED caboose is wrong – done Changed: Resize land pattern from 0603 to 0805 – done Changed: Resize land pattern, orientation oscillator– done New: Audio Out Schematic – done New: Solder jumpers at digital pin 6.8 and analog pin 2.3 for compatibility with lcd shield – done New: Head phone antenna option – done New: ISP connector for MEGA spi combatibility – done New: Head Phone Amplifier – done New: footprint phone Jack sj-A351x-sm – done New: Floating pin A1, B1 from Voltage Level Translator to GND – done New: Vias and GND plane below IC – done New: Gndref on bottom side of pcb for controlled Impedance – done New: Separate gnd for oscillator					
	0.5.1 Changed: Tuner chip PINS from GNDD net to GND net changed: Tuner chip PIN 39–42 from GNDD to GND Changed: Power and GND lines from 0.25mm to 0.3mm Changed: Orientation of PCB audio out left, antenna in right for housing, new crystal, changed footprints Changed: Compatibility with adafruit HT cap touch screen pinning Changed: Compatibility with Arduino Project Enclosure SparkFun PRT–1008B Changed: Compatibility with Arduino Project Enclosure Adafruit ID: 337 New: tactile switch analog pin 1 New: Logic Level Translator TXB010B, changed footprint New: footprint solder jumper to smaller New: I2C/SPI solder switch					
D	Initial New: Solder Pads for selectors (Bus Mode, WP) – done New: Audio Out Connector Jack 3.9mm – done New: Flash 1.8V – done New: GND pavilions – done New: SMA A1A U.F.L Jack Solder Pad for Antenna – done New: Arduino Uno connector – done New: Arduino Uno mechanical check – done New: ERC; All GND as Power Input –done					D
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				IGB		
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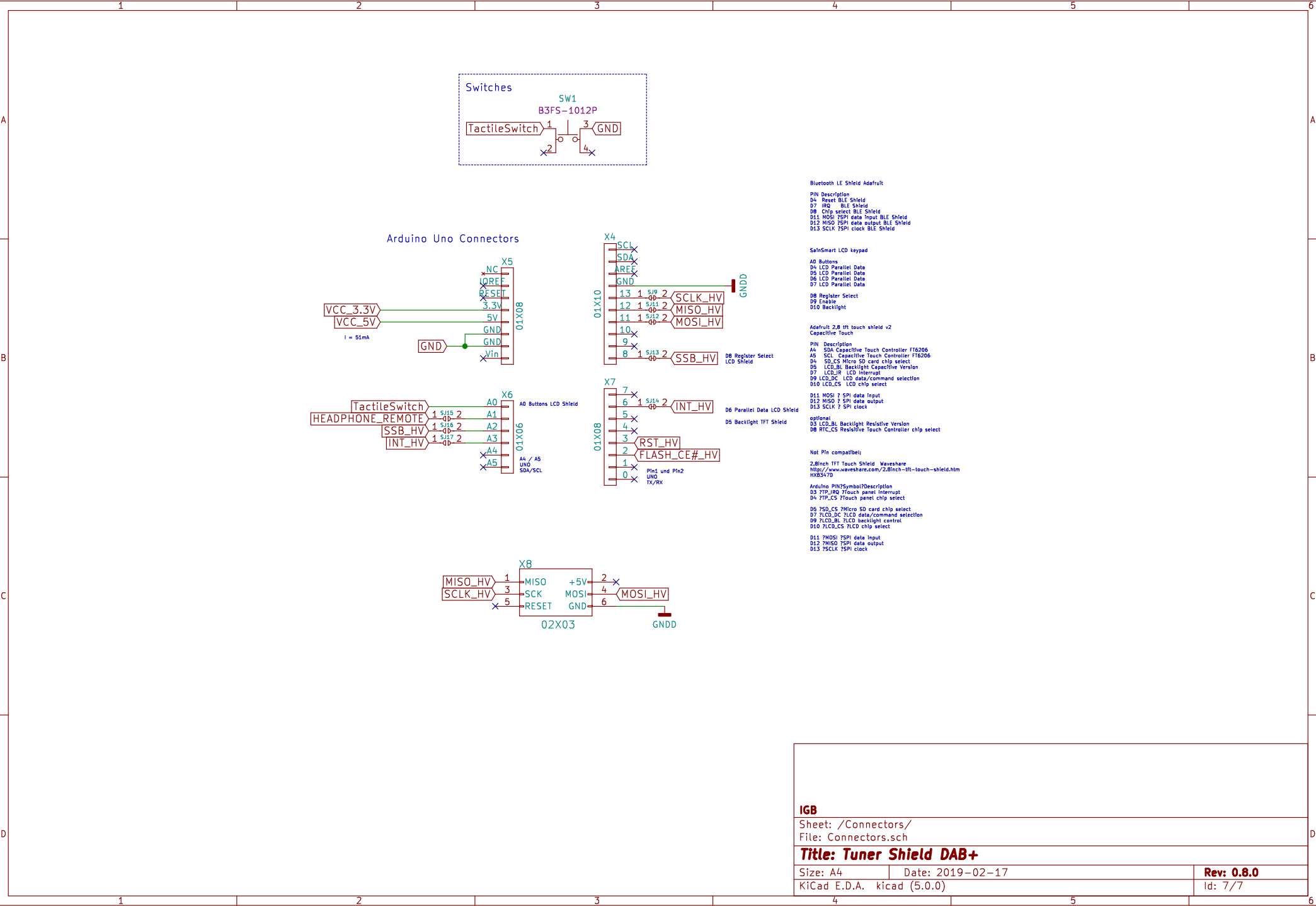
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