Compliant with IEC 62474/ D9.00

| MICROCHIP Semiconductor Device Type: PT / PF (V3X) 064 TQFP 14x14x1mi | | | Termination Base Alloy: Copper Alloy (Cu) | | | Package Homogeneous Materials: 8.1 Electronics (e.g. pc boards, displays) | | | | JEDEC 97 Product Marking and/or Pkg. Labeling e3 |
|---|---|--|--|--|---|--|---|---|---|--|
| Basic Substance | CAS Number | "Contained In" Sub-Component | % I otal Weight | mg/part | ppm | 289.33 | (mg) Total | Mold Compound | % ot Total Weight | 53.58 |
| Silica, vitreous (or fused) | 60676-86-0 | Mold Compound | 45.543 | 245.932 | 455,430 | | Silica, vitreous (or fused) | 60676-86-0 | 85.00 | i e |
| Epoxy Resin | Trade Secret | Mold Compound | 4.661 | 25.172 | 46,615 | | Epoxy Resin | Trade Secret | 8.70 | 1 |
| Phenolic Resin | Trade Secret | Mold Compound | 3.215 | 17.360 | 32,148 | | Phenolic Resin | Trade Secret | 6.00 | 1 |
| Carbon Black | 1333-86-4 | Mold Compound | 0.161 | 0.868 | 1,607 | | Carbon Black | 1333-86-4 | 0.30 |] |
| Copper | 7440-50-8 | Lead Frame | 32.381 | 174.856 | 323,807 | | | Total | 100.00 | |
| Tin | 7440-31-5 | Lead Frame | 0.083 | 0.449 | 831 | 179.50 | (mg) Total | Lead Frame | % of Total Weight | 33.24 |
| Silver | 7440-22-4 | Lead Frame | 0.633 | 3.419 | 6,332 | | Copper | 7440-50-8 | 97.42 | |
| Zinc | 7440-66-6 | Lead Frame | 0.060 | 0.323 | 598 | | Tin | 7440-31-5 | 0.25 |] |
| Chromium | 7440-47-3 | Lead Frame | 0.083 | 0.449 | 831 | | Silver | 7440-22-4 | 1.91 |] |
| Silver (Ag) | 7440-22-4 | Die Attach | 1.129 | 6.096 | 11,288 | | Zinc | 7440-66-6 | 0.18 | |
| ANHYDRIDE | Trade Secret | Die Attach | 0.122 | 0.661 | 1,224 | | Chromium | 7440-47-3 | 0.25 | ļ |
| EPOXY RESIN | Trade Secret | Die Attach | 0.109 | 0.588 | 1,088 | | | Total | 100.00 | |
| Silicon | 7440-21-3 | Chip (Die) | 10.540 | 56.916 | 105,400 | 7.34 | (mg) Total | Die Attach | % of Total Weight | 1.36 |
| Gold | 7440-57-5 | Wire Bond | 0.340 | 1.836 | 3,400 | | Silver (Ag) | 7440-22-4 | 83.00 | |
| Tin | 7440-31-5 | Plating on external leads (pins) - Matte Tin / annealed at 150°C for 1 hour | 0.940 | 5.076 | 9,400 | | ANHYDRIDE | Trade Secret | 9.00 | |
| | | TOTALS: | 100.000 | 540.000 | 1,000,000 | | EPOXY RESIN | Trade Secret | 8.00 | ! |
| | 0.5400 | g Total Mass | | | | | | Total | 100.00 | |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified v | emption (zero) ria internal design contro | • | | | | 56.92 | Total (mg) Doped Silicon | 7440-21-3 Total | % of Total Weight 100.00 100.00 | |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified vehicles (substance is absent from the list above, the cheprorated's knowledge and belief as of the date of this do | emption (zero) ria internal design contro emical substance is NOT cument, there is no credi | ls, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce | , to the best of | Microchip Ted | chnology | 56.92 | , | 7440-21-3 | 100.00 | |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified vehicles have been verified vehicles above, the cheorporated's knowledge and belief as of the date of this doing is not below the threshold of regulatory concern for any | emption (zero) ria internal design control emical substance is NOT cument, there is no credi regulatory scheme world ummability standard for p | ls, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce | , to the best of ntration of the | Microchip Teo | chnology | 1.84 | , | 7440-21-3 | 100.00 | |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified vachemical substance is absent from the list above, the cheorporated's knowledge and belief as of the date of this do, is not below the threshold of regulatory concern for any lding compounds used by Microchip meet the UL94 V0 flas://ul.com/global/eng/pages/offerings/industries/chemicals | emption (zero) ria internal design control emical substance is NOT cument, there is no credi regulatory scheme work immability standard for p s/plastics/ | ls, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce d-wide. | , to the best of ntration of the | Microchip Tec chemical subs | chnology stance, if | | Doped Silicon | 7440-21-3 Total | 100.00 | |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified vehicles (ELV) without exempliance with the above EU Directives has been verified vehicles as the sense of the date of this does not below the threshold of regulatory concern for any idding compounds used by Microchip meet the UL94 V0 flast/ful.com/global/eng/pages/offerings/industries/chemicals protective "tubes" in which the specific product is shipp tain "reels" may be made from PVC plastic. rochip Technology Incorporated believes the information ir original packing materials is true and correct to the best protectives of the protected from disclosure as trade secretariation is often protected from disclosure as trade secretariation is often protected from disclosure as trade secretarias. | emption (zero) ria internal design control emical substance is NOT cument, there is no credi regulatory scheme work immability standard for p s/plastics/ end are made from polyvi in this form concerning t of its knowledge and be as been compiled based it ts and some information rts and the average weig | Is, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conced-wide. lastics. You can access the UL iQTM family of databases the concentration of th | to the best of ntration of the co obtain a test old the packing ncorporated's gy Incorporate vided by raw m and raw materi | Microchip Tec chemical subs report at g slip on the ou semiconducto d cannot guara taterial supplie | chnology stance, if uter box and or devices in antee the ers. Supplier | | Doped Silicon (mg) Total | 7440-21-3 Total Wire Bond | 100.00 100.00 % of Total Weight | |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified with the above EU Directives has been verified with the above EU Directives has been verified with the comporated's knowledge and belief as of the date of this doing to the low the threshold of regulatory concern for any low long compounds used by Microchip meet the UL94 V0 flactiful configlobal/eng/pages/offerings/industries/chemicals by protective "tubes" in which the specific product is shipped than "reels" may be made from PVC plastic. Trochip Technology Incorporated believes the information in original packing materials is true and correct to the best pleteness and accuracy of data in this form because it homation is often protected from disclosure as trade sector wided only as estimates of the average weight of these palopants, metals, and non-metal materials contained withir rochip Technology Incorporated does not provide any was | emption (zero) ria internal design control ria internal design control remical substance is NOT cument, there is no credi regulatory scheme work regulatory scheme work singulatics/ red are made from polyvir in this form concerning t of its knowledge and be as been compiled based of test and some information rits and the average weig n silicon devices (silicon arranty, express or implie | Is, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conced-wide. lastics. You can access the UL iQTM family of databases the concentration of th | to the best of ntration of the coobtain a test old the packing ncorporated's agy incorporated by raw mand raw materiese estimates dution. The exclution of the cook of the coo | Microchip Tec chemical subs report at g slip on the or semiconducto d cannot guara aterial supplie al suppliers. Ii lo not include | chnology stance, if uter box and or devices in antee the ers. Supplier restraction is trace levels | | Doped Silicon (mg) Total Doped Gold (mg) Total | 7440-21-3 Total Wire Bond 7440-57-5 | 100.00 100.00 % of Total Weight | 0.34 |
| 5) and 2002/53/EC (End-of-Life Vehicles (ELV) without exempliance with the above EU Directives has been verified vehicles (ELV) without exempliance with the above EU Directives has been verified vehicles and the sent of the date of this does not provided and belief as of the date of this does not provided by the sent of the date of this does not provided by the sent of the date of the date of the date of this does not provided by the sent of the date of the | emption (zero) ria internal design control remical substance is NOT cument, there is no credi regulatory scheme work urregulatory scheme work ummability standard for p s/plastics/ red are made from polyvir in this form concerning t of its knowledge and be as been compiled based of tsts and some information rits and the average weig n silicon devices (silicon urranty, express or implie and its subsidiaries are co- nanges to Material Contei cusers' reliance on the in | Is, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conced-wide. Iastics. You can access the UL iQTM family of databases the concentration of the | n, to the best of ntration of the so obtain a test old the packing ncorporated's gy Incorporate vided by raw m and raw materi see estimates d tion. The exclu sale. These are | Microchip Tecchemical substreport at solution semiconducto d cannot guaraterial supplies al suppliers. In lo not include sive, limited p provided in No., consequential substreport of the sive, consequential substrates the sive of the siv | chnology stance, if uter box and or devices in antee the ers. Supplier information is trace levels roduct flicrochip's | 1.84 | Doped Silicon (mg) Total Doped Gold (mg) Total | 7440-21-3 Total Wire Bond 7440-57-5 Total Plating on external leads (pins) - Matte Tin / annealed at 150°C for 1 | 100.00 100.00 % of Total Weight 100.00 | 0.34 |

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