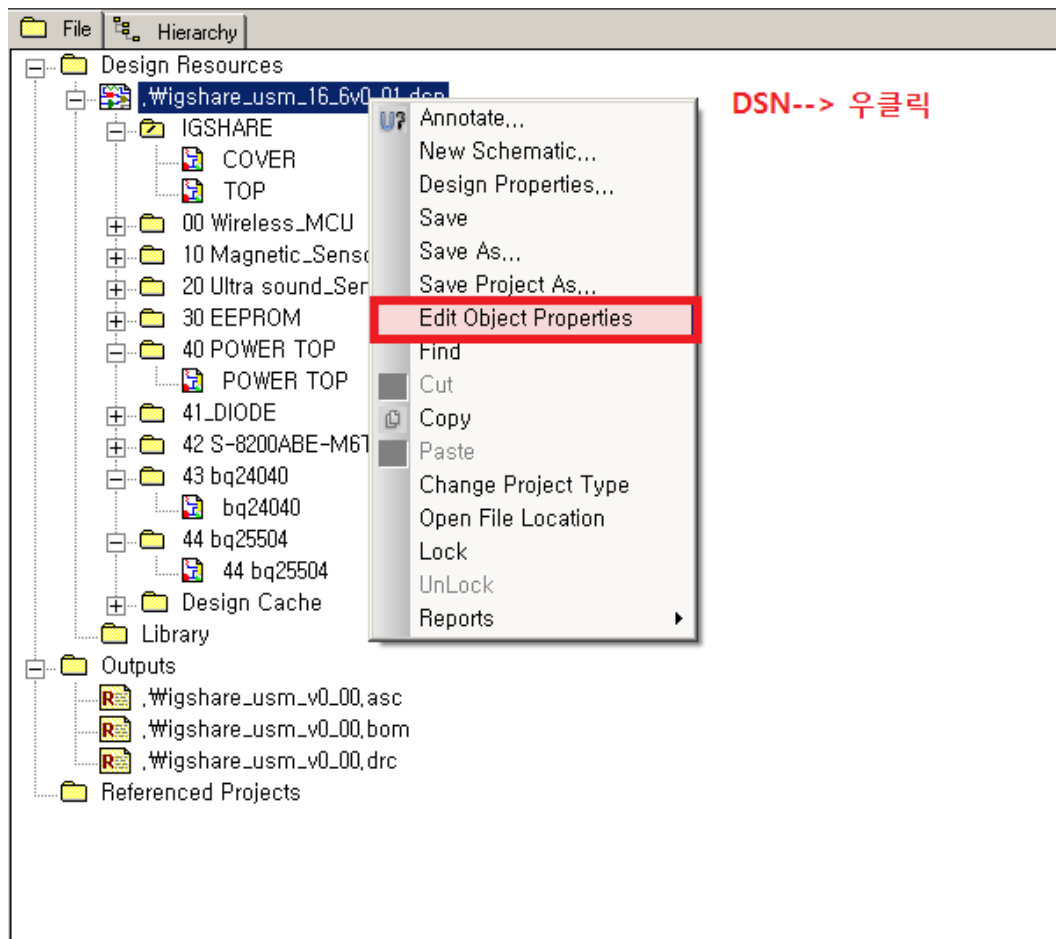


## **Artwork 핀 수 확인 방법**



Start Page IGSHARE_... TOP Wireless... IGSHARE_...				
New Property... Apply Display... Delete Property Pivot Filter by: < Current properties >				
	Is No Connect	Name	Net Name	Number
45	<input type="checkbox"/>	VBAT	N16775934	14
45	<input type="checkbox"/>	VBAT_OK	RESERVED0	11
45	<input type="checkbox"/>	VBAT_OV	N16774758	6
45	<input type="checkbox"/>	VBAT_UV	N16775001	8
45	<input type="checkbox"/>	VIN_DC	N16774118	2
45	<input type="checkbox"/>	VOC_SAMP	N16774028	3
45	<input type="checkbox"/>	VRDIV	N16775067	7
45	<input type="checkbox"/>	VREF_SAMP	N16774024	4
45	<input type="checkbox"/>	VSS	GND	1
46	<input type="checkbox"/>	VSS	GND	13
46	<input type="checkbox"/>	VSTOR	N16775877	15
46	<input type="checkbox"/>	IGSHARE : COVER : H1 : 1	GND	1
46	<input type="checkbox"/>	IGSHARE : COVER : H2 : 1	GND	1
46	<input type="checkbox"/>	IGSHARE : COVER : H3 : 1	GND	1
46	<input type="checkbox"/>	IGSHARE : COVER : H4 : 1	GND	1
46	<input type="checkbox"/>	IGSHARE : TOP : EEPROM : I	I2C_SCL	6
46	<input type="checkbox"/>	IGSHARE : TOP : EEPROM : I	I2C_SDA	6
46	<input type="checkbox"/>	IGSHARE : TOP : Magnetic_	I2C_SCL	6
46	<input type="checkbox"/>	IGSHARE : TOP : Magnetic_	I2C_SDA	6
47	<input type="checkbox"/>	IGSHARE : TOP : Magnetic_	INT	6
47	<input type="checkbox"/>	IGSHARE : TOP : power : RE	RESERVED0	RESERVED0
47	<input type="checkbox"/>	IGSHARE : TOP : Ultra soun	RX_ADC	RX_ADC
47	<input type="checkbox"/>	IGSHARE : TOP : Ultra soun	TX_TRIG	TX_TRIG
47	<input type="checkbox"/>	IGSHARE : TOP : Wireless_	I2C_SCL	I2C_SCL
47	<input type="checkbox"/>	IGSHARE : TOP : Wireless_	I2C_SDA	I2C_SDA
47	<input type="checkbox"/>	IGSHARE : TOP : Wireless_	INT	INT
47	<input type="checkbox"/>	IGSHARE : TOP : Wireless_	RESERVED0	RESERVED0
47	<input type="checkbox"/>	IGSHARE : TOP : Wireless_	RX_ADC	RX_ADC
47	<input type="checkbox"/>	IGSHARE : TOP : Wireless_	TX_TRIG	TX_TRIG
Parts Schematic Nets Flat Nets Pins Title Blocks Globals Ports Aliases /				
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19				

안보이는 숫자 오른쪽으로 넓게 확장

	Is No Connect	Name
451	<input type="checkbox"/>	VBAT
452	<input type="checkbox"/>	VBAT_OK
453	<input type="checkbox"/>	VBAT_OV
454	<input type="checkbox"/>	VBAT_UV
455	<input type="checkbox"/>	VIN_DC
456	<input type="checkbox"/>	VOC_SAMP
457	<input type="checkbox"/>	VRDIV
458	<input type="checkbox"/>	VREF_SAMP
459	<input type="checkbox"/>	VSS
460	<input type="checkbox"/>	VSS
461	<input type="checkbox"/>	VSTOR
462	<input type="checkbox"/>	IGSHARE : COVER : H1 : 1
463	<input type="checkbox"/>	IGSHARE : COVER : H2 : 1
464	<input type="checkbox"/>	IGSHARE : COVER : H3 : 1
465	<input type="checkbox"/>	IGSHARE : COVER : H4 : 1
466	<input type="checkbox"/>	IGSHARE : TOP : EEPROM : I
467	<input type="checkbox"/>	IGSHARE : TOP : EEPROM : I
468	<input type="checkbox"/>	IGSHARE : TOP : Magnetic_
469	<input type="checkbox"/>	IGSHARE : TOP : Magnetic_
470	<input type="checkbox"/>	IGSHARE : TOP : Magnetic_
471	<input type="checkbox"/>	IGSHARE : TOP : power : RE
472	<input type="checkbox"/>	IGSHARE : TOP : Ultra soun
473	<input type="checkbox"/>	IGSHARE : TOP : Ultra soun
474	<input type="checkbox"/>	IGSHARE : TOP : Wireless_
475	<input type="checkbox"/>	IGSHARE : TOP : Wireless_
476	<input type="checkbox"/>	IGSHARE : TOP : Wireless_
477	<input type="checkbox"/>	IGSHARE : TOP : Wireless_
478	<input type="checkbox"/>	IGSHARE : TOP : Wireless_
479	<input type="checkbox"/>	IGSHARE : TOP : Wireless_
Parts Schematic Nets Flat Nets Pins Title Blocks Globals Ports Aliases /		

최종 핀 수