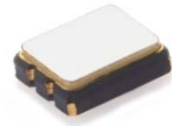


# LVDS TYPE

## SLO-32

- Ultra-compact with Dimensions of 3.2×2.5 mm
- 0.5 ps typical, RMS phase jitter(12 kHz to 20 MHz)
- 2.5 V, 3.3 V Supply Voltage
- LVDS Output
- Tri-state Function Available



### ELECTRICAL SPECIFICATIONS

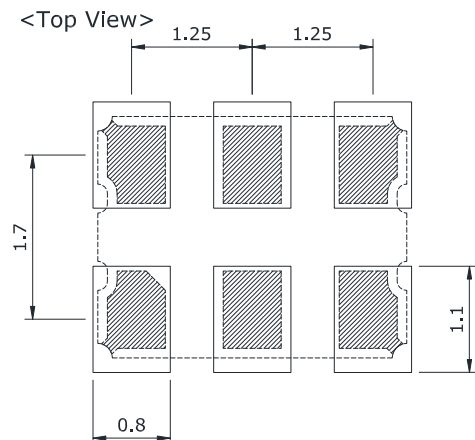
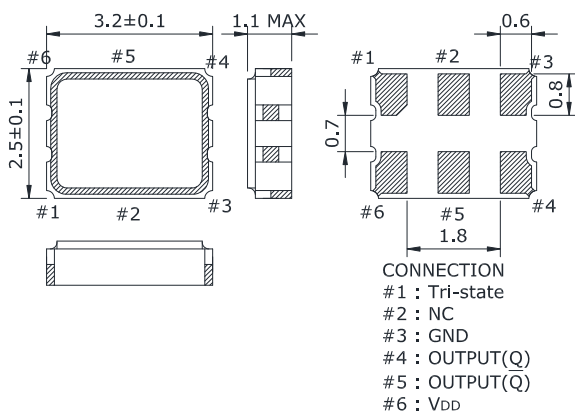
ITEM	Value	Remarks
Output Logic Type	LVDS	LVDS XO
Frequency Range	13.5MHz to 66.666 MHz	
Supply Voltage(V <sub>DD</sub> )	2.5 V <sub>DC</sub> ±5 %, 3.3 V <sub>DC</sub> ±5 %	
Operating Temperature Range	-20 to +70 °C, -40 to +85 °C	
Storage Temperature Range	-55 to +125 °C	
Frequency Stability	±25 ppm, ±50 ppm Max.	Over operating Temperature range
Input Current	45 mA Max. at 2.5 V, 65 mA Max. at 3.3 V	
Output Voltage Logic High(V <sub>OH</sub> )	1.43 V Typ., 1.60 V Max.	
Output Voltage Logic Low(V <sub>OL</sub> )	1.10 V Typ., 0.90 V Min.	
Differential Output Voltage(V <sub>OD</sub> )	247 mV Min., 330 mV Typ., 454 mV Max.	
Offset Voltage(V <sub>OS</sub> )	1.125 V Min., 1.250 V Typ., 1.375 V Max.	
Rise / Fall Time	0.3 ns Typ., 0.7 ns Max.	Measured over 20 % to 80 % of waveform
Duty Cycle	45 to 55 %, 40 to 60 %	Measured at 50 % of waveform
Start-up Time	10 ms Max.	
Output Load Condition(LVDS)	100 Ω	Between Output and Complementary Output
Output Enable Function (V <sub>IH</sub> and V <sub>IL</sub> )	70 % of V <sub>DD</sub> min. to Enable Output 30 % of V <sub>DD</sub> max. to Disable Output	High Impedance
RMS Phase Jitter	0.5 ps Typ., 1 ps Max.	BW : 12 kHz to 20 MHz
Frequency Aging	±5 ppm Max.	25 °C, First year

### MECHANICAL DIMENSIONS

(mm)

### LAND PATTERN

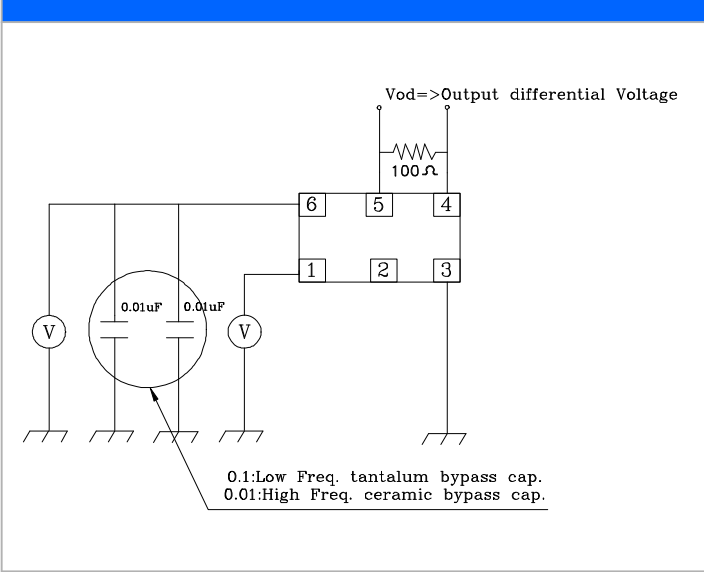
(mm)



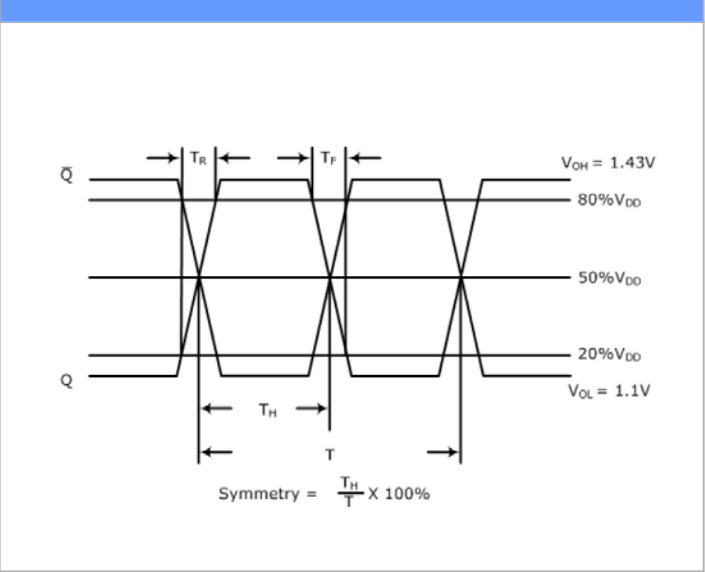
PART NUMBERING GUIDE

SLO – 32 33 50 A D S R – 50.000M									
SUPPLY VOLTAGE(V <sub>DD</sub> )		33 : 3.3 V, 25 : 2.5 V						FREQUENCY	
								M : MHz	
FREQUENCY STABILITY		25 : ±25 ppm, 50 : ±50 ppm						PACKAGE OPTION	
		BLANK : ±100 ppm						R : TAPE AND REEL	
								BLANK : BULK	
OPERATING TEMPERATURE RANGE		A : -40 to 85 °C, B : -20 to 70 °C						PIN 1 CONNECTION	
		BLANK : 0 to 70 °C						S : TRI-STATE, E/D	
								BLANK : NO CONNECTION	
DUTY CYCLE		D : 45/55, E : 40/60							

TEST CIRCUIT (LVDS)



WAVEFORM (LVDS)

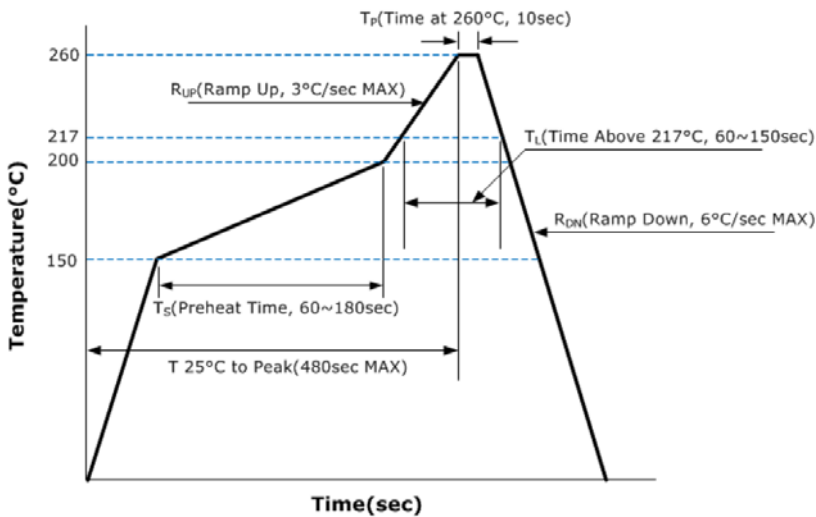


ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Solderability	MIL-STD-883, Method 2003

REFLOW PROFILE

MARKING GUIDE



Frequency in MHz

LINE 1 : XXX.XXX

LINE 2 : ● SL Y WW

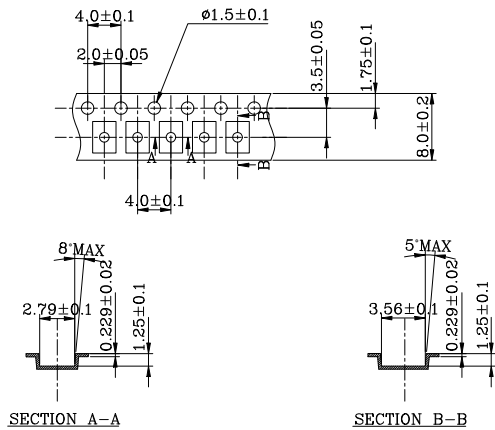
Model Name

Year

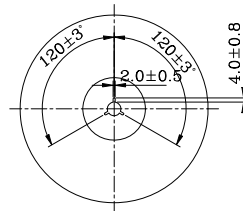
Week

TAPE AND REEL DIMENSIONS

MAT'L : P.S  
COLOR : BLACK  
REFERENCE R=0.2



MAT'L : P.S  
COLOR : BLACK



NOTE  
1.COVER TAPE : 5.4mm(WIDTH)X0.06mm(t) MAT'L : PET  
2.COLOR : WHITE

