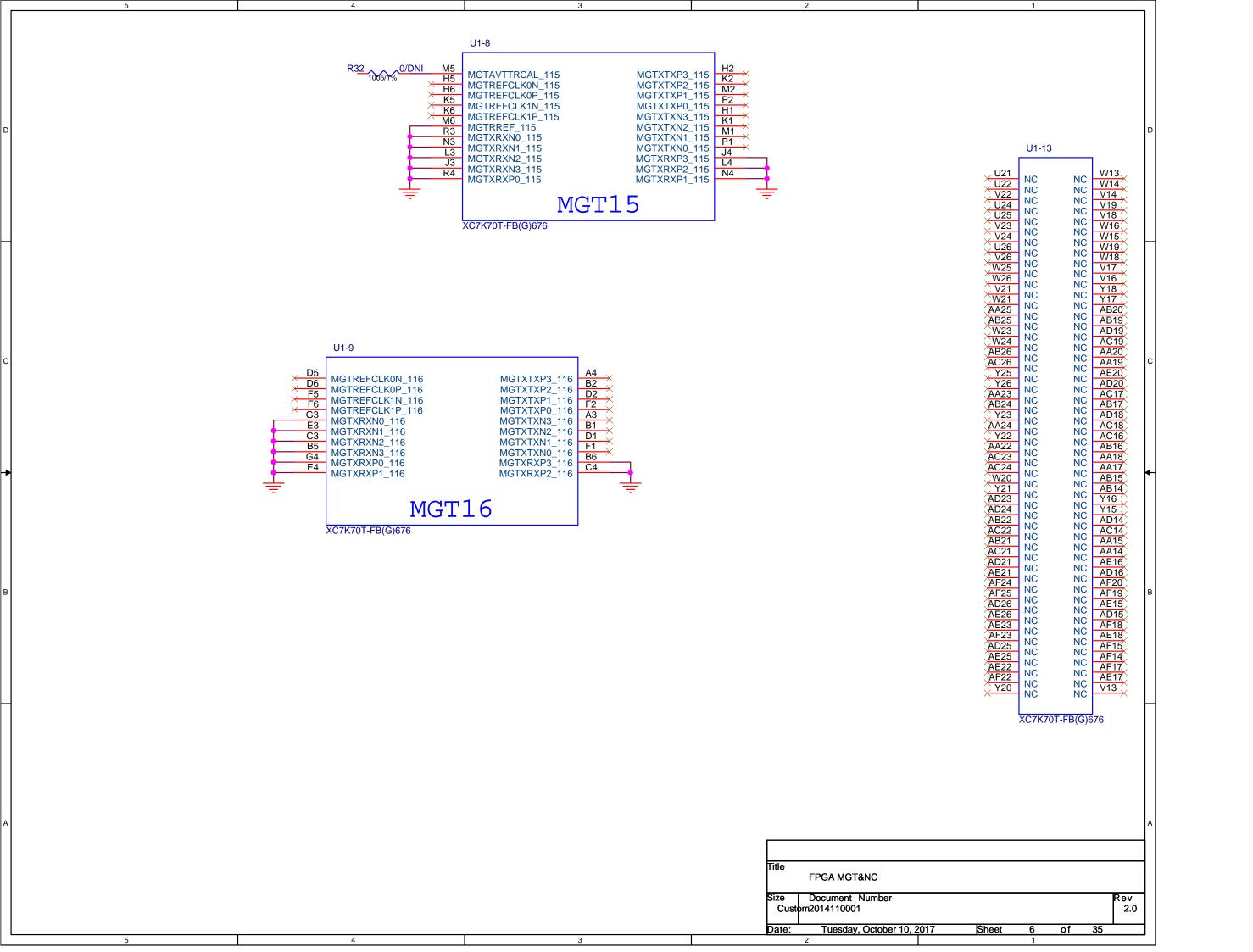


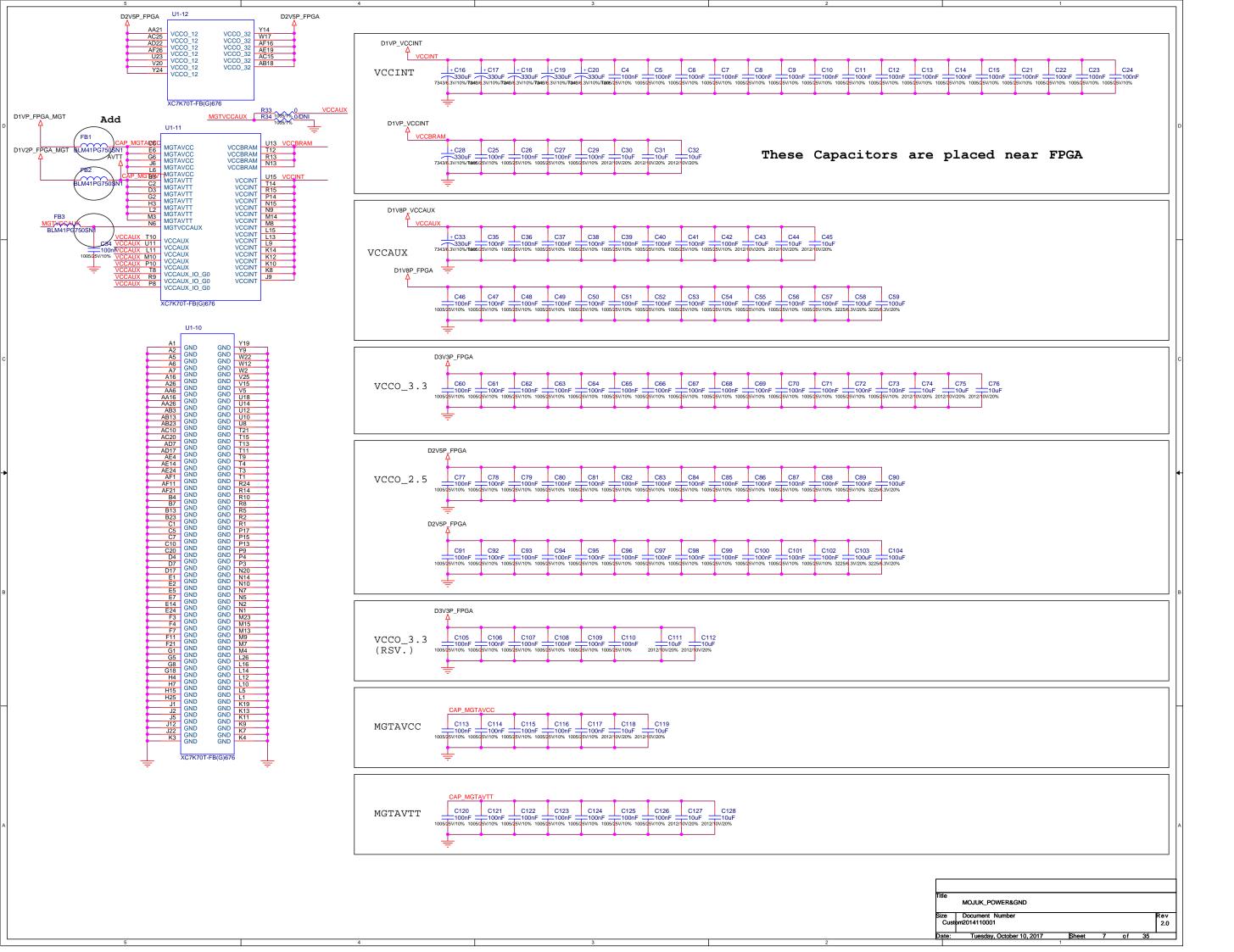
SPI_SSN SPI_CLOCK SPI_MOSI D3V3P_FPGA D3V3P_FPGA D2V5P_FPGA L1 C1 =10pF 1005/50V/10% BLM21PG221SN1 R18 330 D3V3P_FPGA V11-1 1.25V C3 =10nF 1005/50V/10% C2 10nF= 1005/50V/10% R20 330 1005/1% D3V3P_FPGA D3V3P_FPGA D3V3P_FPGA BOX-HEADER-14P,2mm/SMD BANK 0

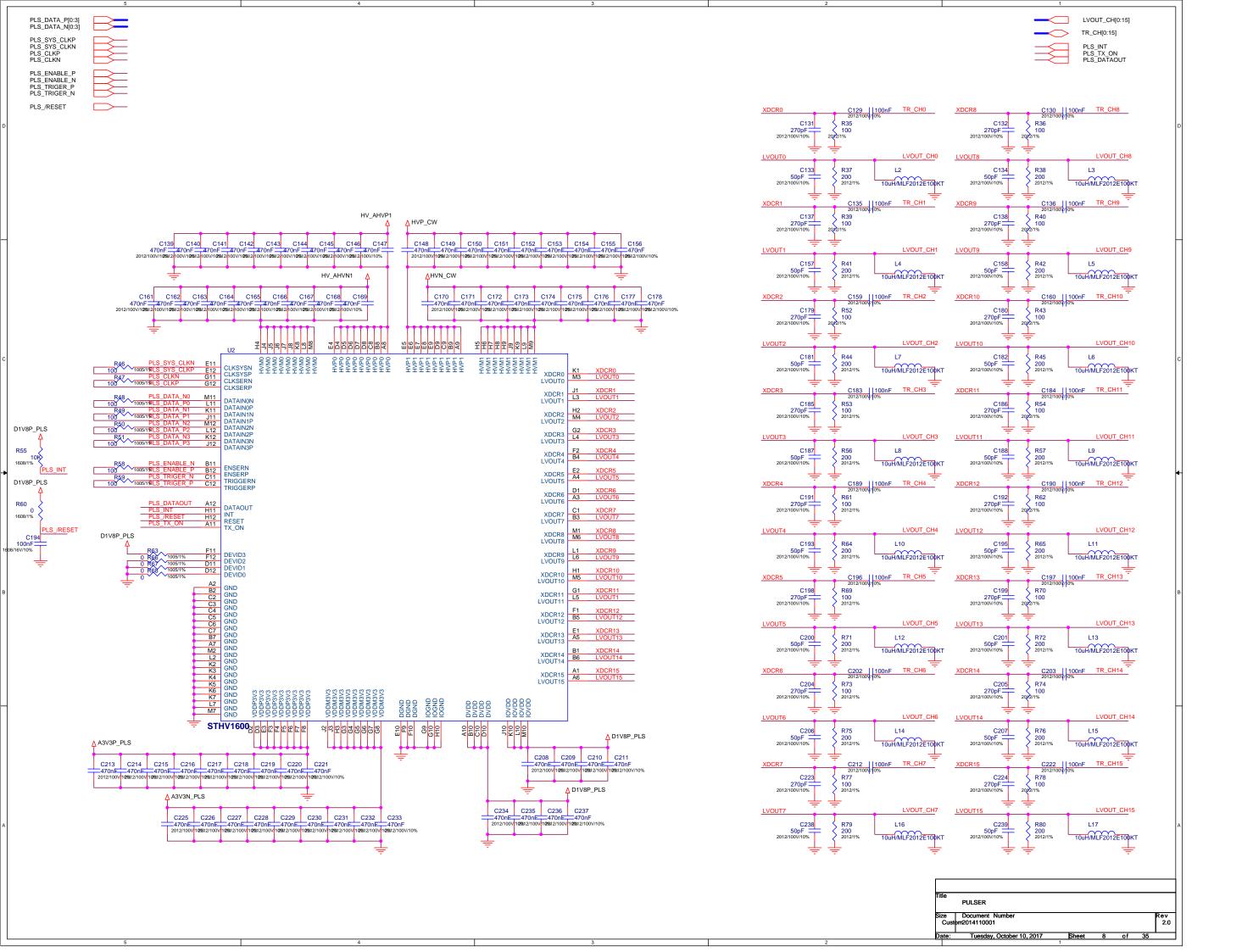
xc7K70T-FB(G)676 Slave serial

M[2:0] = 1 1 1 (Default setting --> internal pull up resistors)

FPGA_CFG_CLK은via hole 이 아닌 point to point로연결

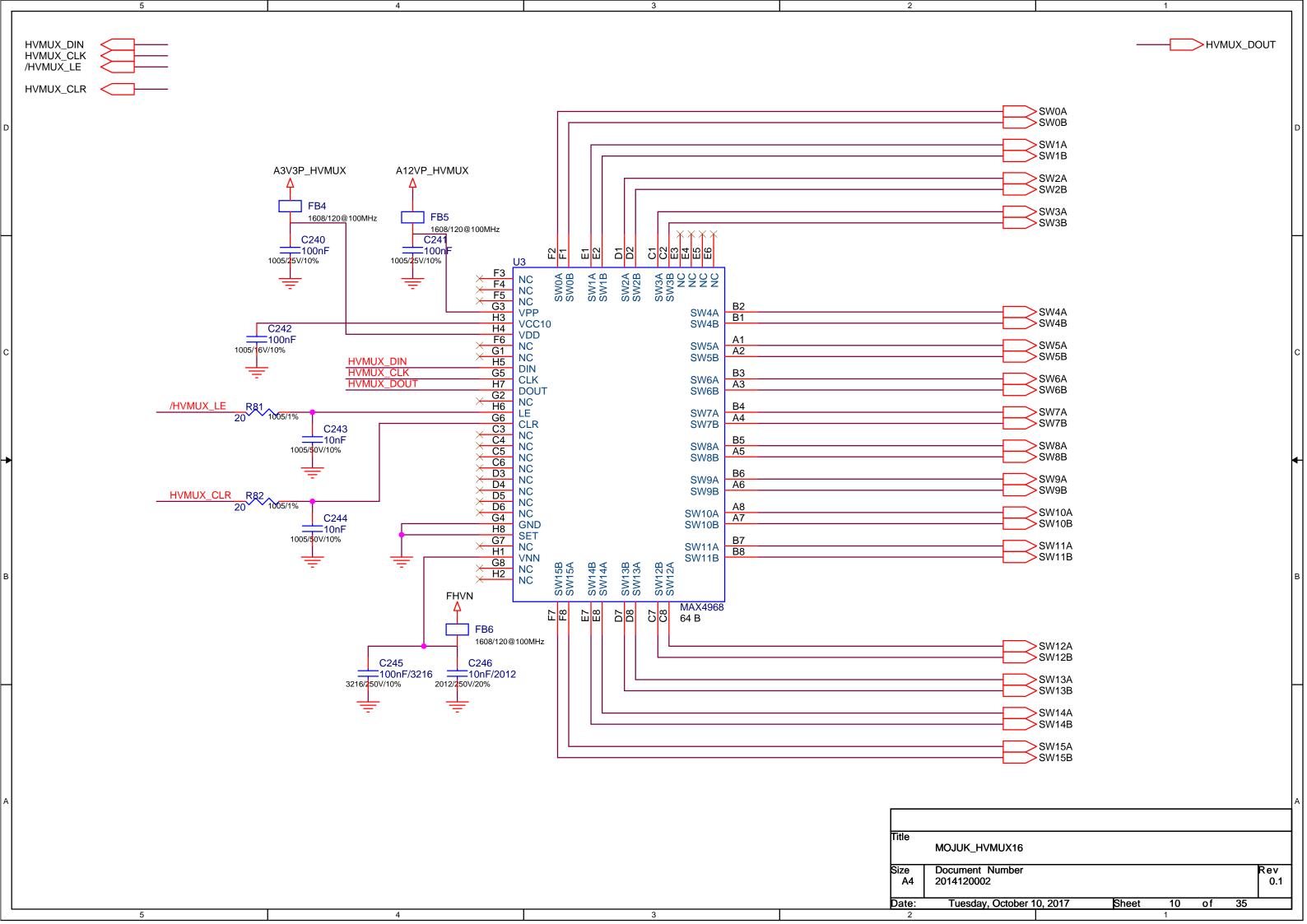


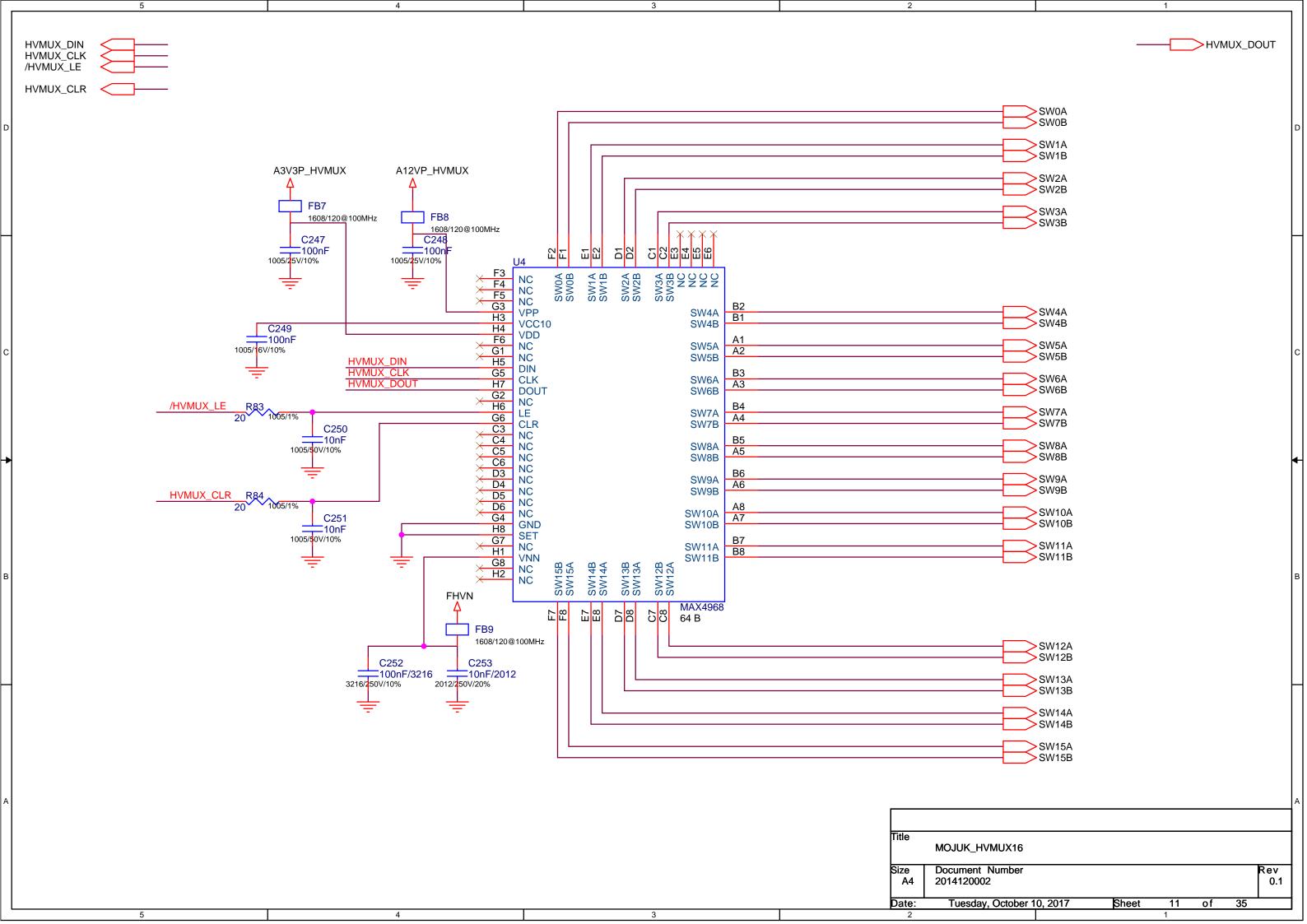


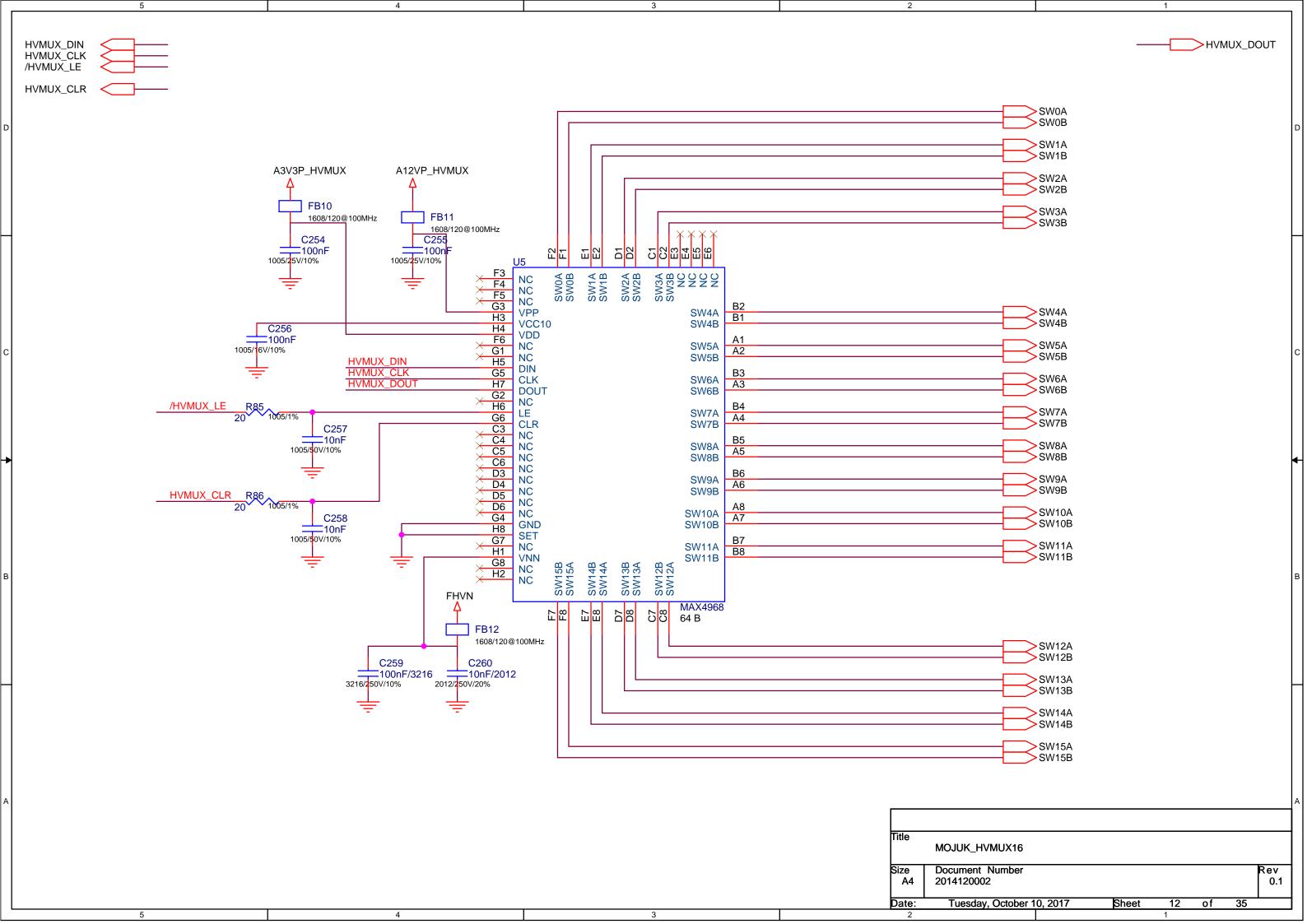


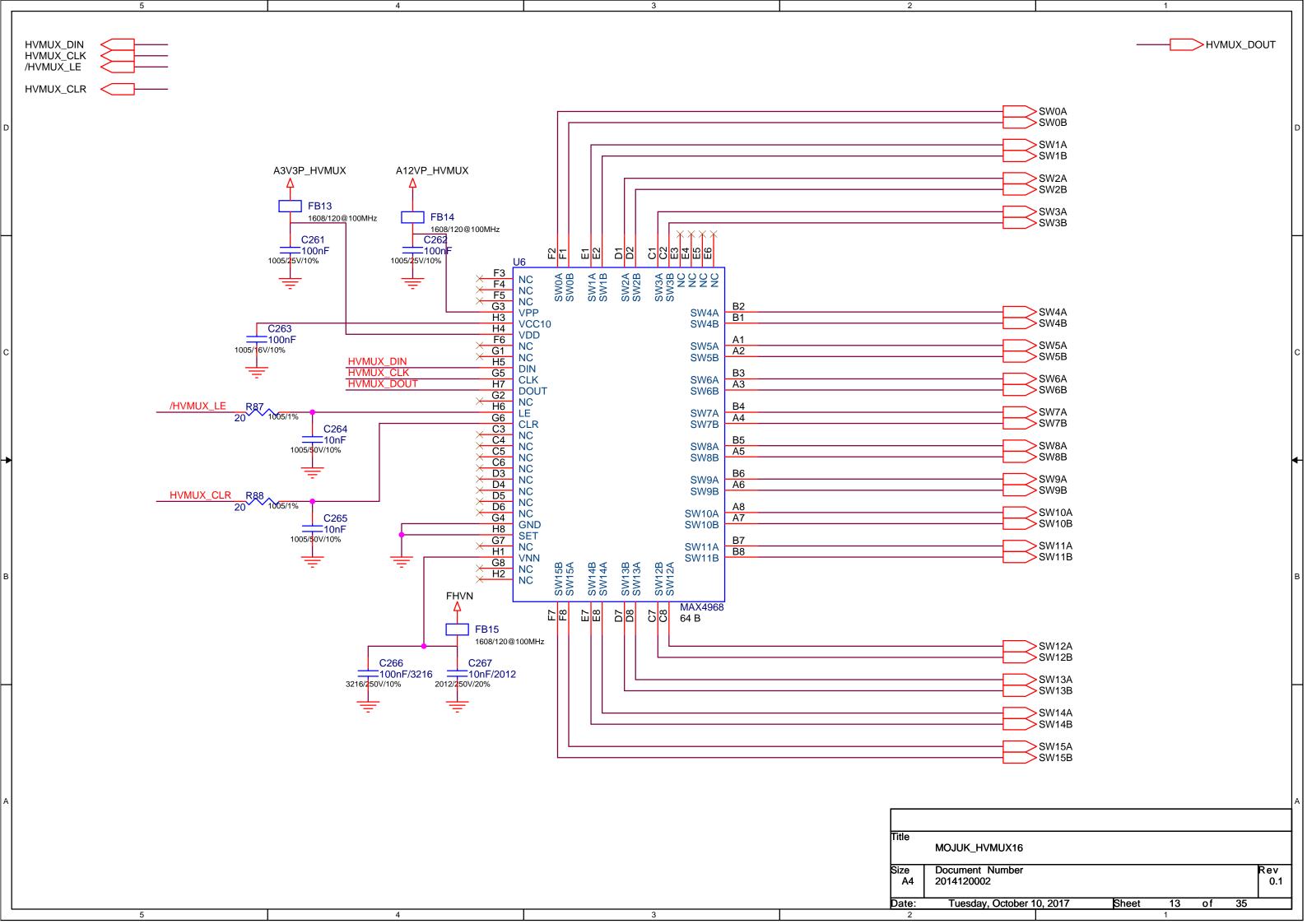
PRB_EL_D[0:63] TR_CH_D[0:15] HVMUX_DOUT HVMUX_DIN HVMUX_EN HVMUX_CLK HVMUX01 HVMUX02 HVMUX03 HVMUX04 TR_CH_D0 PRB_EL_D0 TR_CH_D4 PRB_EL_D4 TR_CH_D8 PRB_EL_D8 TR_CH_D12 PRB_EL_D12 SW0A SW0B SW0A SW0B SW0B SW0B SW1A SW1A SW1A PRB_EL_D16 PRB_EL_D20 PRB_EL_D28 PRB_EL_D24 PRB_EL_D32 PRB_EL_D36 PRB_EL_D40 PRB_EL_D44 SW2B SW2B SW3A SW3B SW3A SW3B SW3A SW3B SW3A SW3B PRB_EL_D48 PRB_EL_D52 PRB_EL_D56 PRB_EL_D60 TR_CH_D1 PRB_EL_D1 TR_CH_D5 PRB_EL_D5 TR_CH_D13 PRB_EL_D13 SW4A SW4B SW4A SW4B SW4A PRB_EL_D9 SW4B SW4B SW5A SW5B SW5A SW5B PRB_EL_D21 PRB_EL_D25 PRB_EL_D29 SW5B SW5B SW6A SW6B SW6A SW6B PRB_EL_D33 PRB_EL_D37 PRB_EL_D41 PRB_EL_D45 SW6B SW6B SW7A SW7A SW7A SW7A PRB_EL_D49 PRB EL D53 PRB EL D61 PRB_EL_D57 SW7B SW7B SW7B TR_CH_D2 PRB_EL_D2 TR_CH_D6 PRB_EL_D6 TR_CH_D10 PRB_EL_D10 TR_CH_D14 PRB_EL_D14 SW8A SW8A SW8B PRB_EL_D18 PRB_EL_D22 PRB_EL_D26 PRB_EL_D30 SW9B SW10A SW10B SW10A SW10B SW10A SW10B PRB_EL_D34 PRB_EL_D38 PRB_EL_D46 PRB_EL_D42 SW10B PRB_EL_D50 PRB_EL_D54 PRB_EL_D58 PRB_EL_D62 TR_CH_D15 PRB_EL_D15 TR_CH_D11 TR_CH_D3 TR_CH_D7 SW12B SW12B SW12B SW12B SW13A SW13B SW13A SW13B PRB_EL_D23 SW13A SW13B PRB_EL_D19 PRB_EL_D31 PRB_EL_D27 SW13B SW14A SW14B PRB_EL_D35 SW14A SW14A PRB_EL_D39 PRB_EL_D43 PRB_EL_D47 SW15A SW15B SW15A SW15B PRB_EL_D51 PRB_EL_D55 PRB_EL_D59 PRB_EL_D63 SW15B SW15B HVMUX_DOUT HVMUX_DOUT HVMUX_DOUT HVMUX_DOUT HVMUX_DOUT VMUX_CLR 유 의 의 의 /HVMUX_LE HVMUX_CLK HVMUX_DIN 유 의 백호름 딩 HVMUX 31 HVMUX_16 31 HVMUX_16 31 HVMUX_16 31 HVMUX_16 **‡** HVMUX_EN HVMUX_CLK HVMUX_EN HVMUX_CLK HVMUX_DIN HVMUX_EN HVMUX_CLK HVMUX_EN HVMUX_CLK MOJUK_HVMUX Document Number Rev 0.1

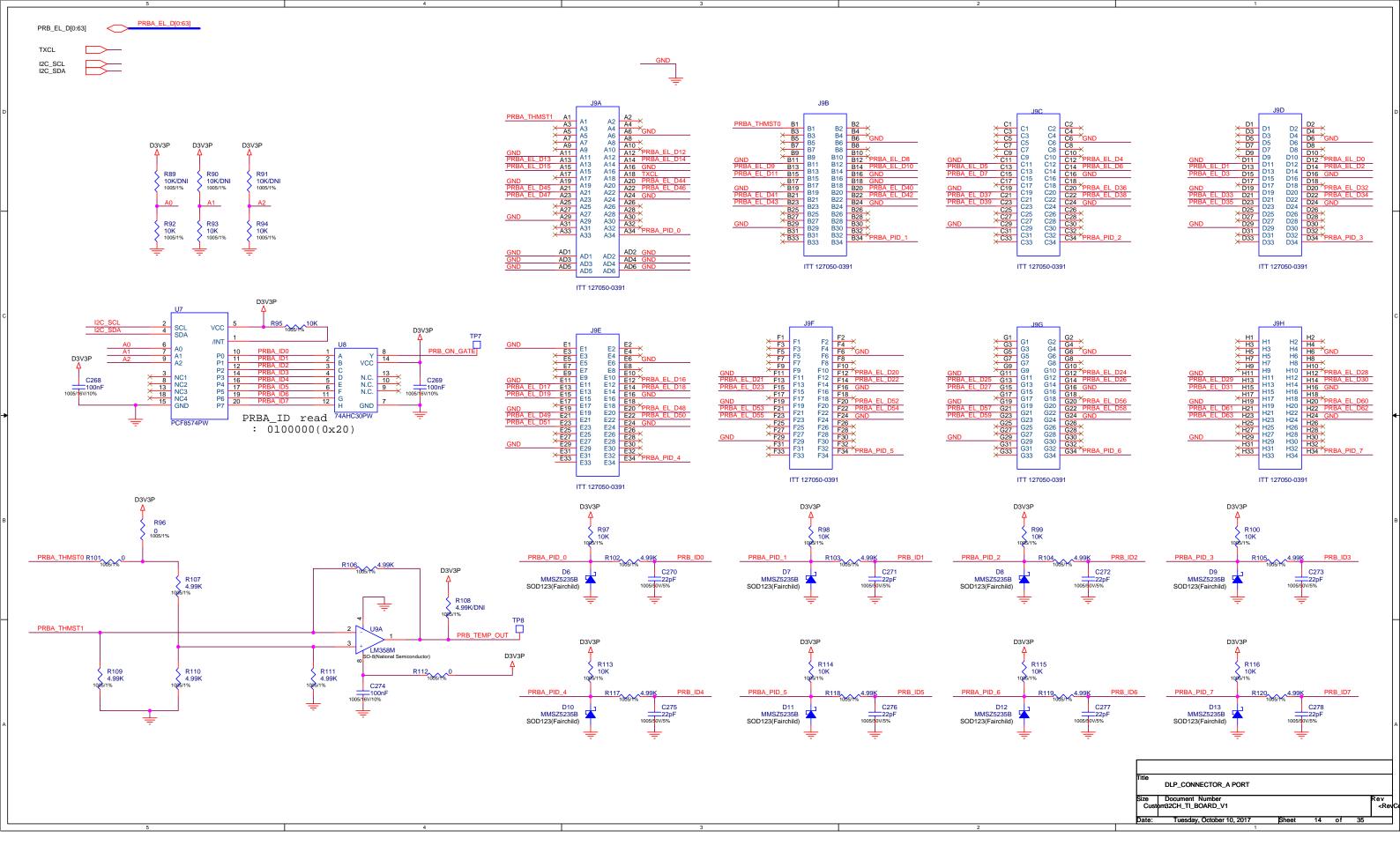
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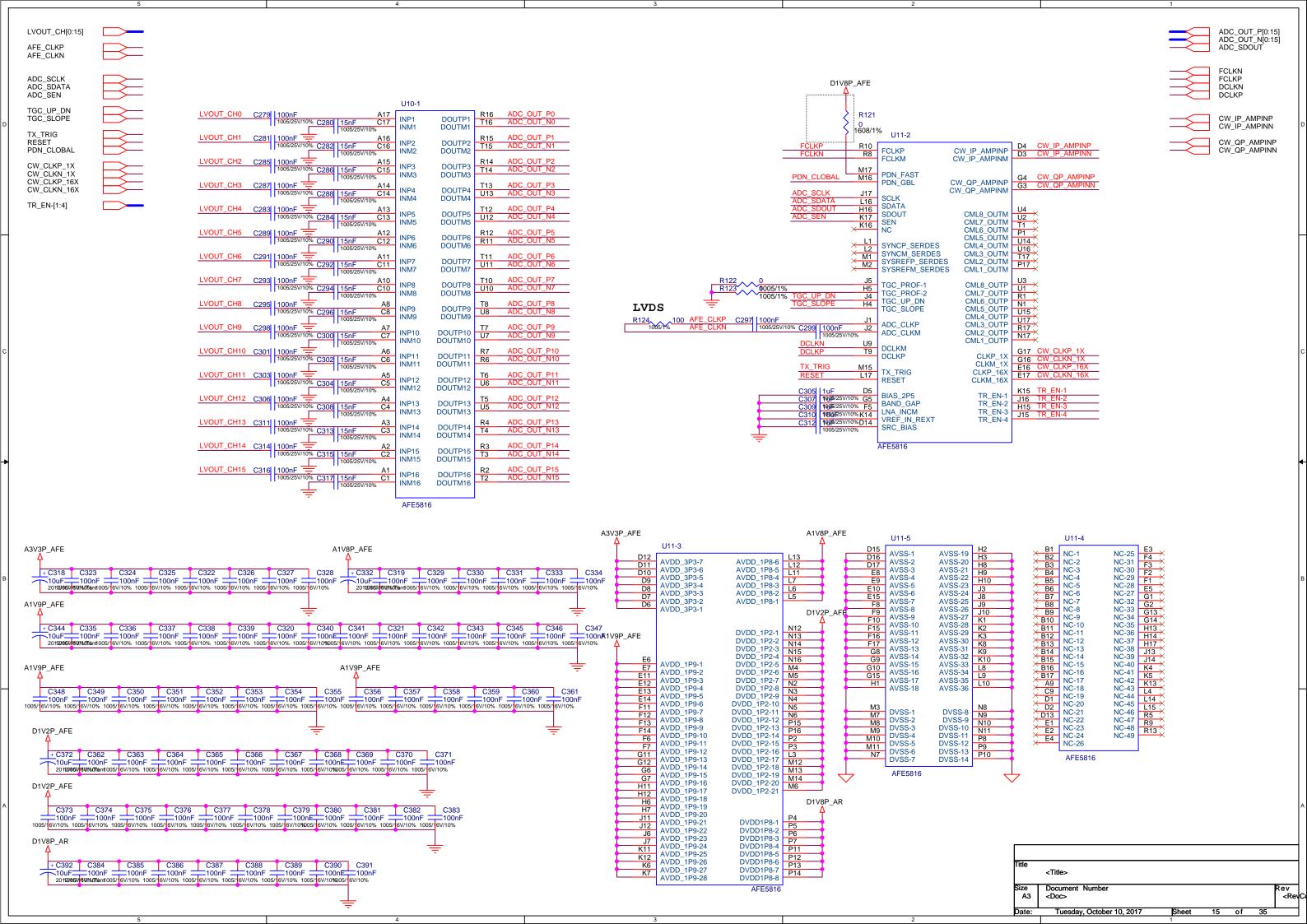


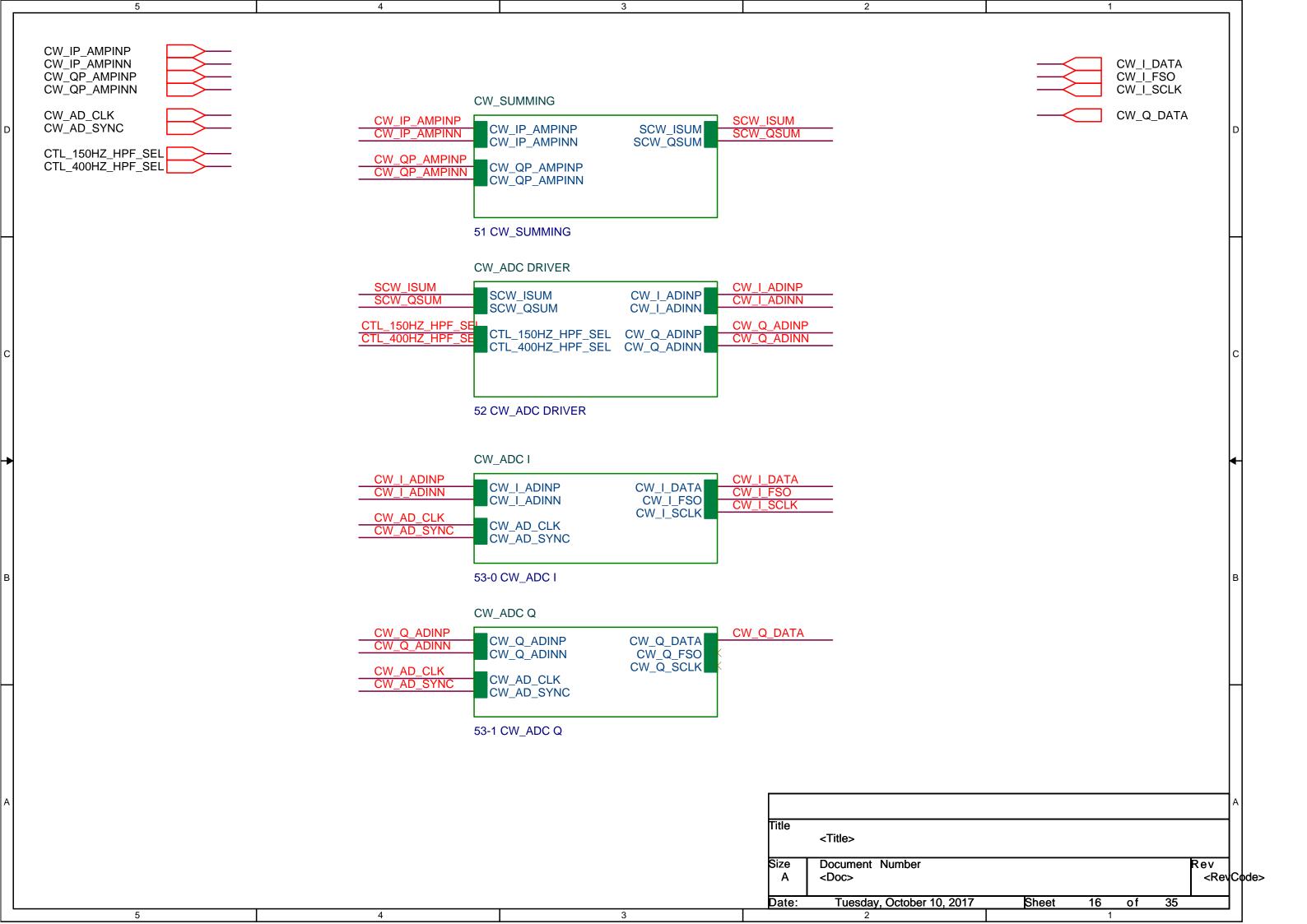


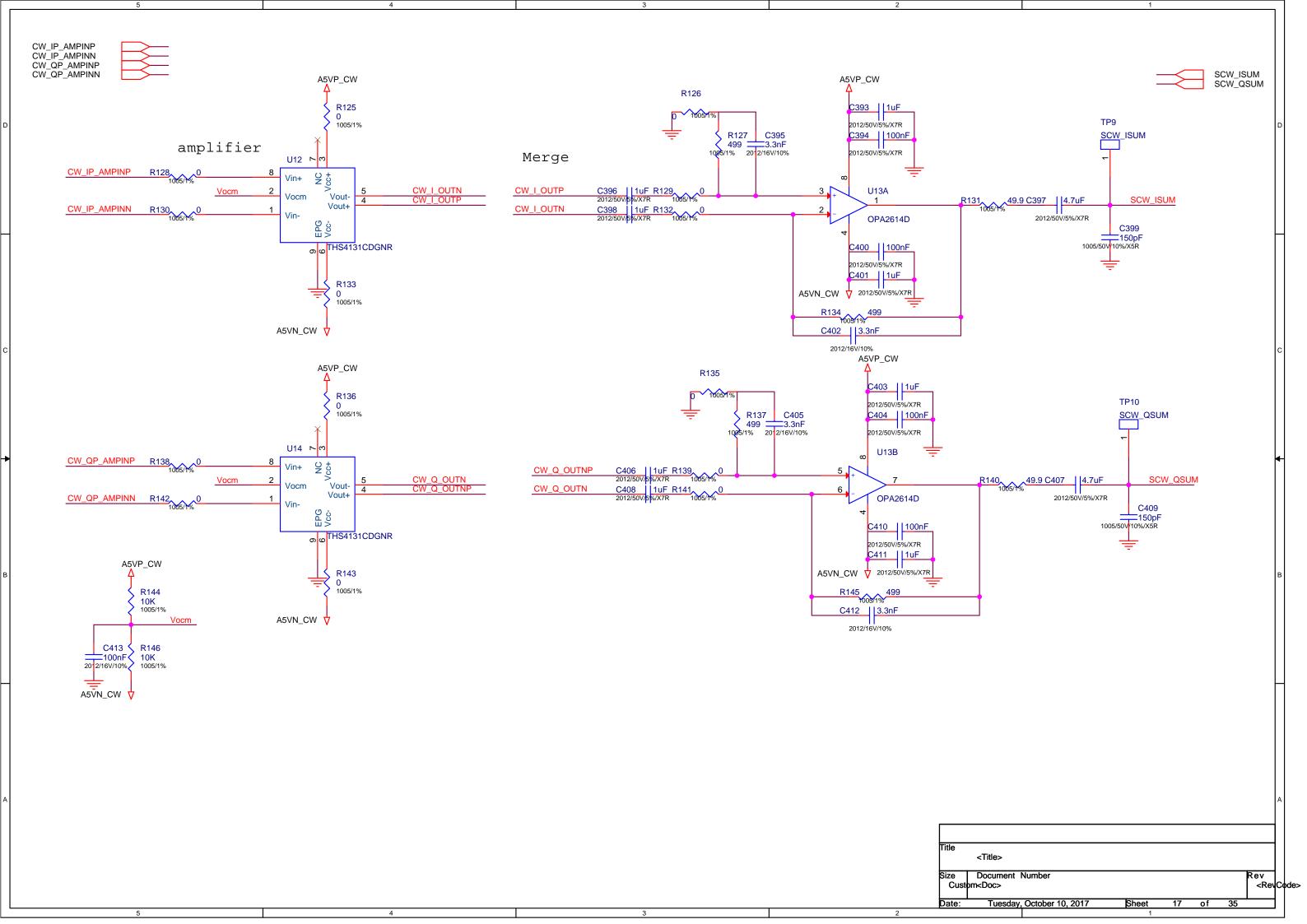


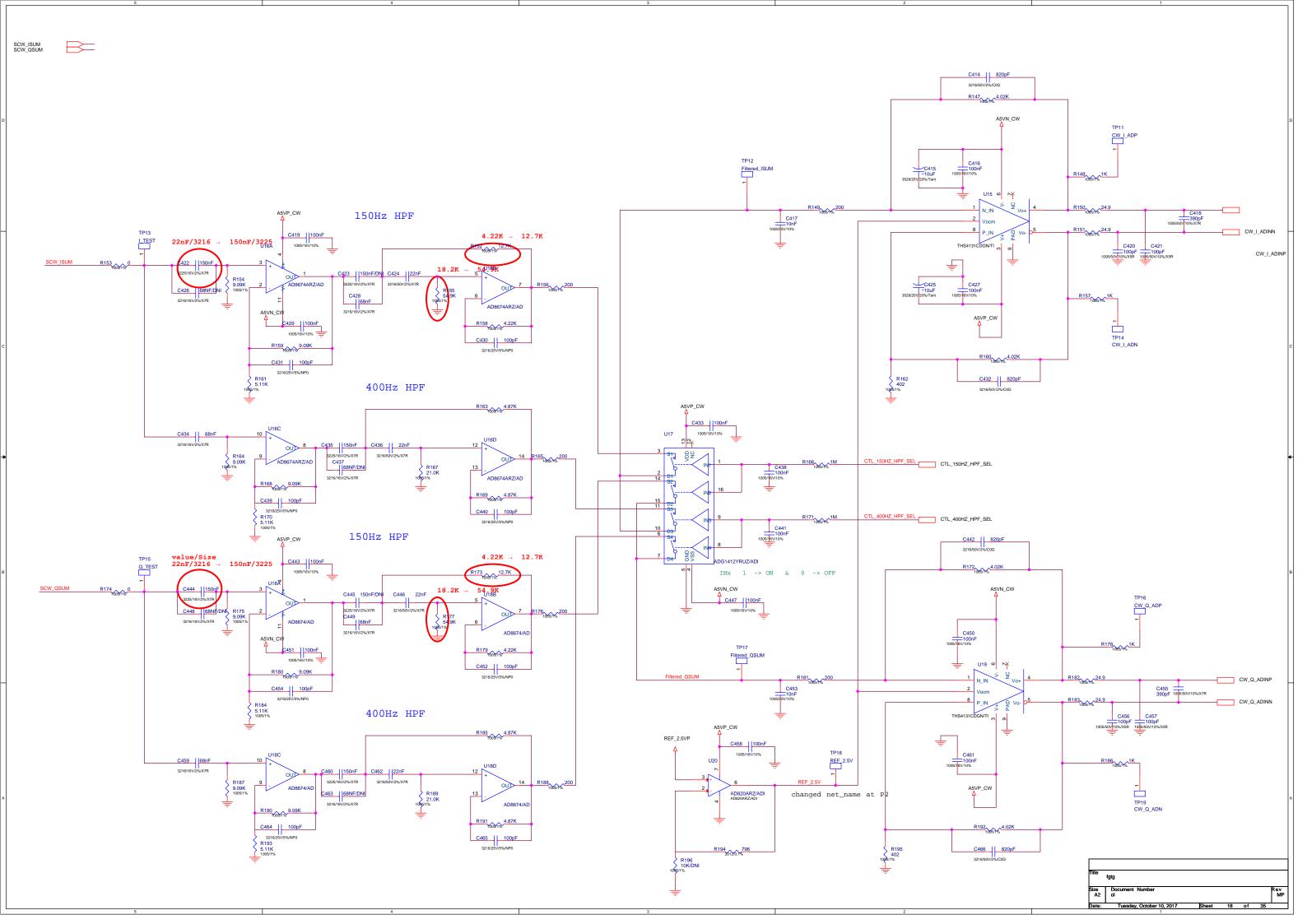


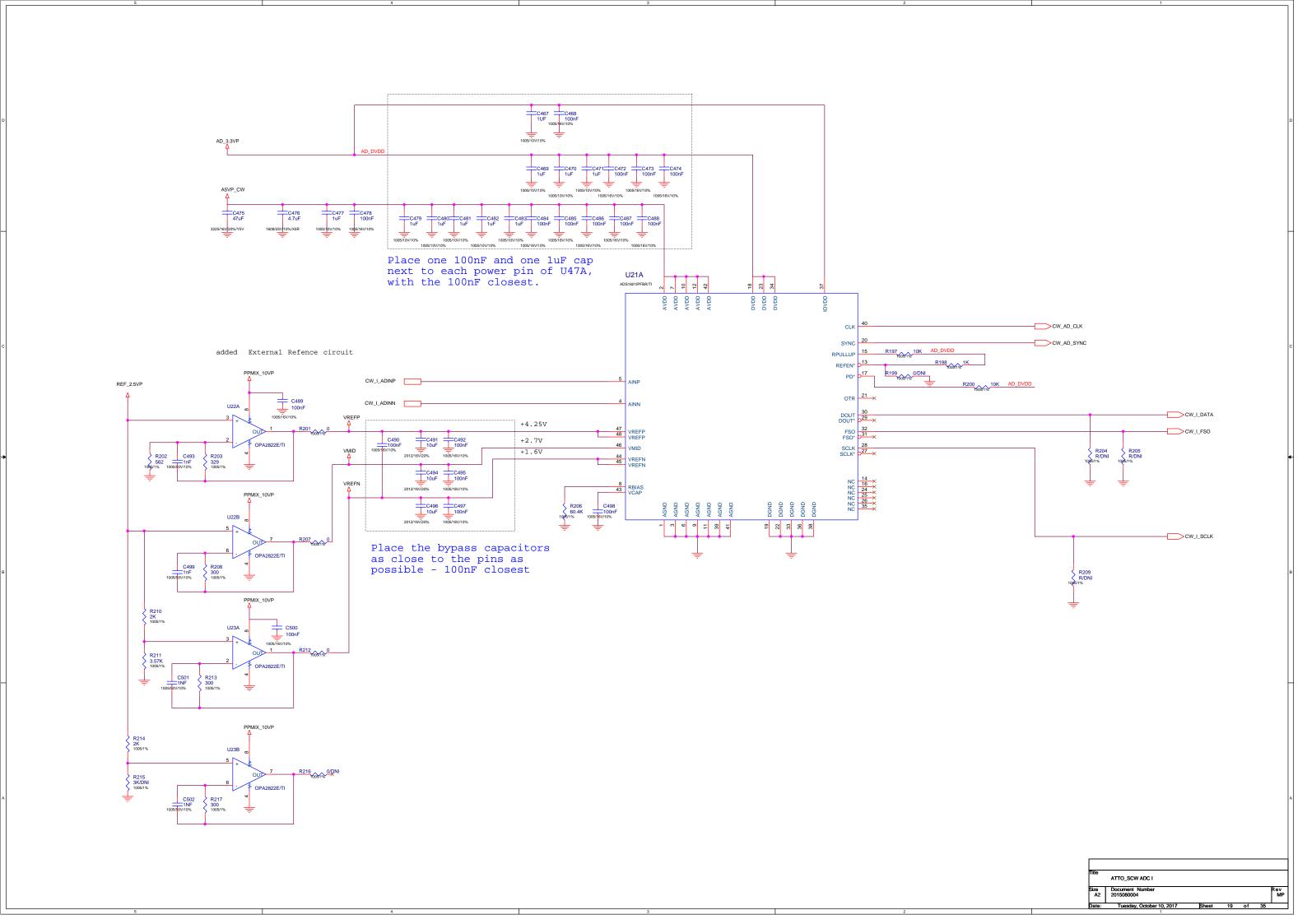


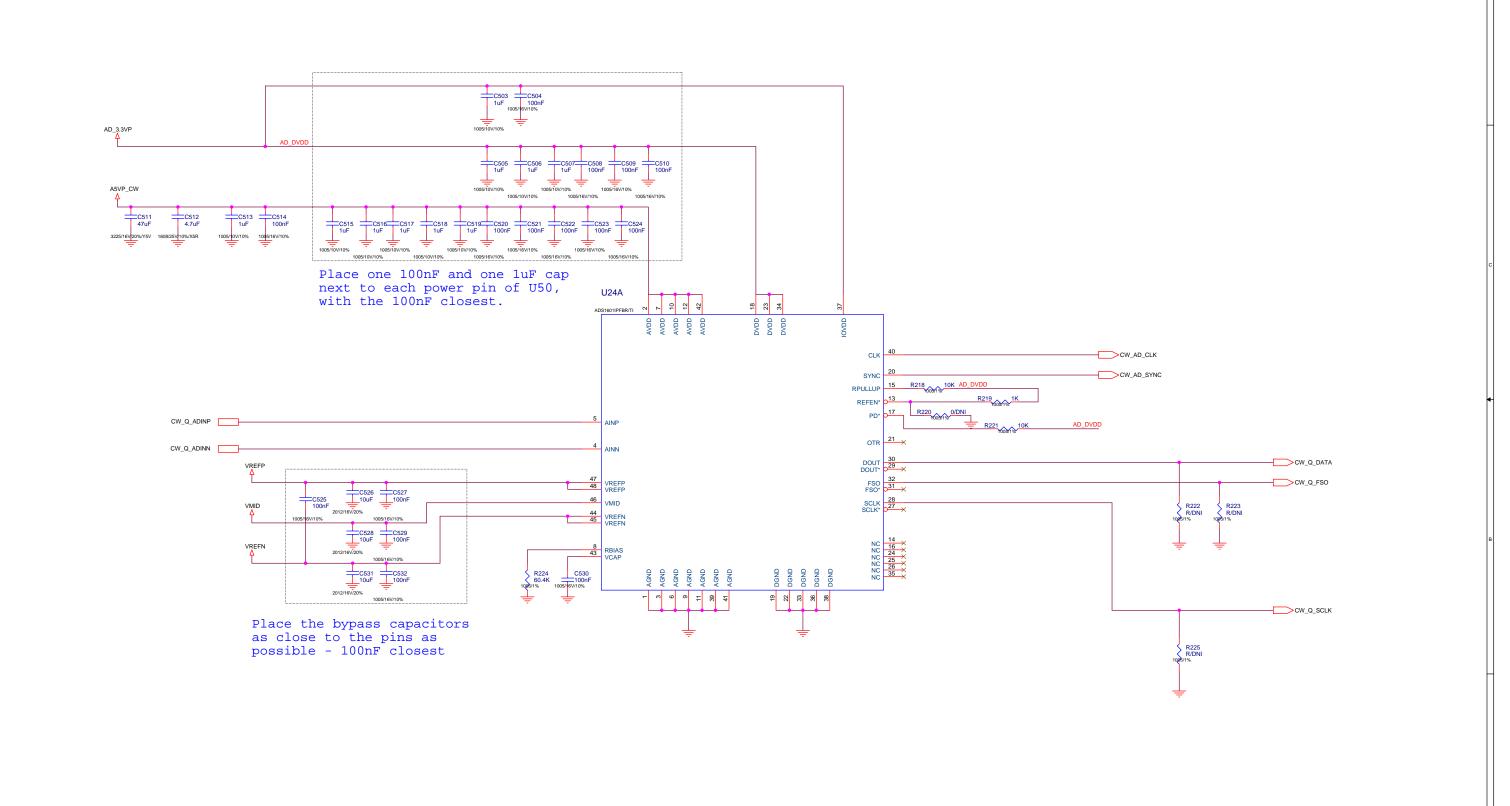


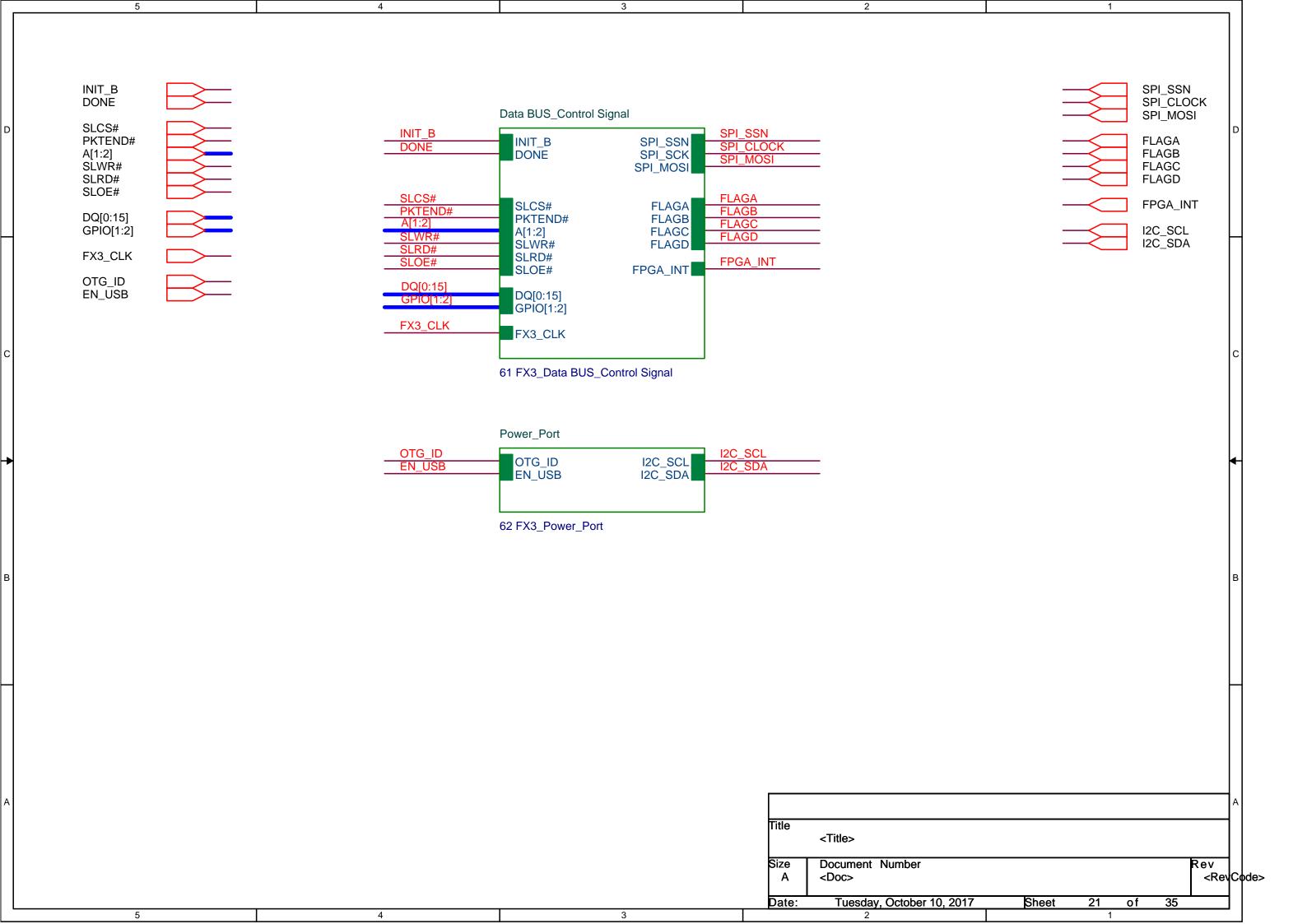


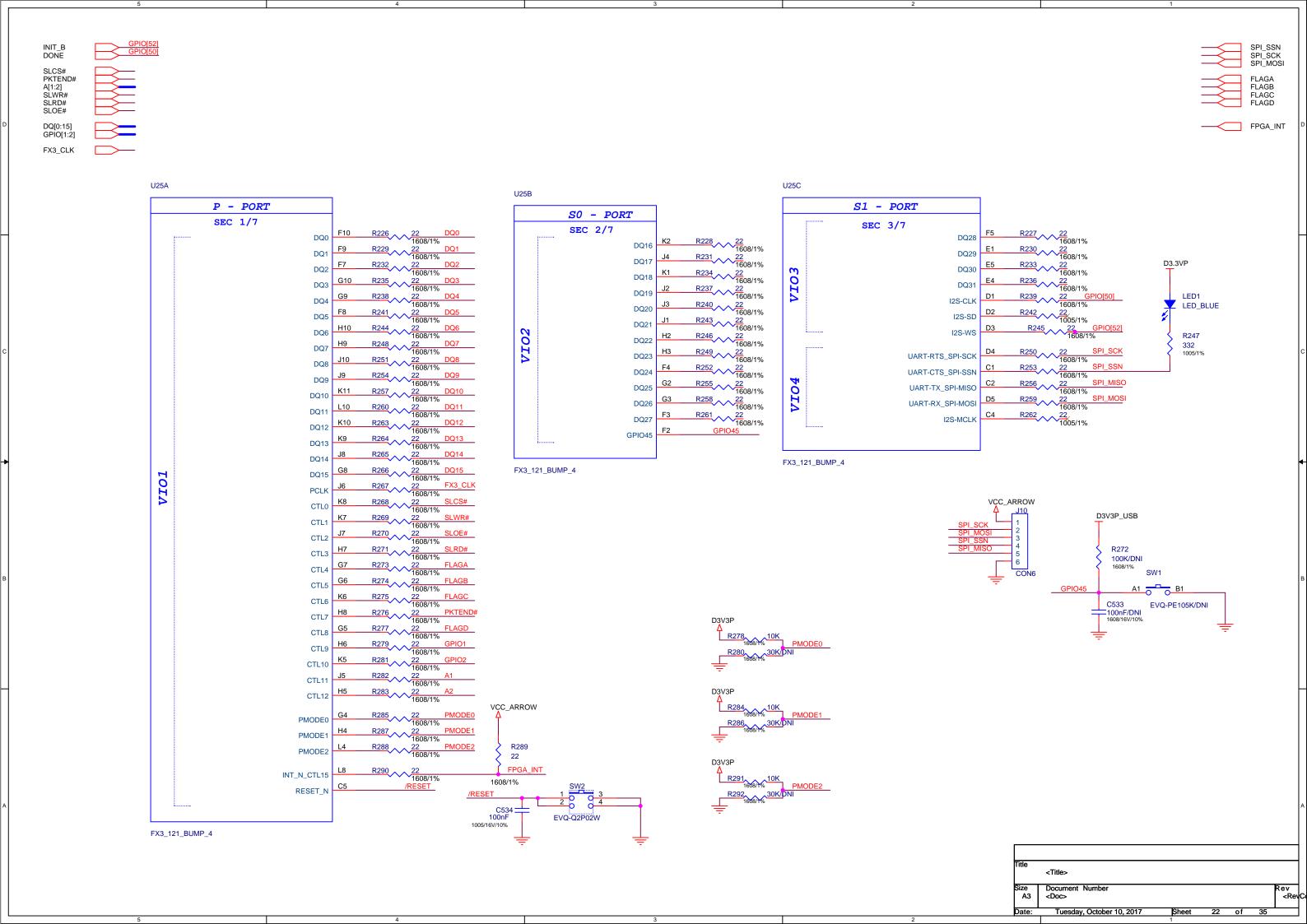


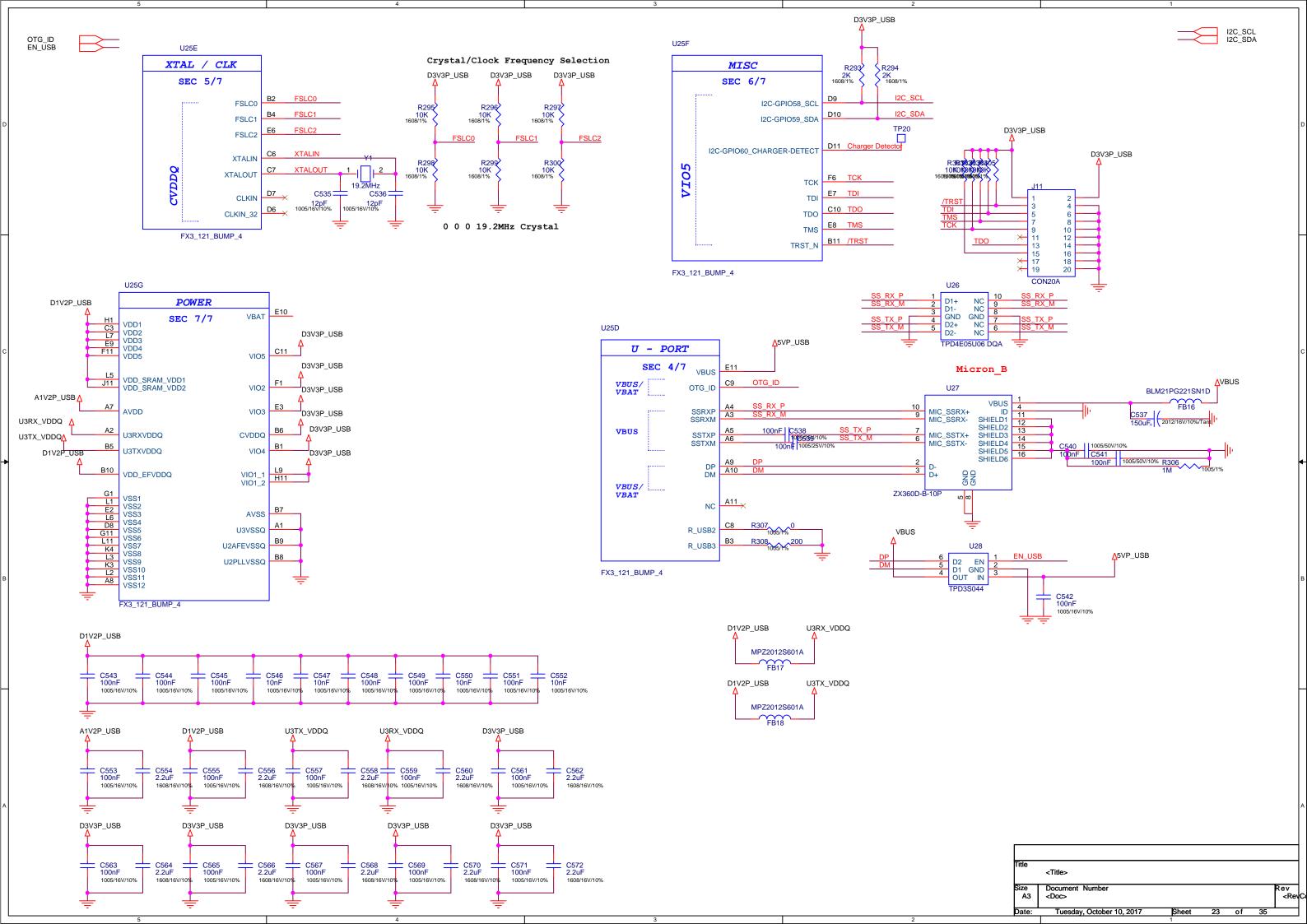


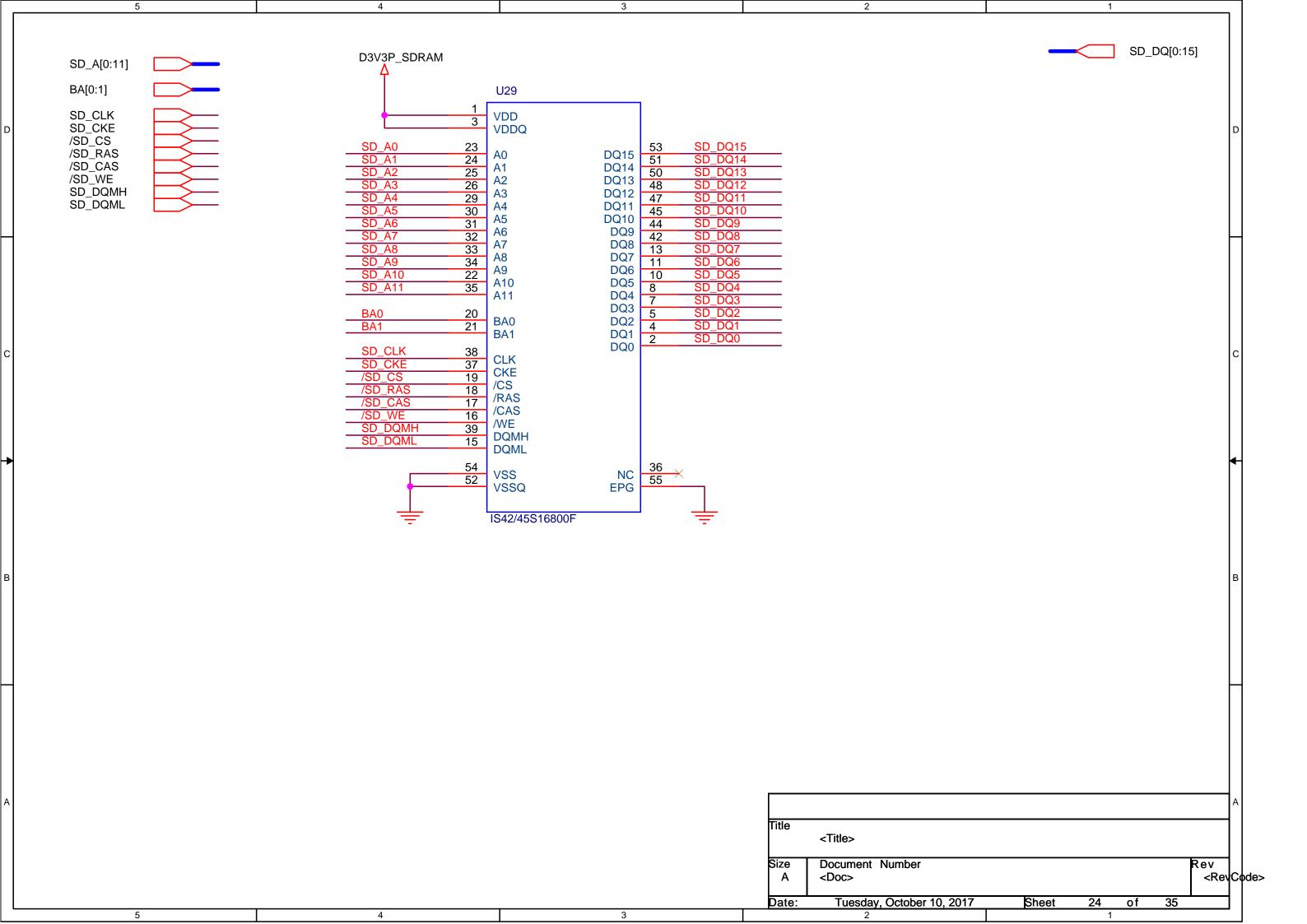


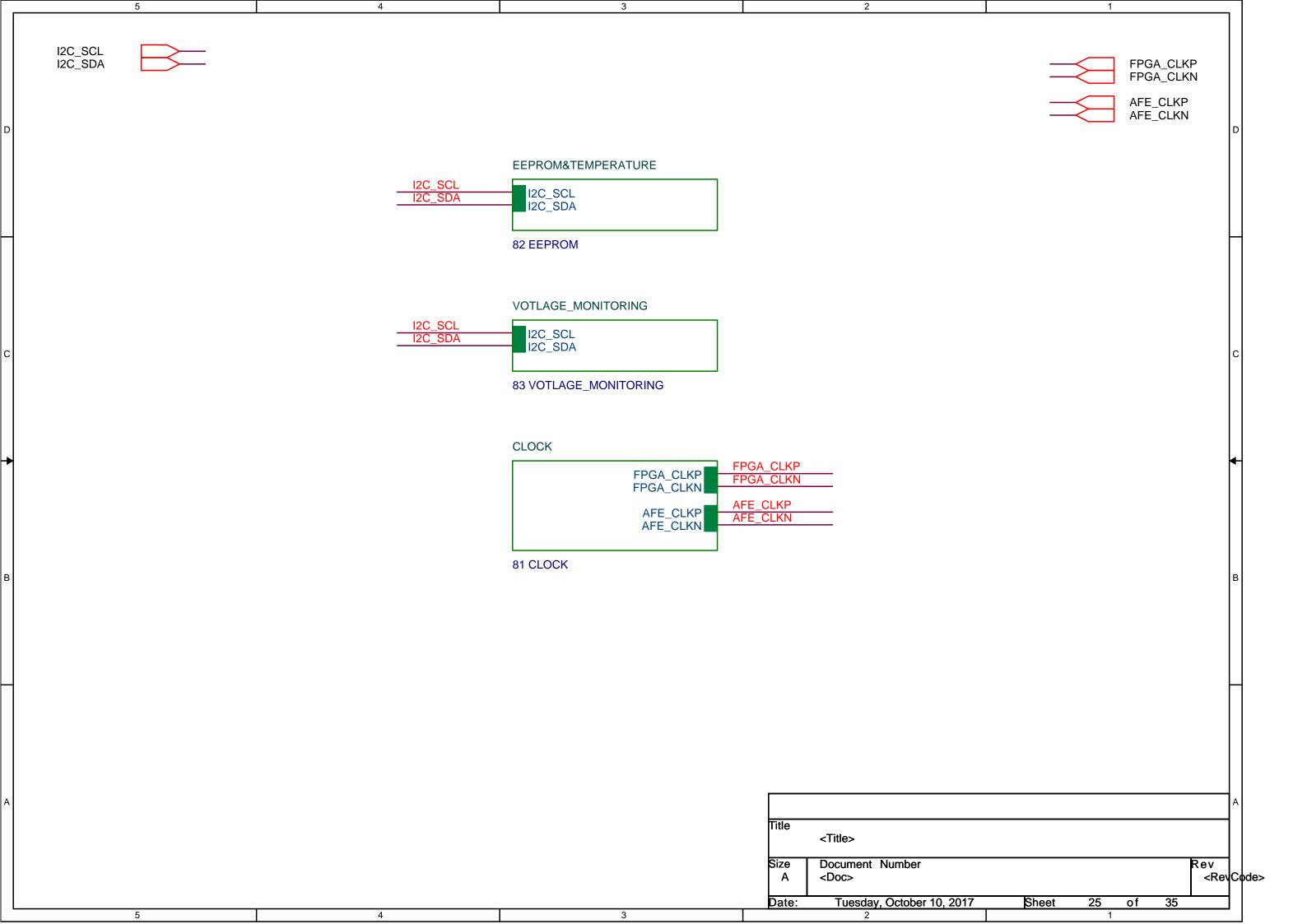


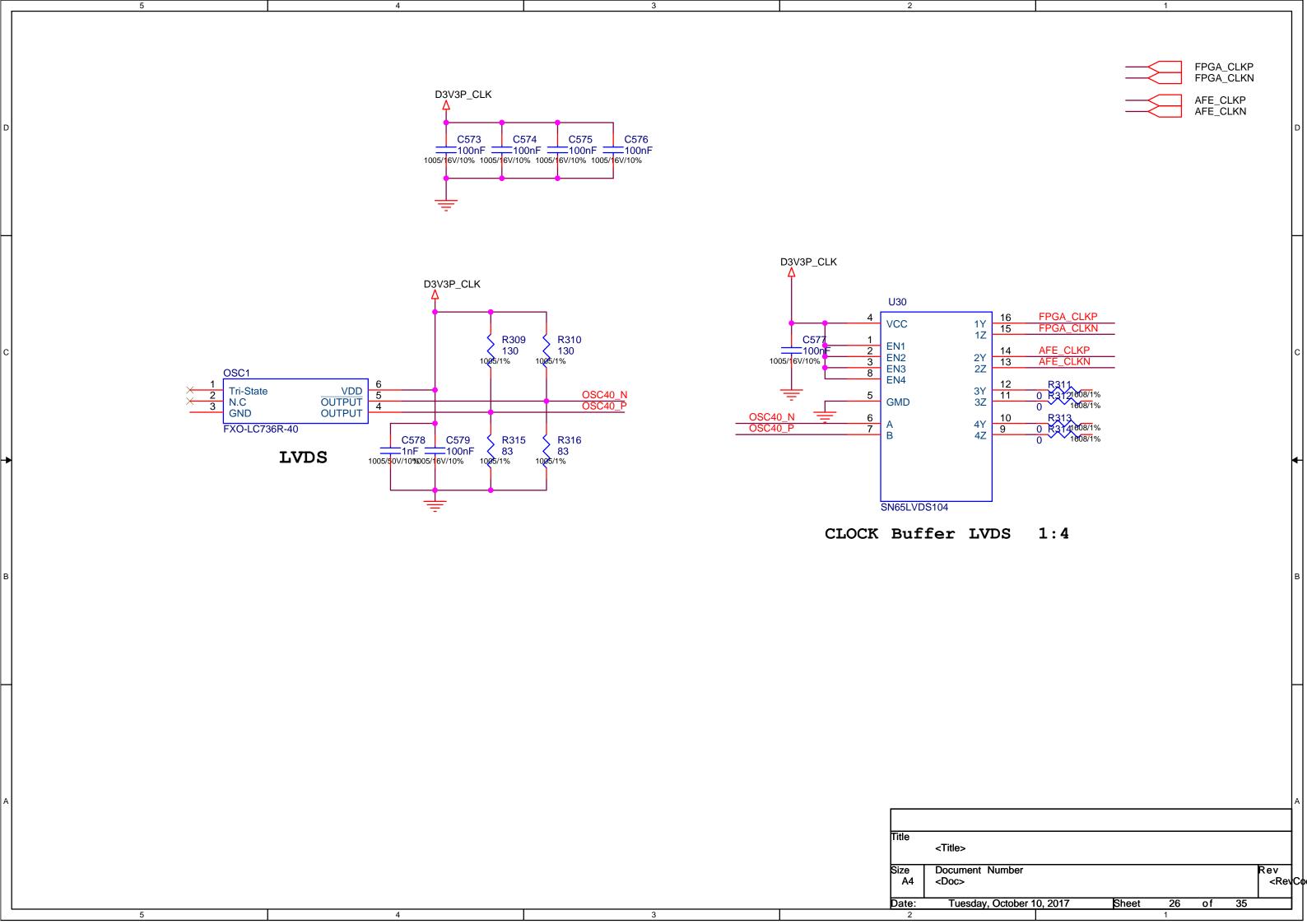


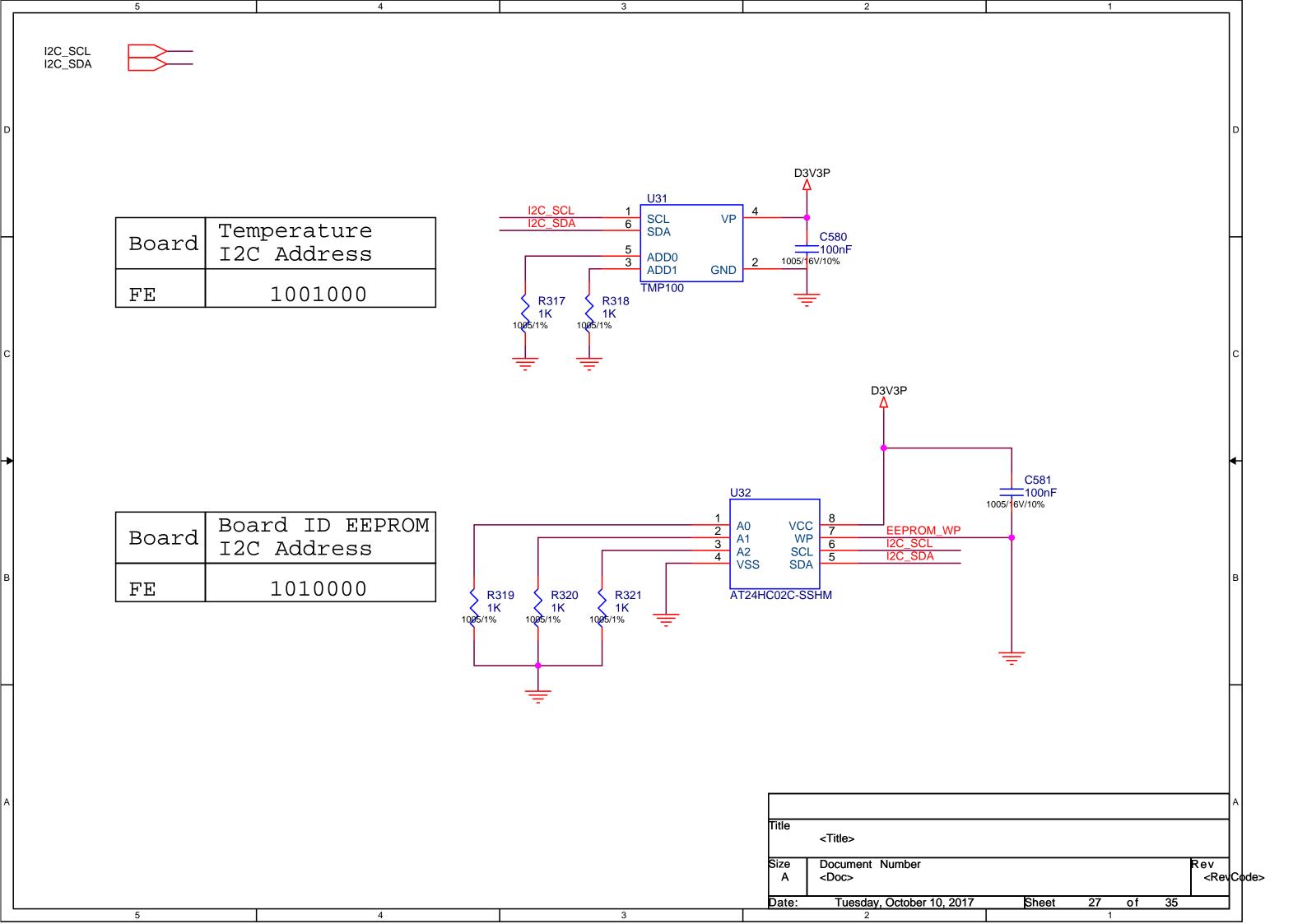


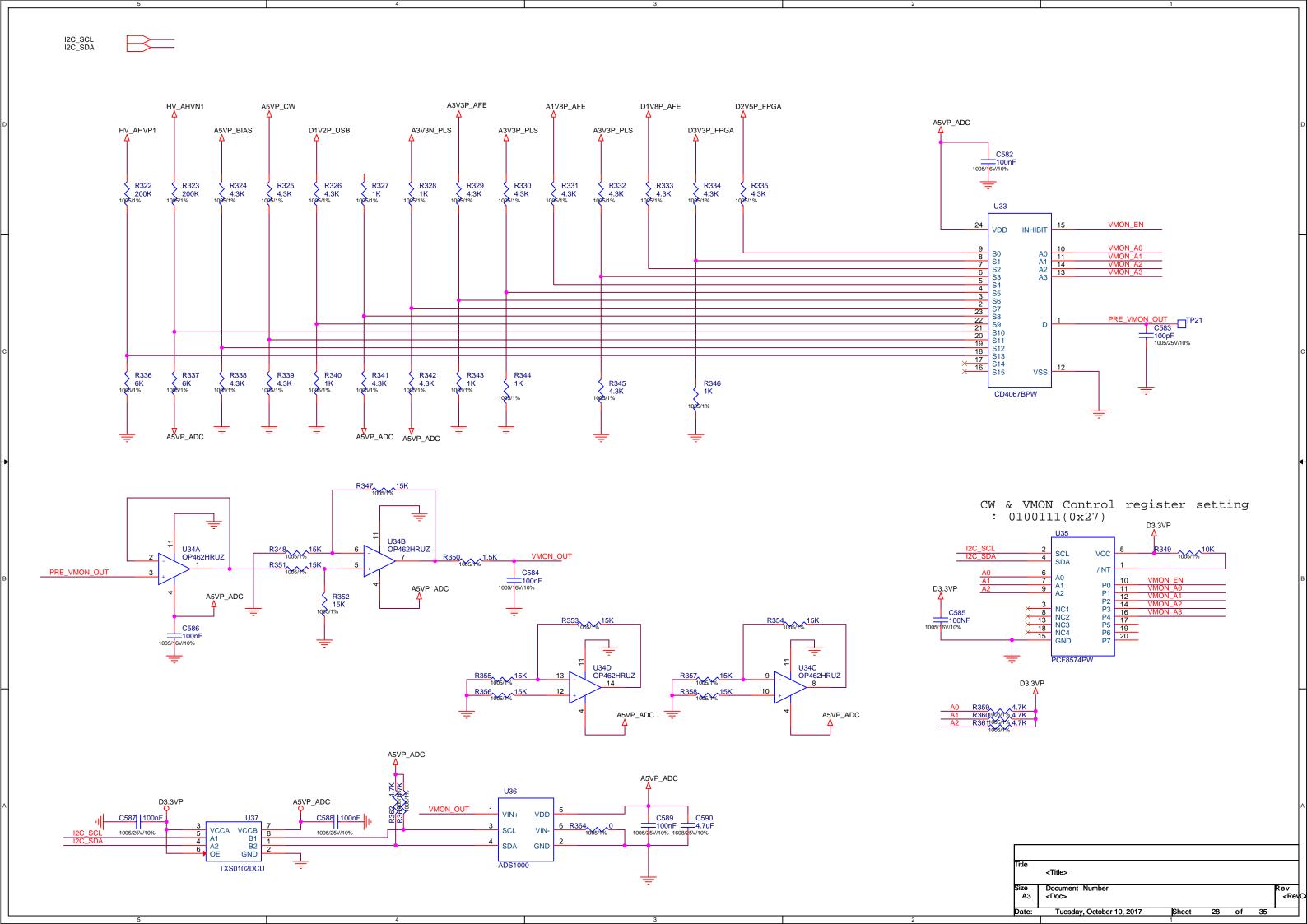


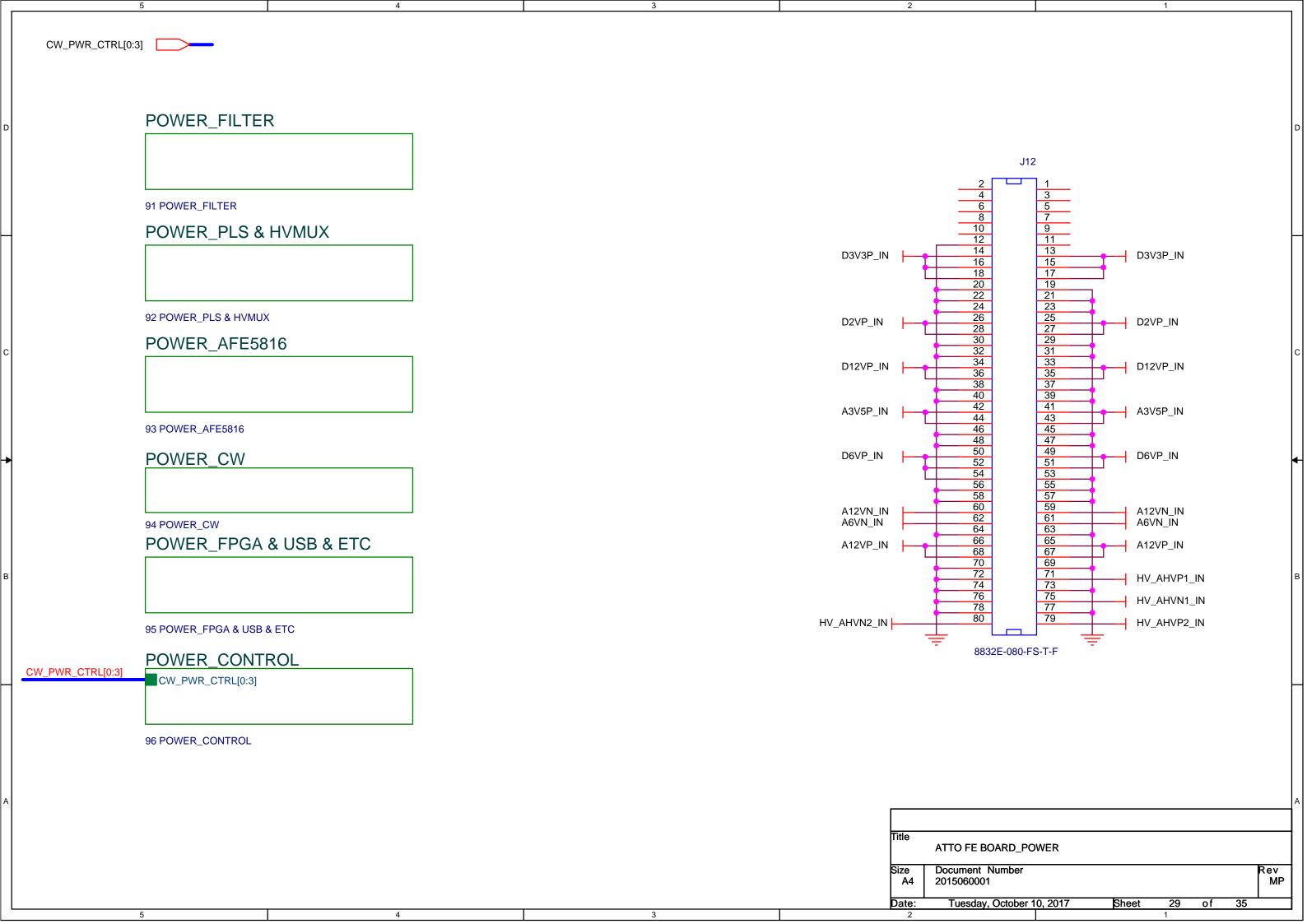


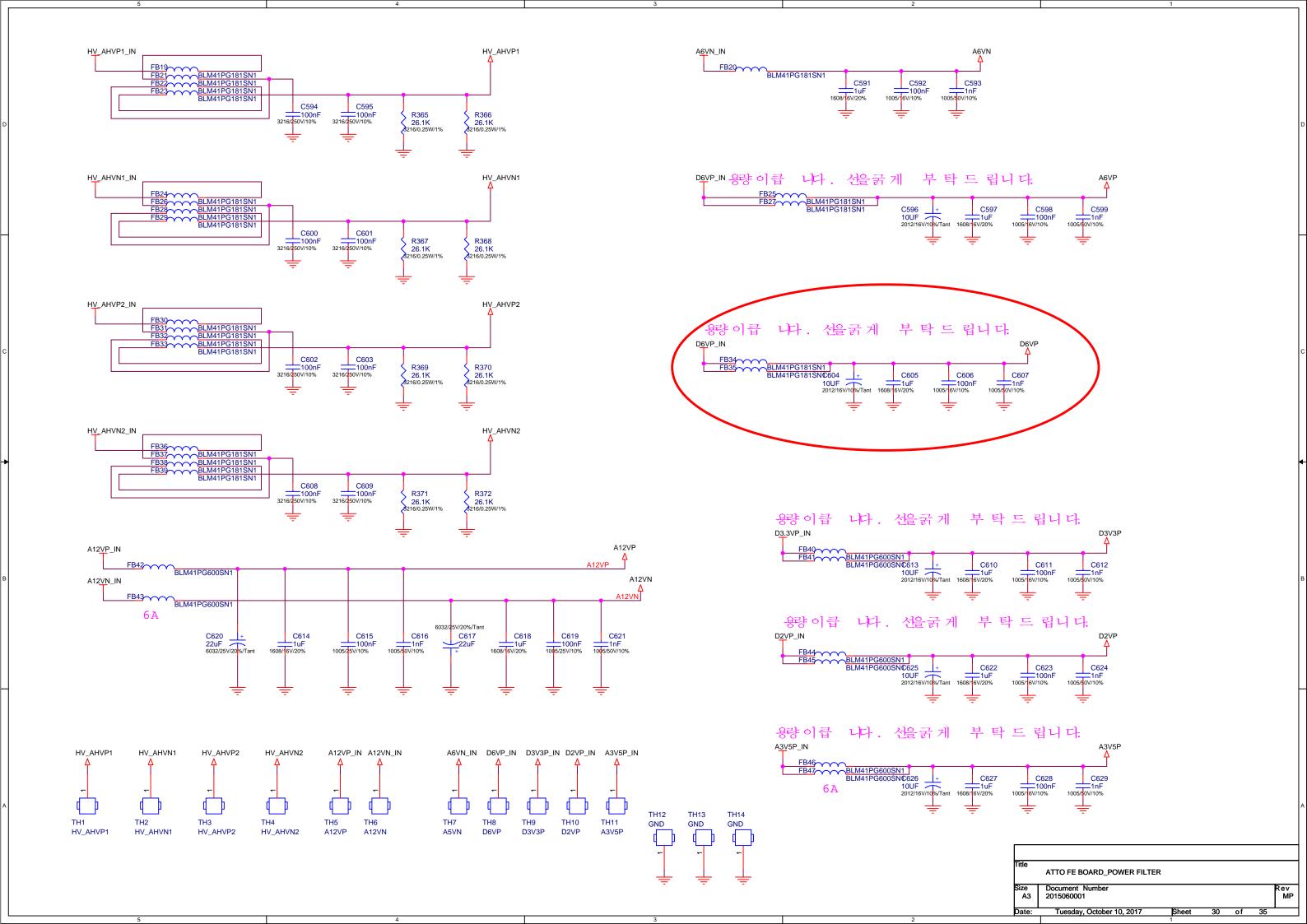


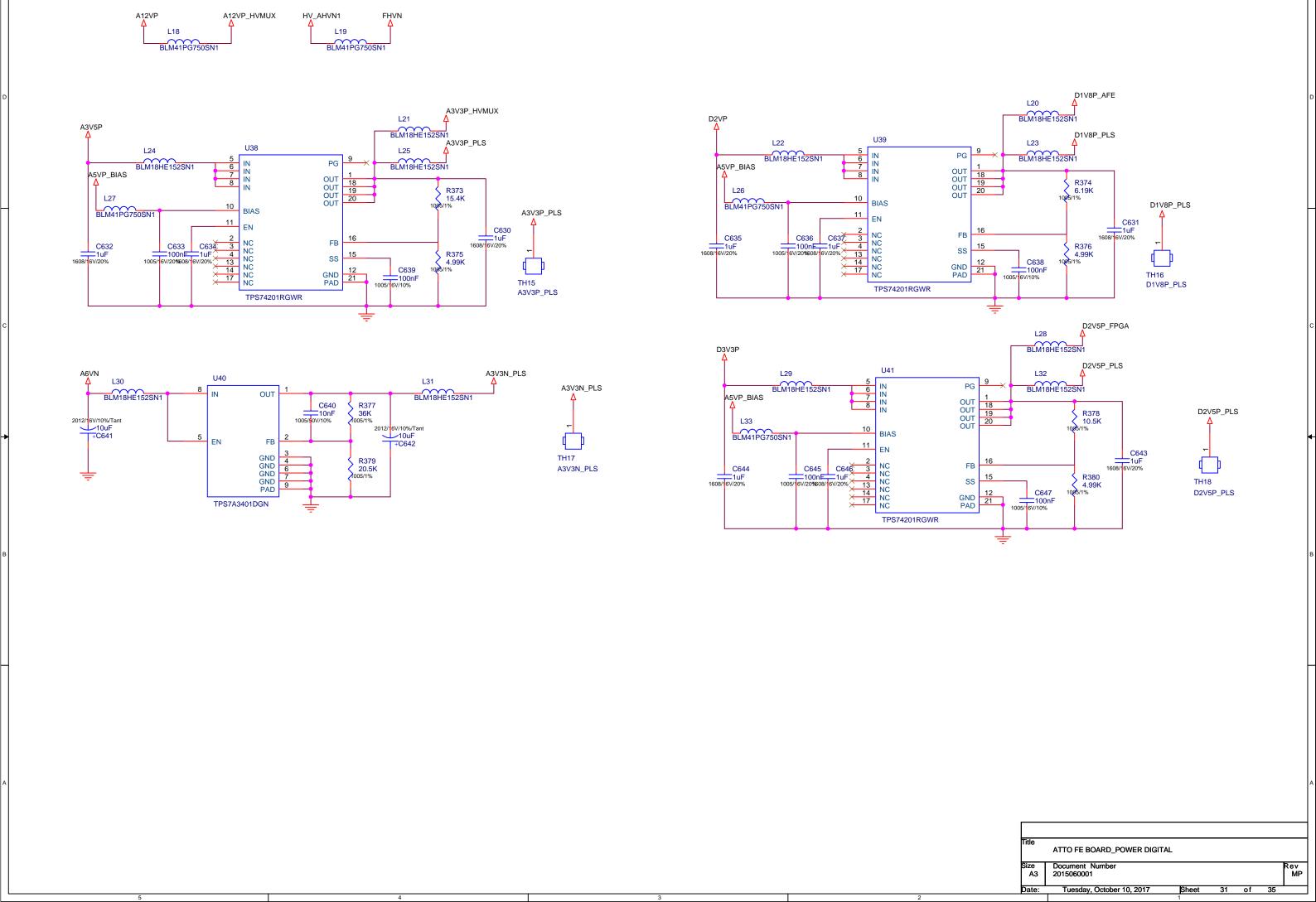


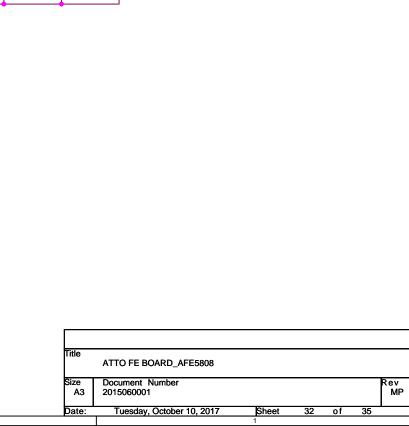












A1V9P_AFE

A1V9P_AFE

TH22 A1V9P_AFE

C652 =1uF TH21

GND

R382 6.98K

R384 4.99K

L37

C657 = 1nF 1005/16V/10%

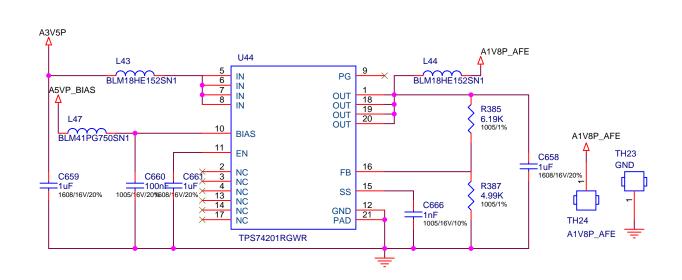
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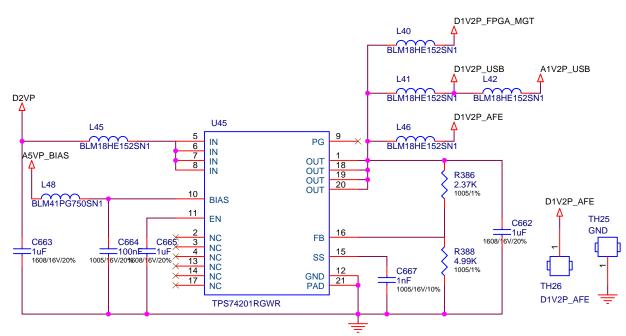
PG

OUT OUT OUT OUT

FB

SS 15 12 PAD 21 PAD





U43

BIAS

ΕN

TPS74201RGWR

C654 C655 3 100nE 1uF 4 1005/16V/20%08/6V/20% 13

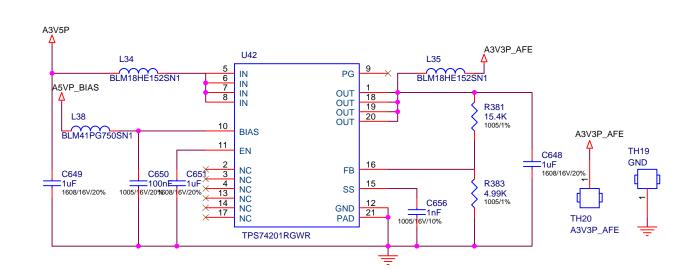
A3V5P

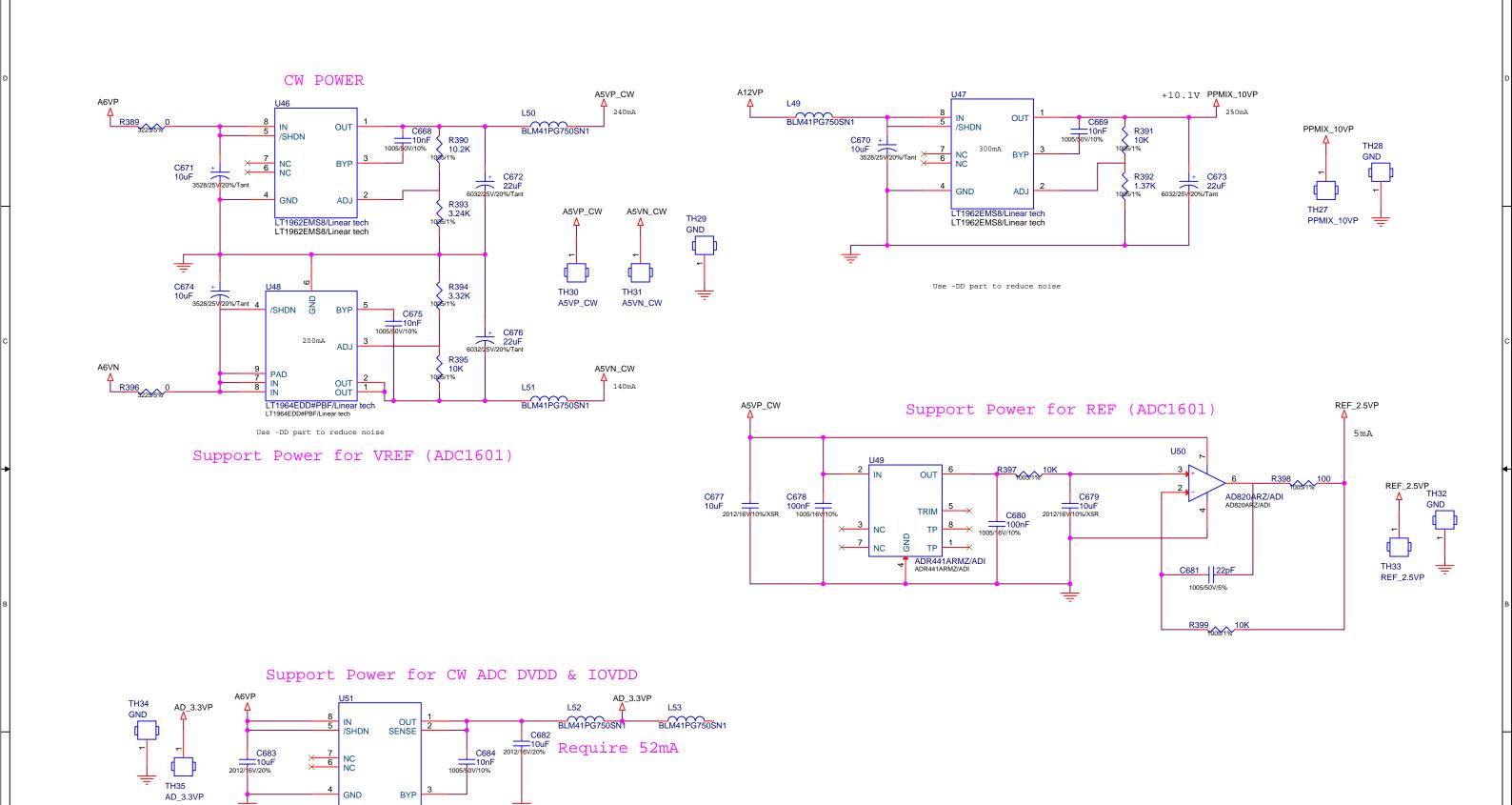
C653 1uF 1608/16V/20%

A5VP_BIAS

L39 BLM41PG750SN1

BLM18HE152SN1





3

LT1962EMS8-3.3 8-Lead Plastic MSOP (Linear tech.)

